

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Advanced trench cell design

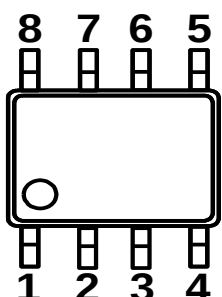
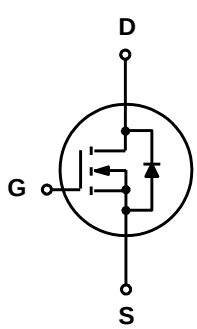
1.2 Applications

- ☒ Motor appliances
- ☒ High power inverter system

1.3 Quick reference

- ☒ $BV \geq 30\text{ V}$
- ☒ $P_{tot} \leq 2\text{ W}$
- ☒ $I_D \leq 8\text{ A}$
- ☒ $R_{DS(ON)} \leq 16.5\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- ☒ $R_{DS(ON)} \leq 20.5\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source(S)	 Top View SOP- 8L	
4	Gate(G)		
5,6,7,8	Drain(D)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_A = 25\text{ }^{\circ}\text{C}$	30	-	V
V_{GS}	Gate-Source Voltage	$T_A = 25\text{ }^{\circ}\text{C}$	-	± 20	V
I_D^*	Drain Current	$T_A = 25\text{ }^{\circ}\text{C}, V_{GS} = 10\text{ V}$	-	8	A
		$T_A = 100\text{ }^{\circ}\text{C}, V_{GS} = 10\text{ V}$	-	5.3	A
$I_{DM}^{*,**}$	Pulsed Drain Current	$T_A = 25\text{ }^{\circ}\text{C}, V_{GS} = 10\text{ V}$	-	32	A
P_{tot}	Total Power Dissipation	$T_A = 25\text{ }^{\circ}\text{C}$	-	2	W
T_{stg}	Storage Temperature		- 55	150	$^{\circ}\text{C}$
T_J	Junction Temperature		- 55	150	$^{\circ}\text{C}$
I_S	Diode Forward Current	$T_A = 25\text{ }^{\circ}\text{C}$	-	8	A
$R_{\theta JA}^*$	Thermal Resistance- Junction to Ambient		-	62.5	$^{\circ}\text{C} / \text{W}$

Notes :

- * Surface Mounted on 1 in² pad area, $t \leq 10\text{ sec}$
- ** Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

4. Marking Information

Product Name	Marking
KJ4404S	4404 YWWXXX YWWXXX: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ4404S	SOP8			3000	

Note: KUAJIEXIN defines " Green " as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics ($T_A=25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

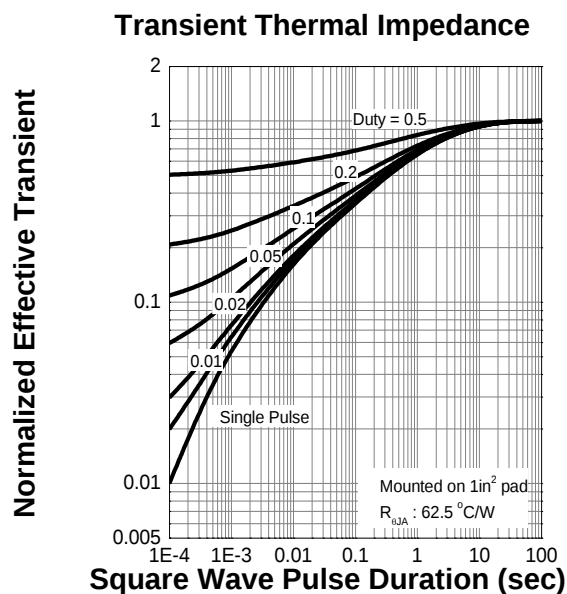
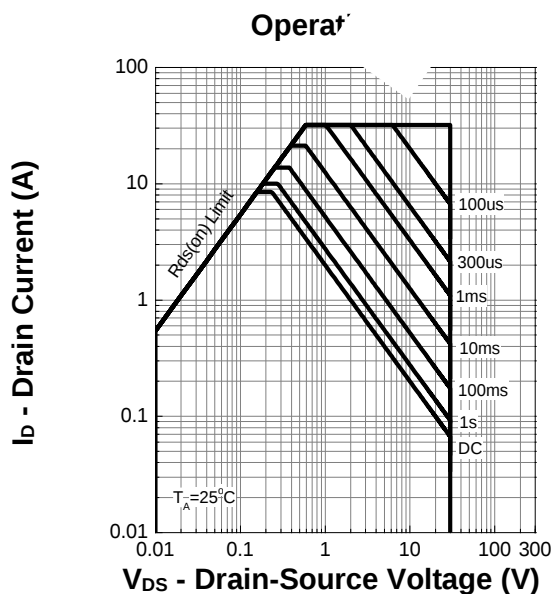
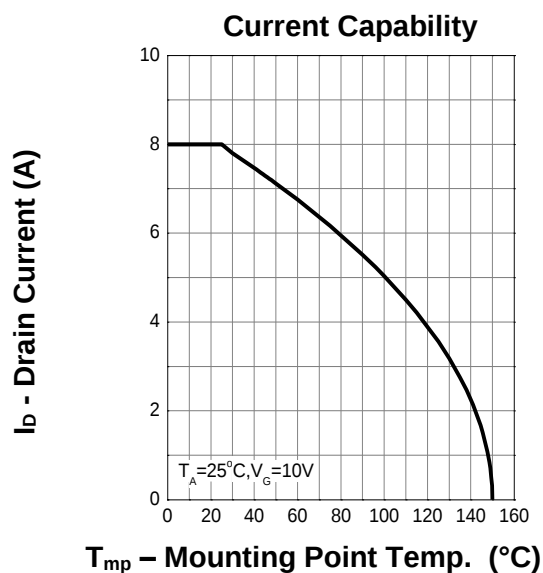
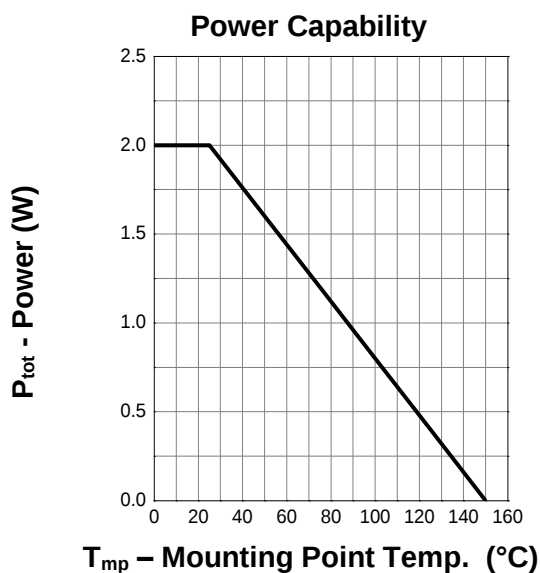
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _{DS} = 250 μA	30	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = 250 μA	0.5	-	1.0	V
I _{DSS}	Drain Leakage Current	V _{DS} = 24V, V _{GS} = 0 V	-	-	1	μA
		T _J = 85 °C	-	-	30	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} = 10 V, I _{DS} = 8 A	-	14.8	16.5	m Ω
		V _{GS} = 4.5V, I _{DS} = 5 A	-	16.5	20.5	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = 8 A, V _{GS} = 0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{DS} = 8 A, V _{GS} = 0 V di _{SD} /dt = 100 A/μs	-	14	-	nS
Q _{rr}	Reverse Recovery Charge		-	6.9	-	μC
Dyna10mic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 15 V Frequency = 1 MHz	-	630	-	pF
C _{oss}	Output Capacitance		-	72	-	
C _{rss}	Reverse Transfer Capacitance		-	67	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = 15 V, V _{GEN} = 10 V, R _G = 4.5 Ω, R _L = 1.87 Ω, I _{DS} = 8 A	-	4.6	-	nS
t _r	Turn-on Rise Time		-	30	-	
t _{d(off)}	Turn-off Delay Time		-	26	-	
t _f	Turn-off Fall Time		-	21	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{GS} = 10 V, V _{DS} = 15 V, I _{DS} = 8 A	-	21	-	nC
Q _{gs}	Gate-Source Charge		-	2	-	
Q _{gd}	Gate-Drain Charge		-	3.1	-	

Notes :

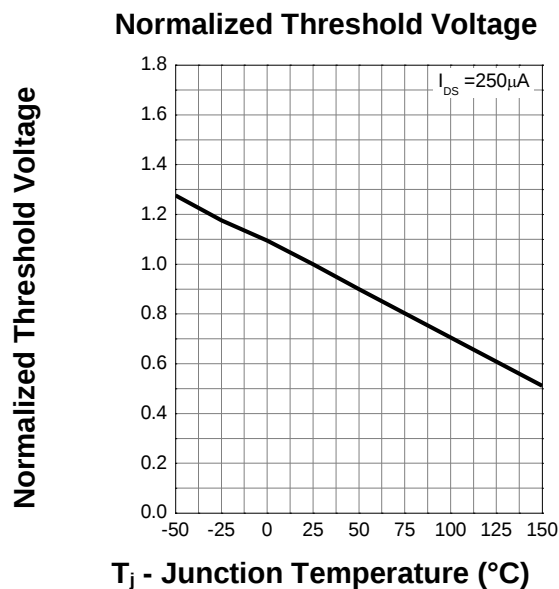
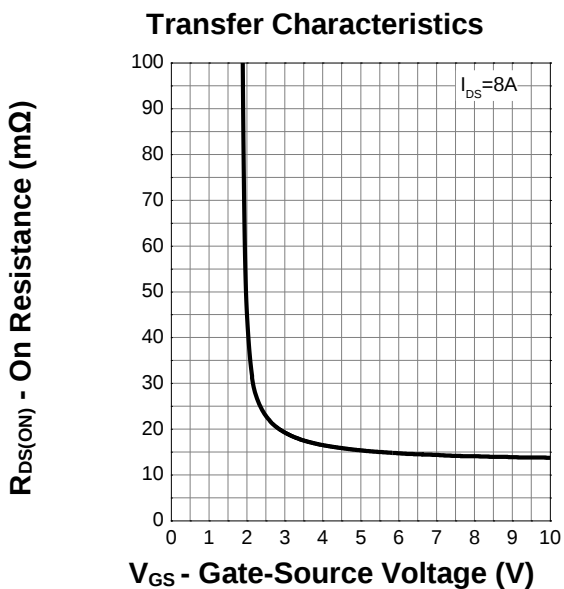
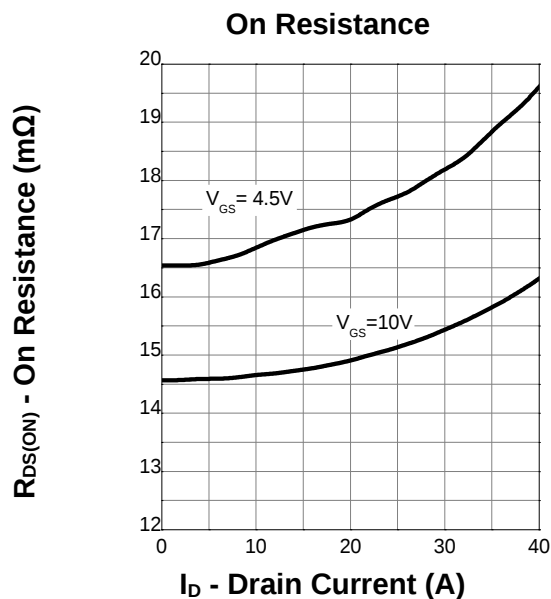
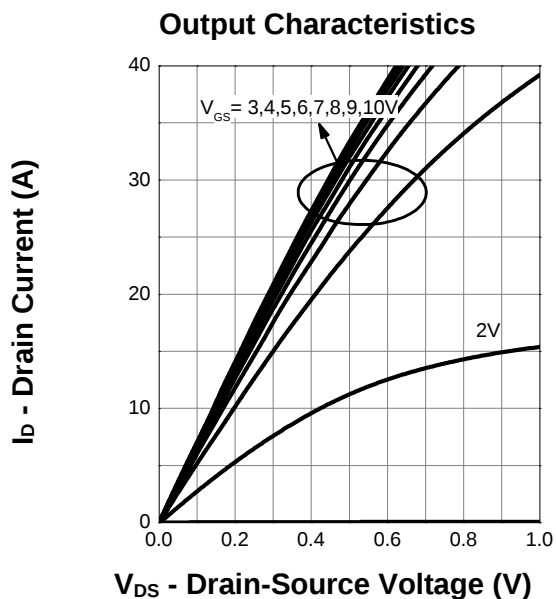
a : Pulse test ; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

b : Guaranteed by design, not subject to production testing

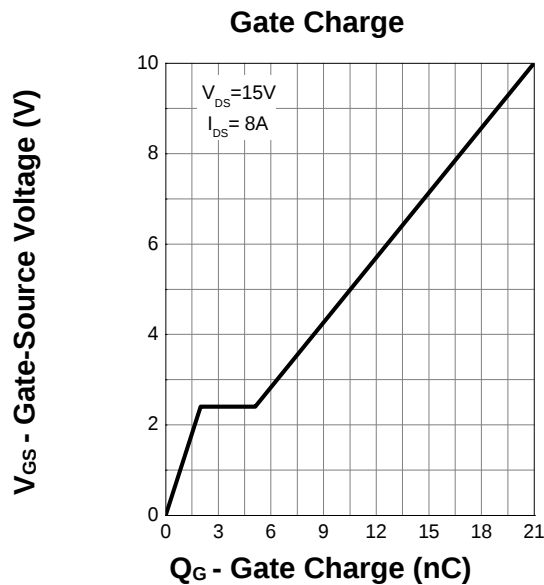
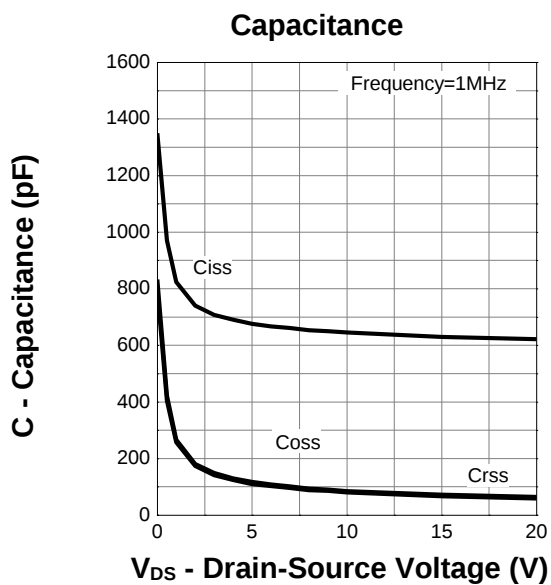
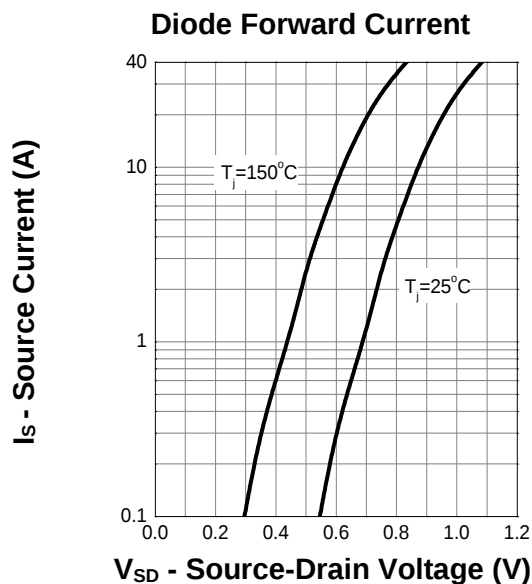
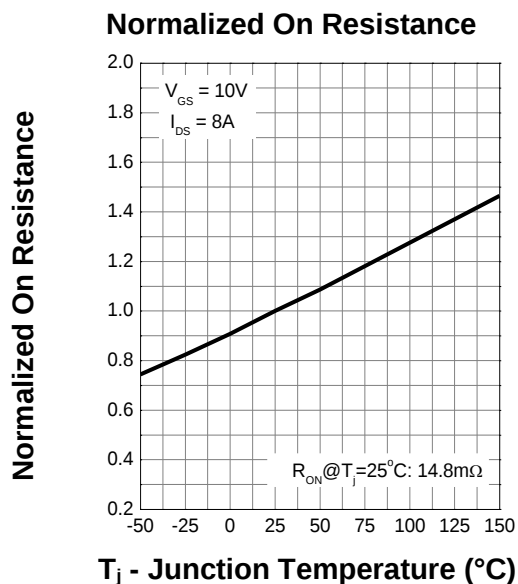
7. Typical Characteristics



7. Typical Characteristics (cont.)

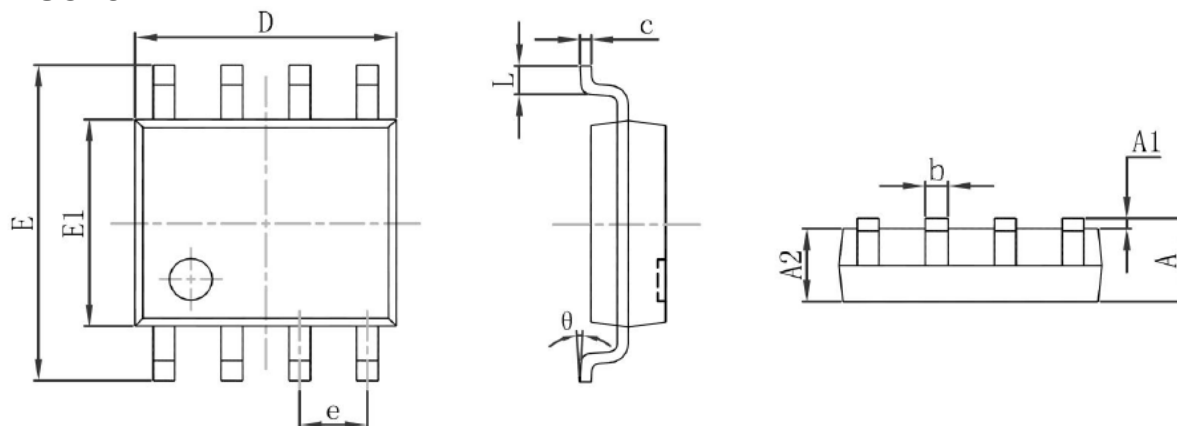


7. Typical Characteristics (cont.)



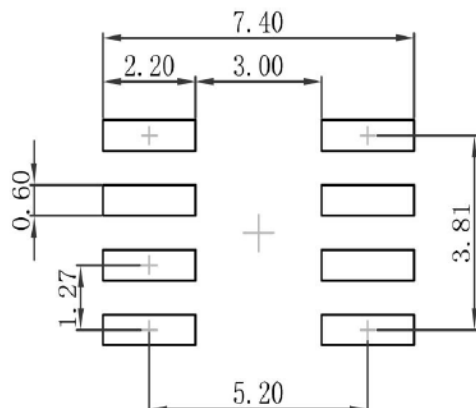
8. Package Dimensions

SOP8



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.350	1.750
A1	0.100	0.250
A2	1.350	1.550
b	0.300	0.510
c	0.170	0.250
D	4.800	5.000
E	5.800	6.200
E1	3.800	4.000
e	1.270 BSC	
L	0.400	1.270
θ	0°	8°

SOP-8 Suggested Pad Layout

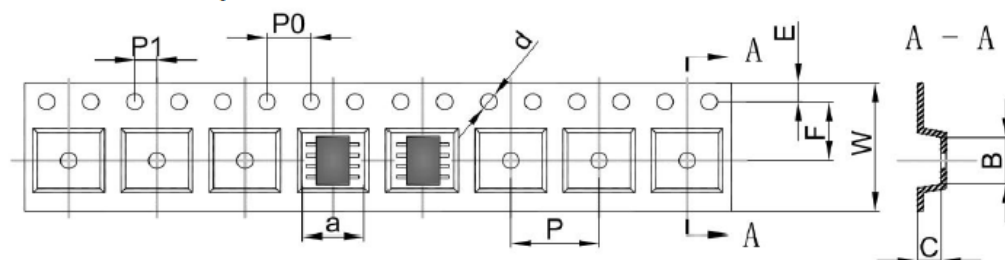


Note:

1. Controlling dimension: in millimeters
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference purposes only

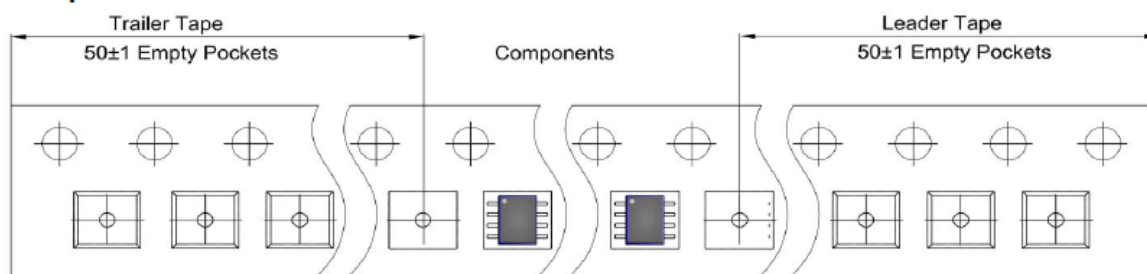
9. SOP-8 Tape and Reel

SOP-8 Embossed Carrier Tape

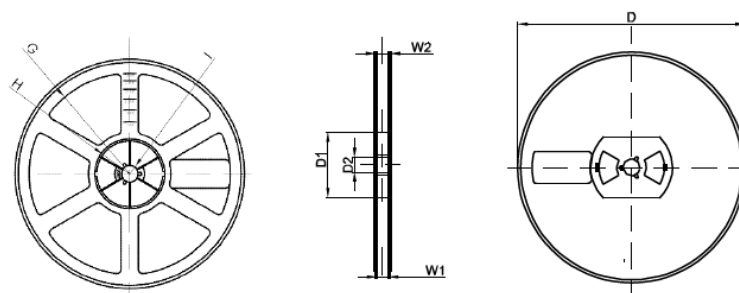


DIMENSIONS ARE IN MILLIMETER										
TYPE	A	B	C	d	E	F	P0	P	P1	W
SOP-8	6.40	5.40	2.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00
TOLERANCE	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1

SOP-8 Tape Leader and Trailer



SOP-8 Reel



DIMENSIONS ARE IN MILLIMETER								
REEL OPTION	D	D1	D2	G	H	I	W1	W2
13" DIA	Ø330.00	100.00	13.00	R151.00	R56.00	R6.50	12.40	17.60
TOLERANCE	±2	±1	±1	±1	±1	±1	±1	±1