

P-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Advanced trench cell design
- Low Thermal Resistance

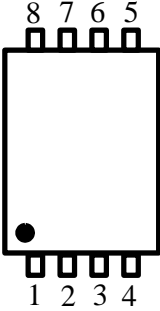
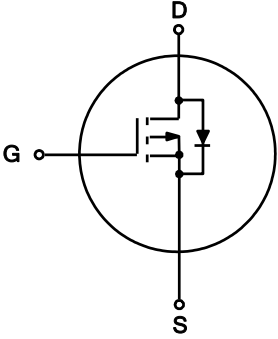
1.2 Applications

- Motor drivers
- DC - DC Converter

1.3 Quick reference

- $BV \geq -20\text{ V}$
- $R_{DS(ON)} \leq 7\text{ m}\Omega @ V_{GS} = -10\text{ V}$
- $P_{tot} \leq 20\text{ W}$
- $R_{DS(ON)} \leq 8\text{ m}\Omega @ V_{GS} = -4.5\text{ V}$
- $I_D \leq -80\text{ A}$
- $R_{DS(ON)} \leq 11\text{ m}\Omega @ V_{GS} = -2.5\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source	 <p style="text-align: center;">Top View PDFN5x6-8L</p>	
4	Gate		
5,6,7,8	Drain		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C = 25 °C	-20	-	V
V _{GS}	Gate-Source Voltage	T _C = 25 °C	-	± 12	V
I _D *	Drain Current	T _C = 25 °C, V _{GS} = -4.5 V	-	-80	A
I _{DM} ^{*,**,***}	Pulsed Source Current	T _C = 25 °C, V _{GS} = -4.5 V	-	-120	A
P _{tot} *	Total Power Dissipation	T _C = 25 °C	-	20	W
T _{stg}	Storage Temperature		- 55	150	°C
T _J	Junction Temperature		-	150	°C
I _S	Diode Forward Current	T _C = 25 °C	-	-80	A
R _{θJC} *	Thermal Resistance- Junction to Ambient		-	6	°C / W

Notes :

- * Surface Mounted on 1 in² pad area, t ≤ 10 sec
- ** Pulse width ≤ 10 μs, duty cycle ≤ 1 %
- *** Limited by bonding wire

4. Marking Information

Product Name	Marking
KJ09P02G	<div style="display: inline-block; background-color: black; color: white; padding: 2px;">09P02 YWWXXX</div> YWW: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ09P02G	PDFN5*6			5000	

Note: KUAJIEXIN defines " Green " as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)



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KJ09P02G

6. Electrical Characteristics (T_C = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA	-20	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = -250 μA	-0.3	-	-1.2	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} = -16V, V _{GS} = 0 V	-	-	-10	μA
		T _J = 85 °C	-	-	-30	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 12 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	Drain-Source On-State Resistance	V _{GS} = -10 V, I _D = -13A	-	6	7	mΩ
		V _{GS} = -4.5 V, I _D = -10 A	-	7	8	mΩ
		V _{GS} = -2.5 V, I _D = -6 A	-	10	11	mΩ
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = -2.6 A, V _{GS} = 0 V	-	-	-1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} = -10 A, dI _{SD} /dt = 100 A/μs	-	36	-	nS
Q _{rr}	Reverse Recovery Charge		-	20	-	nC
Dynamic Characteristics^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = -10 V Frequency = 1 MHz	-	6200	-	pF
C _{oss}	Output Capacitance		-	700	-	
C _{rss}	Reverse Transfer Capacitance		-	380	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = -10 V, V _{GEN} = -5 V, R _G = 3.3 Ω, I _D = -1 A	-	27	-	nS
t _r	Turn-on Rise Time		-	21	-	
t _{d(off)}	Turn-off Delay Time		-	240	-	
t _f	Turn-off Fall Time		-	110	-	
Gate Charge Characteristics^b						
Q _g	Total Gate Charge	V _{GS} = -4.5 V, V _{DS} = -10 V, I _{DS} = -10 A	-	60	-	nC
Q _{gs}	Gate-Source Charge		-	10	-	
Q _{gd}	Gate-Drain Charge		-	13.5	-	

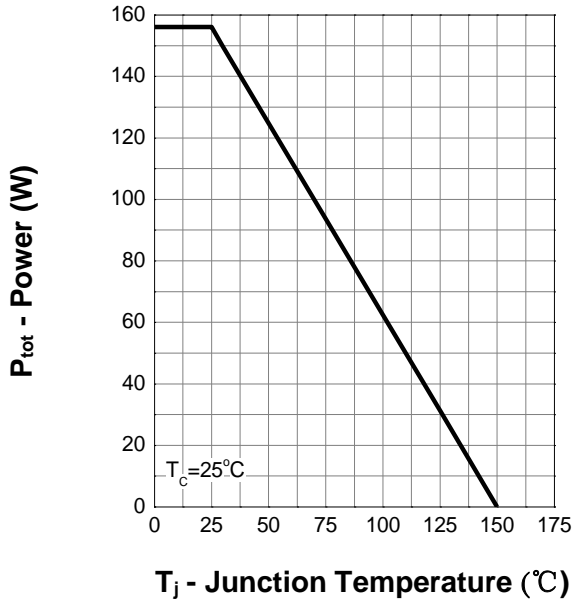
Notes :

- a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- b : Guaranteed by design, not subject to production testing

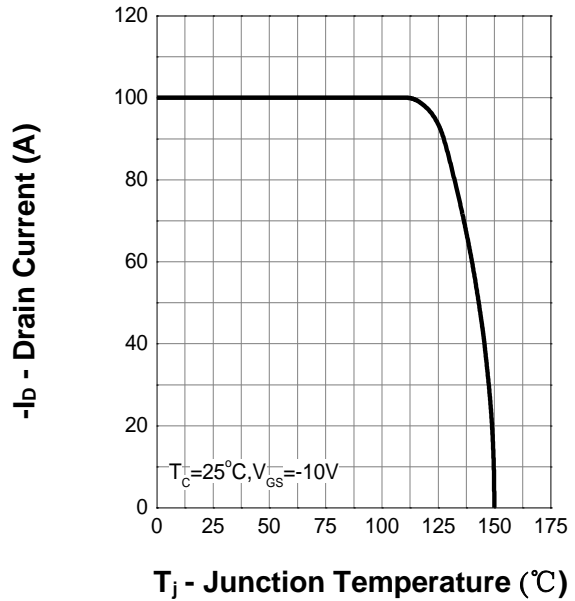


7. Typical Characteristics

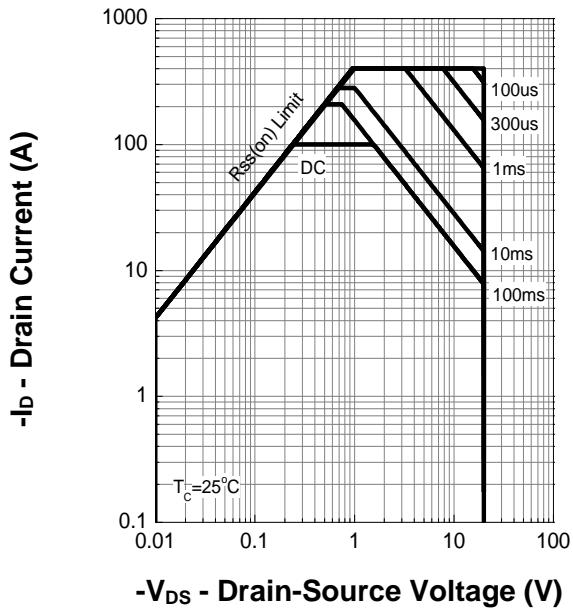
Power Capability



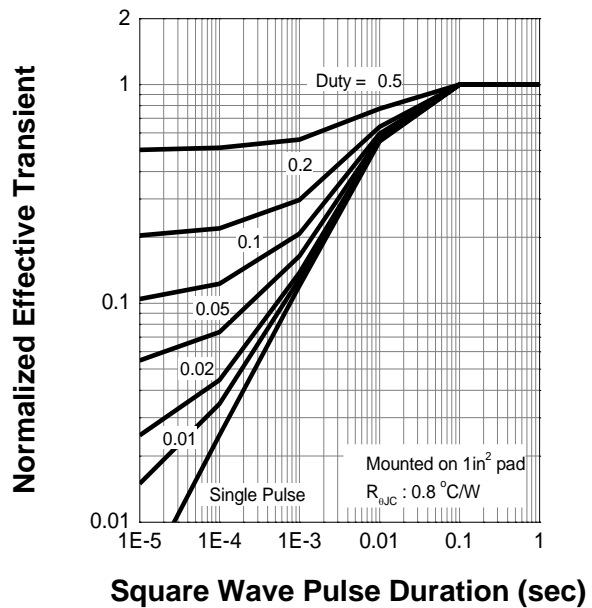
Current Capability



Safe Operatic. Area



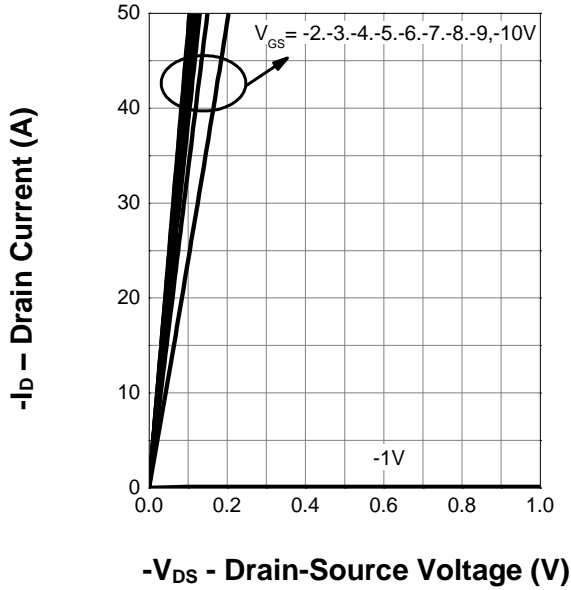
Thermal Transient Impedance



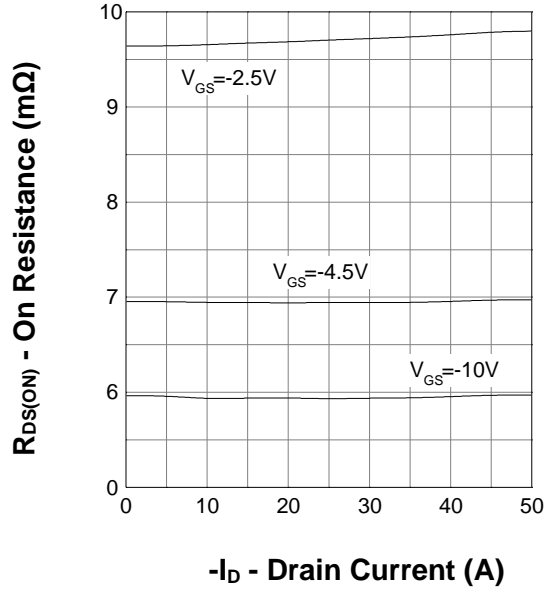


7. Typical Characteristics (cont.)

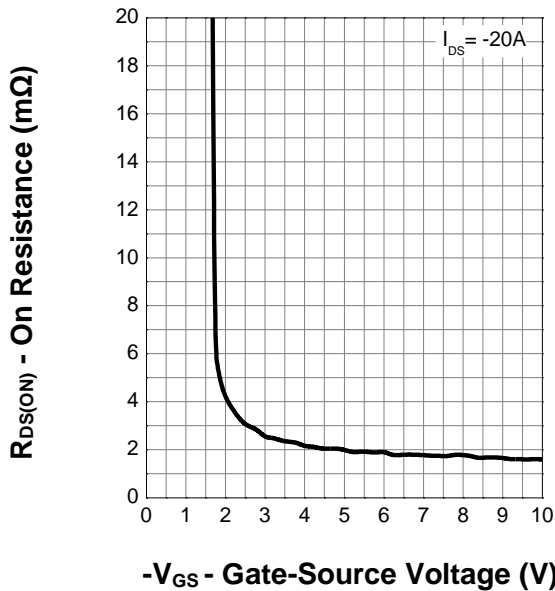
Output Characteristics



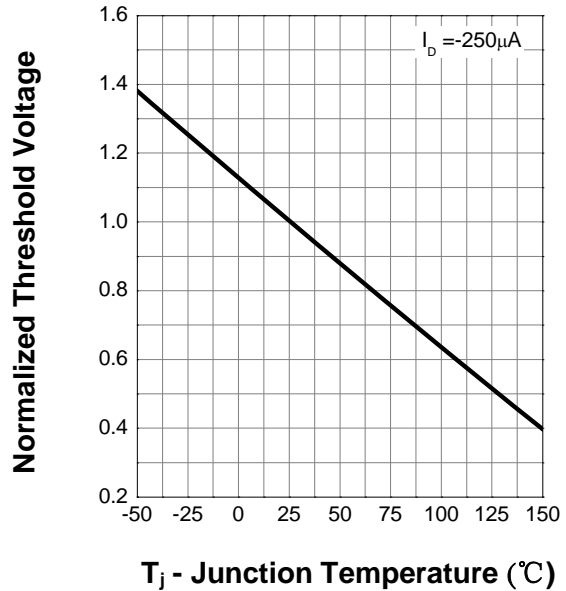
Drain-Source On Resistance



Transfer Characteristics



Gate Threshold Voltage



7. Typical Characteristics (cont.)

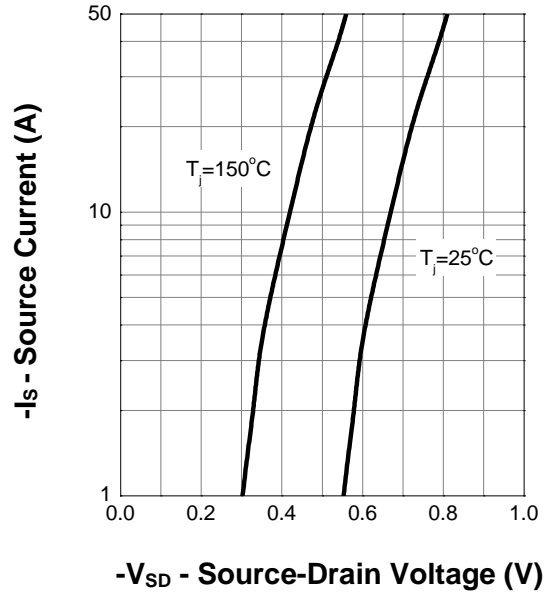


Drain-Source On Resistance

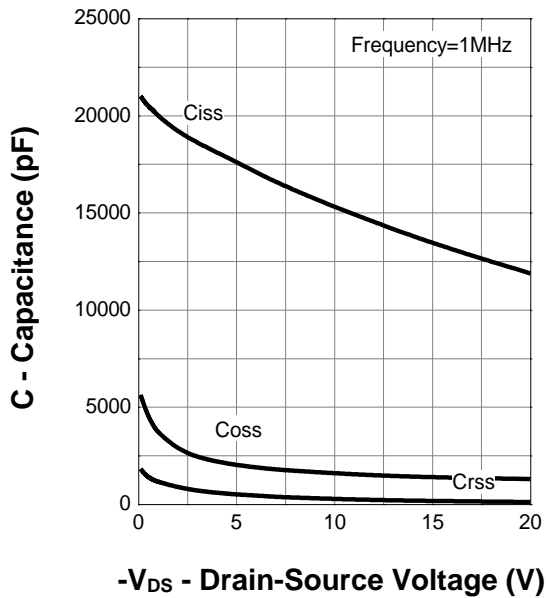
Normalized On Resistance

T_j - Junction Temperature ($^{\circ}\text{C}$)

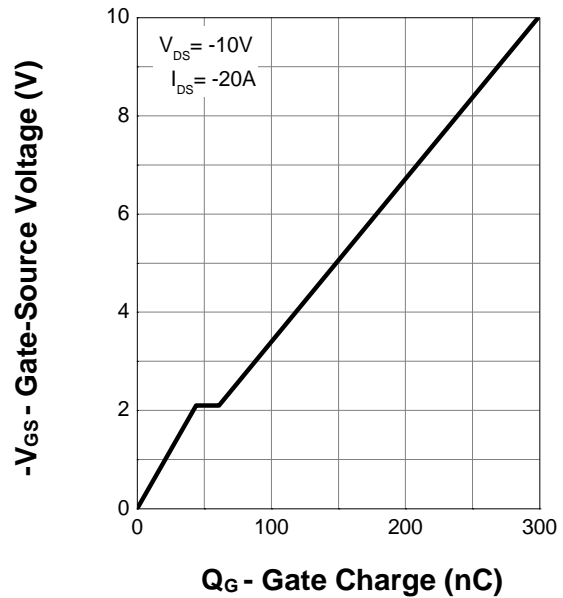
Body Diode Characteristics



Capacitance

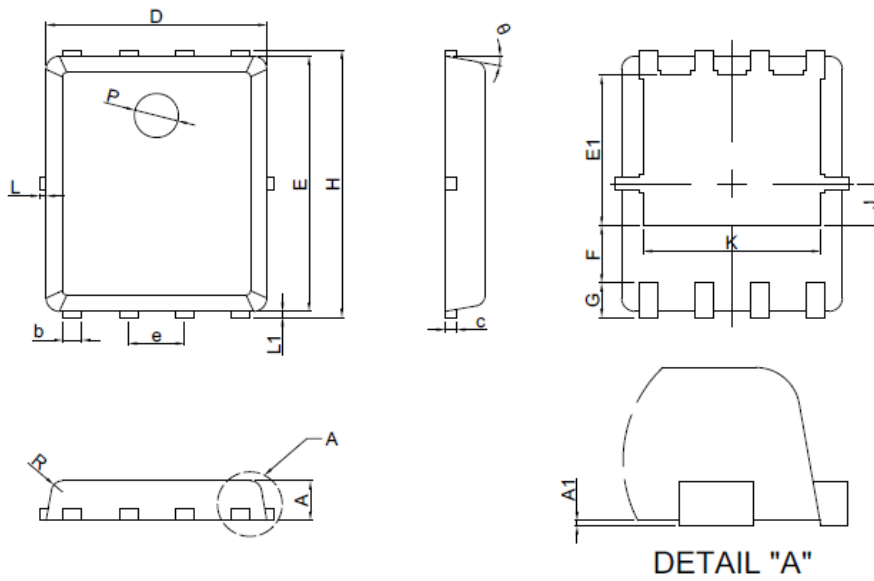


Gate Charge



8. Package Dimensions

PDFN5x6 - 8L Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	0.80	1.00
A1	0.00	0.05
b	0.35	0.49
c	0.254REF	
D	4.90	5.10
F	1.40REF	
E	5.70	5.90
e	1.27BSC	
H	5.95	6.20
L1	0.10	0.18
G	0.60REF	
K	4.00REF	
L	-	0.15
J	0.95BSC	
P	1.00REF	
E1	3.40REF	
theta	6°	14°
R	0.25REF	