

# **N-Channel Enhancement Mode MOSFET**

#### 1. Product Information

#### **Features**

Advanced Technology

Excellent R<sub>DS(ON)</sub>, Low gate charge, Hight EAS

#### **Applications**

Battery protection

Load switch

Uninterruptible power supply

#### **Quick reference**

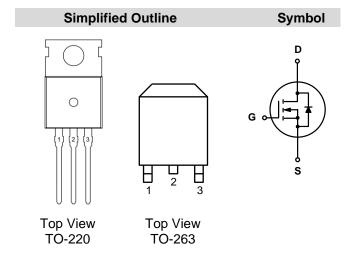
 $V_{\text{DS}} \geqq 68V$ 

 $I_{\text{D}} \leq 80 A$ 

 $R_{DS(ON)} \le 9.0 \text{m}\Omega$  @  $V_{GS} = 10V$  (Type:7.2 m $\Omega$ )

## **Pin Description**

Pin	Description
1	Gate(G)
2	Drain(D)
3	Source(S)



#### **Package Marking and Ordering Information**

Product Name	Package	Mar	king	Reel Size	Tape width	Quantity
KJ80N07C	TO-220	80N07	YWWXXX:			1000
KJ80N07D	TO-263	YWWXXX	Date Code			800

### 2. Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	68	V
VGS	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	80	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	52	Α
IDM	Pulsed Drain Current <sup>2</sup>	320	Α
EAS	Single Pulse Avalanche Energy <sup>3</sup>	110	mJ
IAS	Avalanche Current	22	Α
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	103	W
TSTG	Storage Temperature Range	-55 to 150	$\circ$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$
R <sub>θ</sub> JA	Thermal Resistance Junction-ambient <sup>1</sup>	63	°C/W
R₀JC	Thermal Resistance Junction-Case <sup>1</sup>	1.46	°C/W



### 3. Electrical Characteristics (TJ=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	68	72		V
△BVDSS/△TJ	BVDSS Temperature Coefficient	Reference to 25℃ , I <sub>D</sub> =1mA		0.023		V/°C
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =10A		7.2	9.0	mΩ
VGS(th)	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2.0	3.0	4.0	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	VGS-VDS , ID -230UA		-4.2		mV/℃
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =68V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	uA
1033	Diain-Source Leakage Guirein	$V_{DS}$ =68 $V$ , $V_{GS}$ =0 $V$ , $T_{J}$ =55 $^{\circ}$ C			5	uA
IGSS	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA
Qg	Total Gate Charge (4.5V)			35		
Qgs	Gate-Source Charge	VDS =30V, ID =30A, VGS =10V		11		nC
Qgd	Gate-Drain Charge	100		9		
Td(on)	Turn-On Delay Time			15		
Tr	Rise Time	VDS =30V,ID =30A,		90		
Td(off)	Turn-Off Delay Time	RGEN =3Ω, V GS =10V		45		ns
Tf	Fall Time			30		
Ciss	Input Capacitance			400		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		267		pF
Crss	Reverse Transfer Capacitance			250		
IS	Continuous Source Current <sup>1,5</sup>	V V 0V 5 0 1			80	Α
ISM	Pulsed Source Current <sup>2,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			320	Α
VSD	Diode Forward Voltage <sup>2</sup>	V GS =0V, I S =80A			1.2	V
trr	Reverse Recovery Time	T J =25℃		78		nS
Qrr	Reverse Recovery Charge	I F =20A,dI/dt=100A/μs		51		nC

#### Note:

- 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- $\ensuremath{\mathsf{2}}_{\times}$  The data tested by pulsed , pulse width .The EAS data shows Max. rating .
- 3. The test cond  $\leq$  300us duty cycle  $\leq$  2%, duty cycle ition is TJ =25  $^{\circ}$ C, VDD =35V, VG =10V, R G =25 $\Omega$ , L=0.5mH, IAS =21A
- 4. The power dissipation is limited by 175  $\!\!\!^{\,\circ}\!\!\!^{\,\circ}$  junction temperature
- $5\sqrt{100}$  The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

www.kjx-tech.cn 2 Version 0.1



# **4.Typical Characteristics**

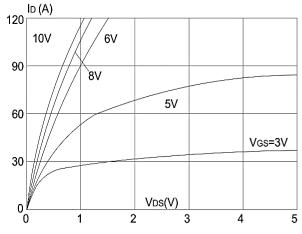


Figure1: Output Characteristics

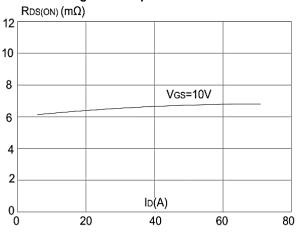


Figure 3:On-resistance vs. Drain Current

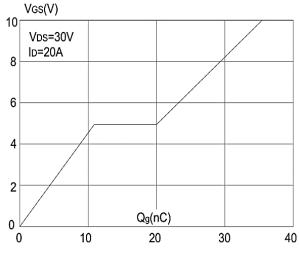


Figure 5: Gate Charge Characteristics

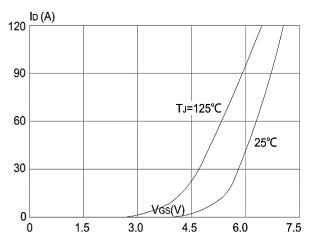


Figure 2: Typical Transfer Characteristics

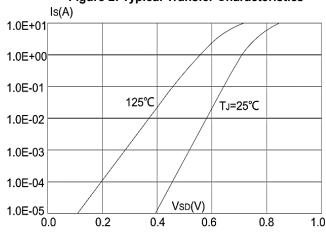
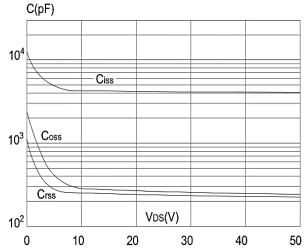


Figure 4: Body Diode Characteristics



**Figure 6: Capacitance Characteristics** 

www.kjx-tech.cn 3 Version 0.1



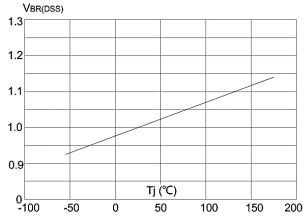


Figure 7: Normalized Breakdown Voltage vs Junction Temperature

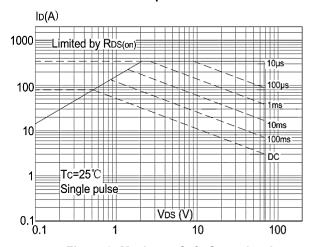


Figure 9: Maximum Safe Operating Area

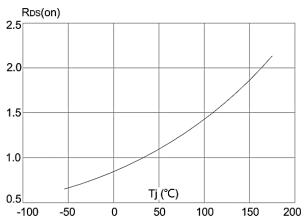


Figure 8: Normalized on Resistance vs.

Junction Temperature

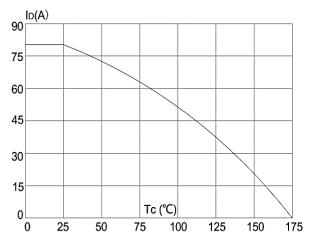


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

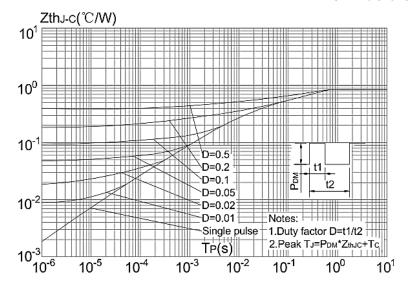
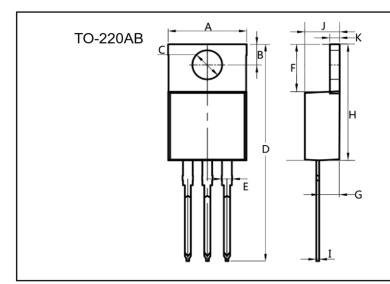


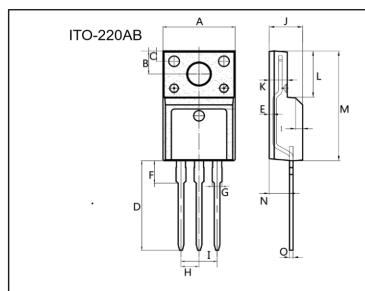
Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambien



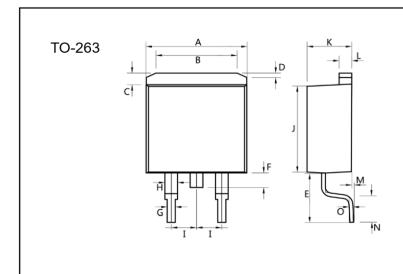
# **5.Package Mechanical Data**



Dim.	Min.	Max.
Α	10.0	10.4
В	2.5	3.0
С	3.5	4.0
D	28.0	30.0
E	1.1	1.5
F	6.2	6.6
G	2.9	3.3
Н	15.0	16.0
I	0.35	0.45
J	4.3	4.7
K	1.2	1.4
All Dimensions in millimeter		



Dim.	Min.	Max.
Α	9.9	10.3
В	2.9	3.5
С	1.15	1.45
D	12.75	13.25
E	0.55	0.75
F	3.1	3.5
G	1.25	1.45
Н	Typ 2.54	
I	Typ 5.08	
J	4.55	4.75
K	2.4	2. 7
L	6.35	6.75
М	15.0	16.0
N	2.75	3.15
0	0.45	0.60
All Dimensions in millimeter		



DIM.	IVIIN.	iviax.
Α	10.0	10. 5
В	7.25	7.75
С	1.3	1.5
D	0.55	0.75
E	5.0	6.0
F	1.4	1.6
G	0.75	0.95
Н	1.15	1.35
	Typ 2.54	
1	Typ	
J	8.4	8.6
J	8.4	8.6
J K	8.4 4.4	8.6 4.6
J K L	8.4 4.4 1.25	8.6 4.6 1.45
J K L	8.4 4.4 1.25 0.02	8.6 4.6 1.45 0.1
J K L M N	8.4 4.4 1.25 0.02 2.4	8.6 4.6 1.45 0.1 2.8 0.45