

P-Channel Enhancement Mode MOSFET

1. Product Information

Features

- Surface mount package
- High power and current handing capability
- Lead free product is acquired

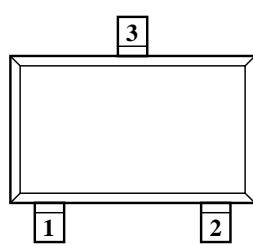
Pin Description

Pin	Description
1	Gate(G)
2	Source(S)
3	Drain(D)

Applications

- PWM applications
- Load switch
- Power management
- Halogen-free

Simplified Outline

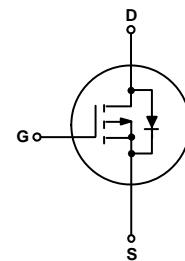


SOT-23

Quick reference

- $V_{DS} \geq -20V$
- $I_D \leq -6.0A$
- $R_{DS(ON)} \leq 30m\Omega$ @ $V_{GS} = -4.5V$ (Type: $25m\Omega$)
- $R_{DS(ON)} \leq 45m\Omega$ @ $V_{GS} = -2.5V$ (Type: $35m\Omega$)

Symbol



Package Marking and Ordering Information

Product Name	Package	Marking	Reel Size	Tape width	Quantity
KJ2333S	SOT-23	2333	-	-	3000

2. Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current -Continuous	I_D	-6	A
Drain Current -Pulsed ^(Note 1)	I_{DM}	-20	A
Maximum Power Dissipation	P_D	1.8	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	69	°C/W
---	-----------------	----	------

3. Electrical Characteristics (TJ=25°C, unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	V_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-20	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-12V, V_{GS}=0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.4	-0.65	-1.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-4.5V, I_D=-6A$	-	25	30	$m\Omega$
		$V_{GS}=-2.5V, I_D=-5A$	-	35	45	
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-6A$		17	-	S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C_{iss}	$V_{DS}=-6V, V_{GS}=0V, F=1.0MHz$	-	1100	-	PF
Output Capacitance	C_{oss}		-	390	-	PF
Reverse Transfer Capacitance	C_{rss}		-	300	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-6V, I_D=-1A, R_L=6\Omega, V_{GEN}=-4.5V, R_g=6\Omega$	-	25	-	nS
Turn-on Rise Time	t_r		-	45	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	72	-	nS
Turn-Off Fall Time	t_f		-	60	-	nS
Total Gate Charge	Q_g	$V_{DS}=-6V, I_D=-6A, V_{GS}=-4.5V$	-	11.5	-	nC
Gate-Source Charge	Q_{gs}		-	1.5	-	nC
Gate-Drain Charge	Q_{gd}		-	3.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V_{SD}	$V_{GS}=0V, I_S=-1.0A$	-	-	-1.2	V
Diode Forward Current ^(Note 2)	I_S		-	-	-6	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

4.Typical Characteristics

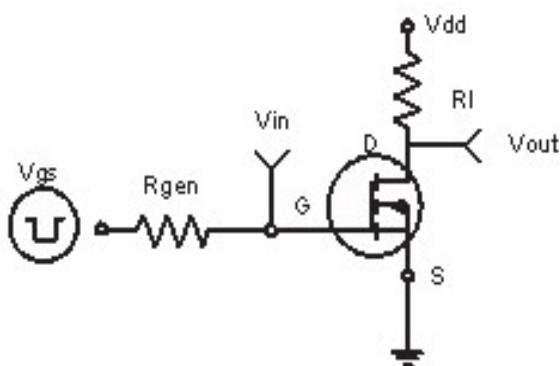


Figure 1:Switching Test Circuit

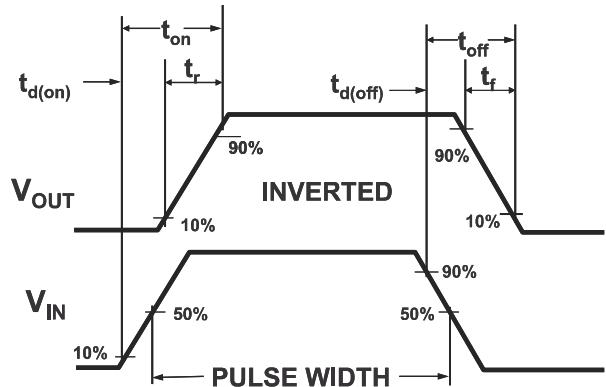
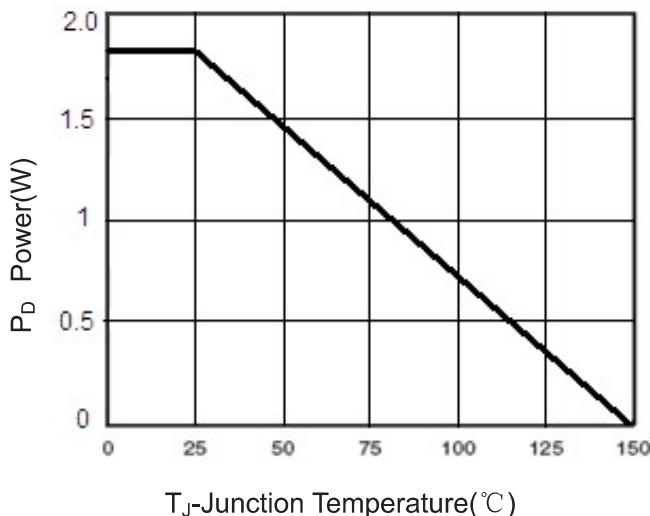
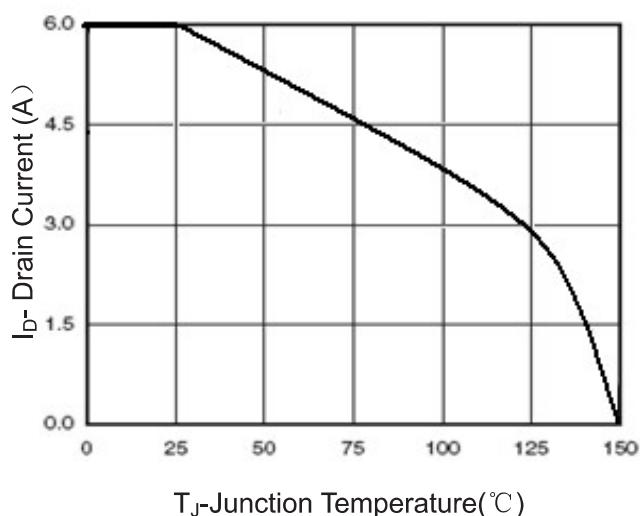


Figure 2:Switching Waveforms



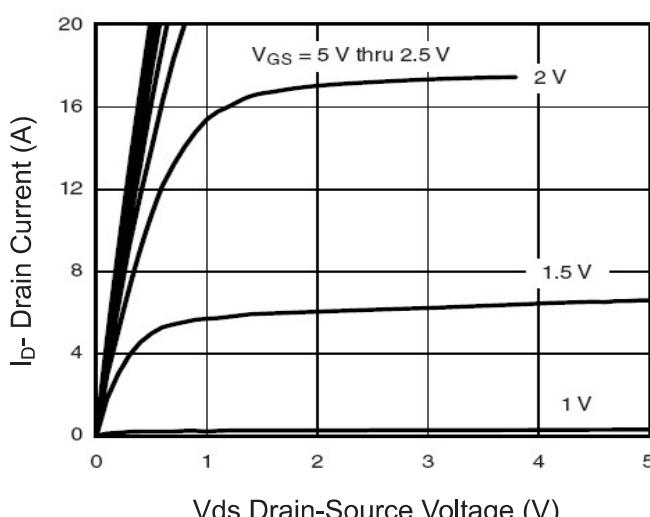
T_J-Junction Temperature(°C)

Figure 3 Power Dissipation



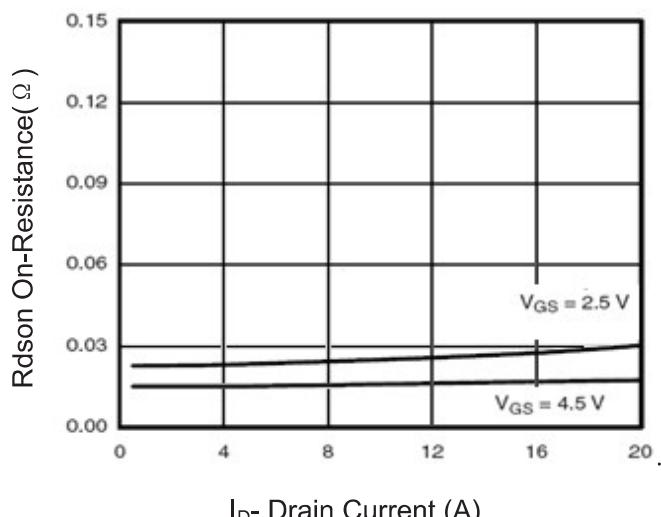
T_J-Junction Temperature(°C)

Figure 4 Drain Current



V_{ds} Drain-Source Voltage (V)

Figure 5 Output Characteristics



I_D- Drain Current (A)

Figure 6 Drain-Source On-Resistance

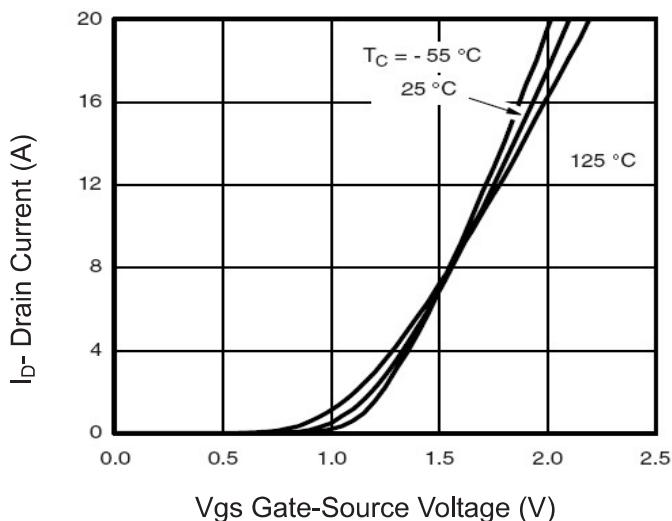


Figure 7 Transfer Characteristics

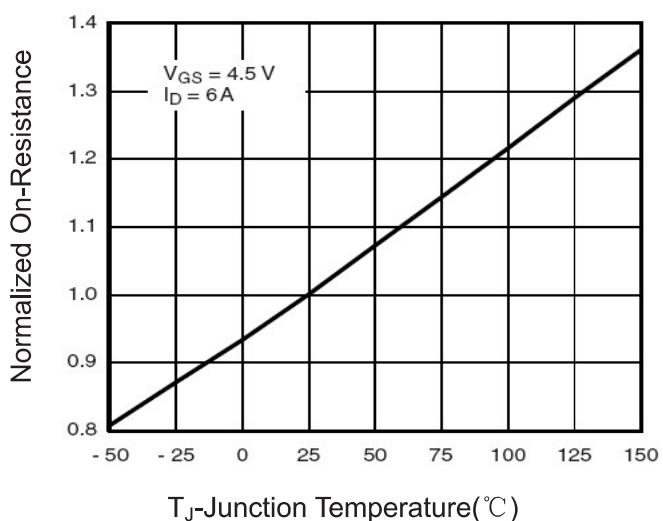


Figure 8 Drain-Source On-Resistance

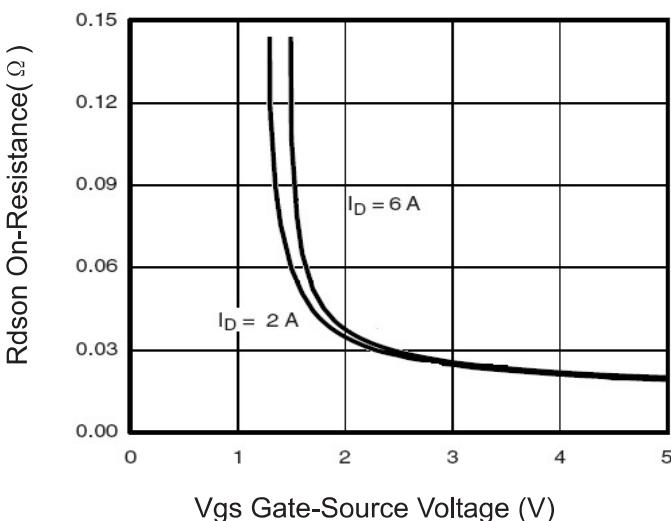
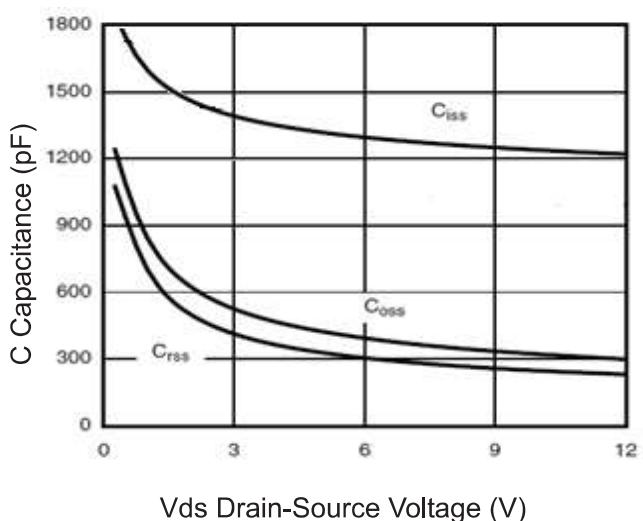
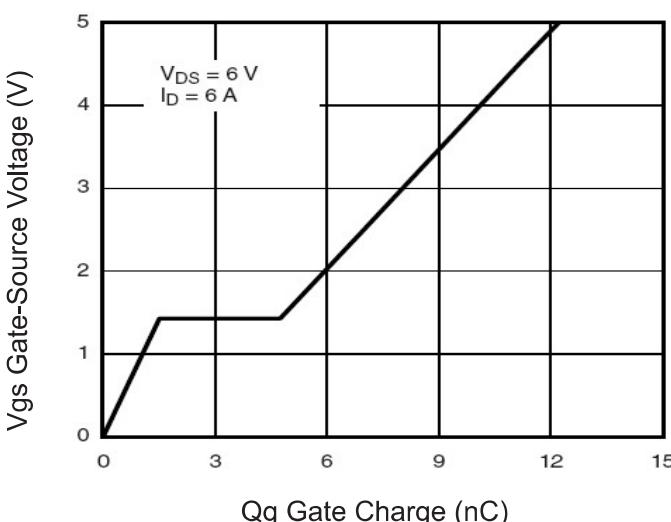
Figure 9 $R_{DS(on)}$ vs V_{GS} Figure 10 Capacitance vs V_{DS} 

Figure 11 Gate Charge

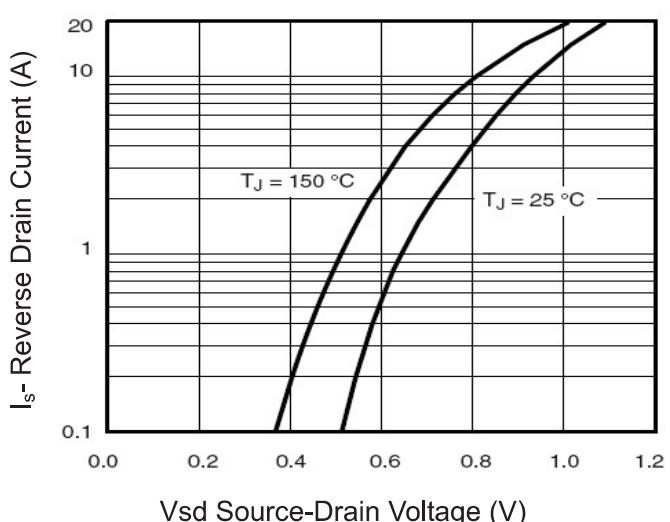
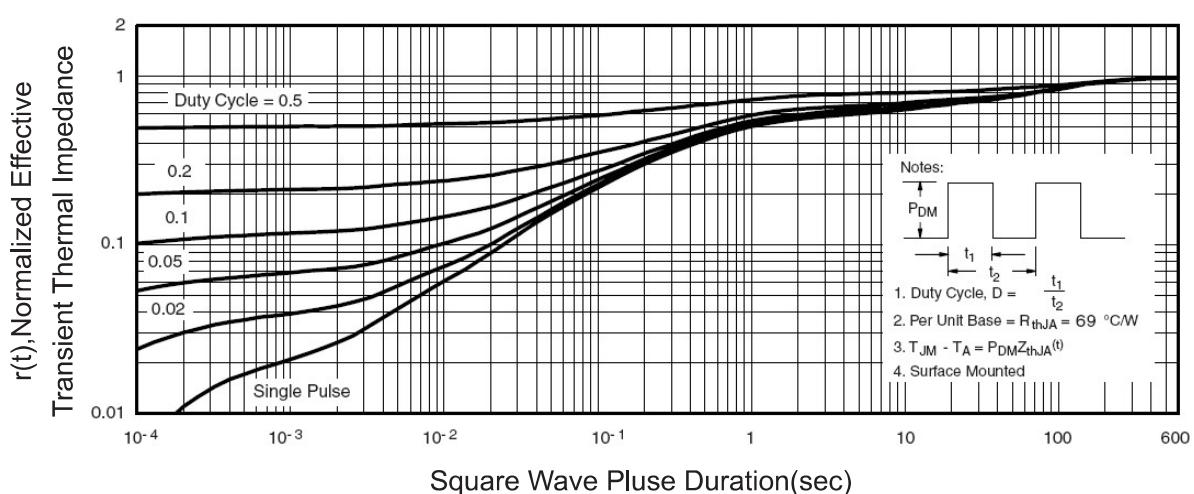
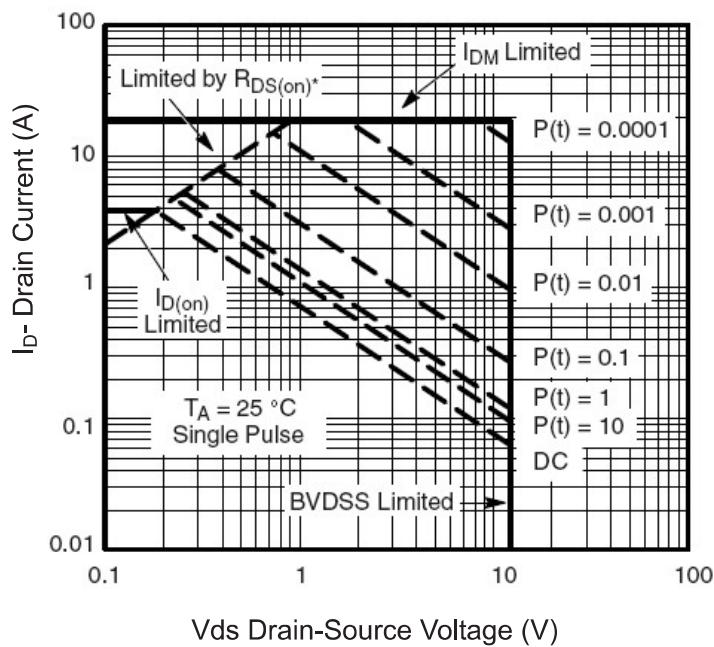
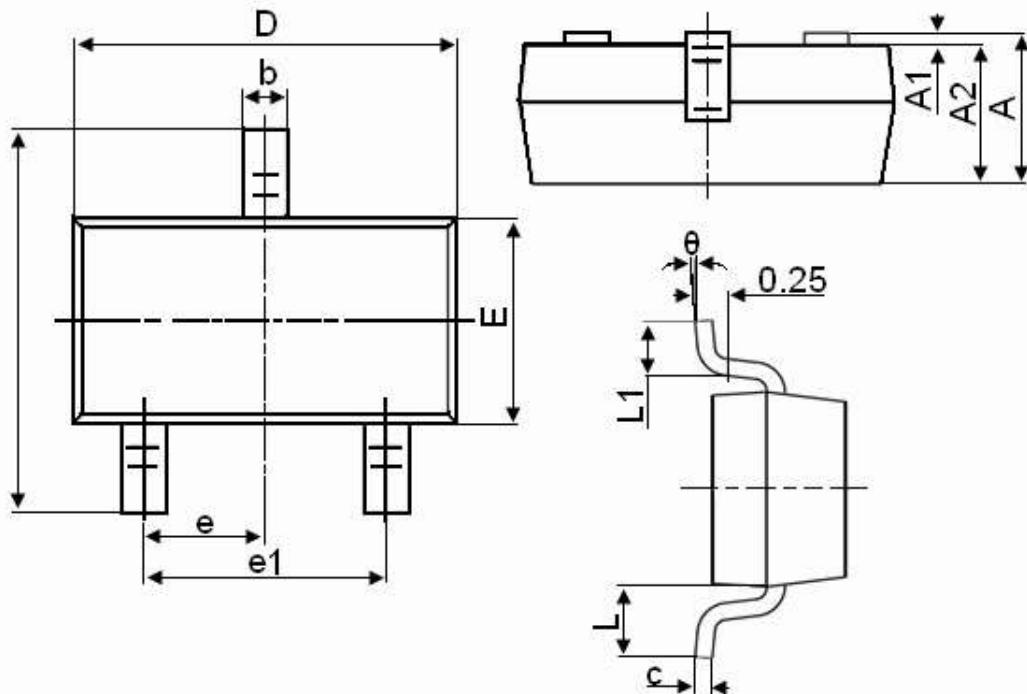


Figure 12 Source-Drain Diode Forward



5.Package Mechanical Data

SOT-23



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

Notes

1. All dimensions are in millimeters.
2. Tolerance $\pm 0.10\text{mm}$ (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.