

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Split Gate Trench
- Lead free product is acquired
- Excellent $R_{DS(ON)}$ and Low Gate Charge

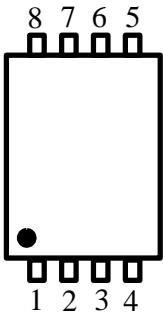
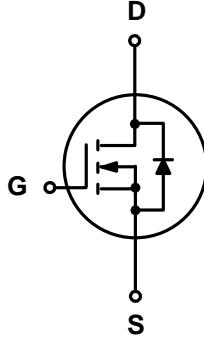
1.2 Applications

- PWM applications
- Power management
- Load Switch

1.3 Quick reference

- $BV \geq 30 \text{ V}$
- $P_{tot} \leq 59 \text{ W}$
- $I_D \leq 180 \text{ A}$
- $R_{DS(ON)} \leq 1.1 \text{ m}\Omega @ V_{GS} = 10 \text{ V}$
- $R_{DS(ON)} \leq 1.7 \text{ m}\Omega @ V_{GS} = 4.5 \text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source		
4	Gate		
5,6,7,8	Drain	 Top View PDFN5x6-8L	

**KJ1R5N03G**

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C = 25 °C	30	-	V
V _{GS}	Gate-Source Voltage	T _C = 25 °C	-	± 20	V
I _D	Drain Current	T _C = 25 °C, V _{GS} = 10 V	-	180	A
		T _C = 100 °C, V _{GS} = 10 V	-	120	A
I _{DM} *	Pulsed Drain Current	T _C = 25 °C, V _{GS} = 10 V	-	378	A
P _{tot}	Total Power Dissipation	T _C = 25 °C	-	59	W
T _{stg}	Storage Temperature		- 55	150	°C
T _J	Junction Temperature		-	150	°C
I _S **	Diode Forward Current	T _C = 25 °C	-	180	A
E _{AS}	Single Pulsed Avalanche Energy	T _J = 25 °C, V _{DD} = 27 V, R _G = 25 Ω, L = 0.3 Mh I _{AS} = 32 V	-	160	mJ
R _{θJC}	Thermal Resistance- Junction to Case		-	1.5	°C / W

Notes :

* Repetitive Rating: pulse width limited by maximum junction temperature

** Surface Mounted on FR4 Board, t ≤ 10 sec

4. Marking Information

Product Name	Marking
KJ1R5N03G	1R5N03 YWWXXX: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ1R5N03G	PDFN5*6	13 inch		5000	

Note: KUAIJIEXIN defines " Green " as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)



KJ1R5N03G

6. Electrical Characteristics ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

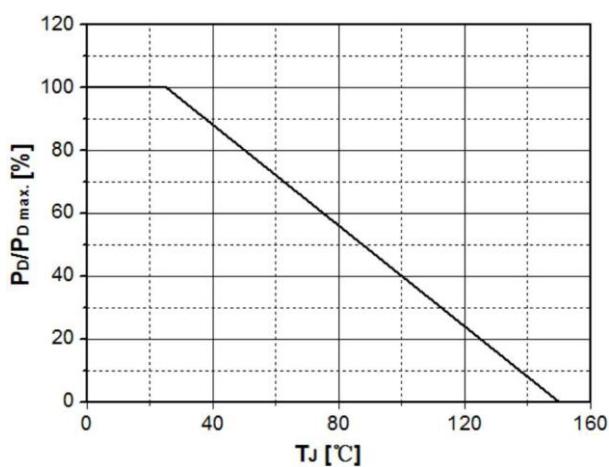
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_{\text{D}} = 250 \mu\text{A}$	30	-	-	V
$V_{\text{GS(th)}}^{\text{a}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{DS}} = 250 \mu\text{A}$	1.2	1.7	2.2	V
I_{DSS}	Zero Gate Voltage Source Current	$V_{\text{DS}} = 40 \text{ V}, V_{\text{GS}} = 0 \text{ V}$	-	-	1	μA
I_{GSS}	Gate Leakage Current	$V_{\text{GS}} = \pm 20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	-	-	± 100	nA
$R_{\text{DS(ON)}}^{\text{a}}$	Drain-Source On-State Resistance	$V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 25 \text{ A}$	-	0.9	1.1	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5 \text{ V}, I_{\text{D}} = 25 \text{ A}$	-	1.5	1.7	
Diode Characteristics						
V_{SD}^{a}	Diode Forward Voltage	$I_{\text{SD}} = 10 \text{ A}, V_{\text{GS}} = 0 \text{ V}$	-	-	1.2	V
t_{rr}	Reverse Recovery Time	$I_{\text{SD}} = 15 \text{ A}, dI_{\text{SD}}/dt = 100 \text{ A}/\mu\text{s}$	-	84	-	nS
Q_{rr}	Reverse Recovery Charge		-	124	-	nC
Dynamic Characteristics^b						
C_{iss}	Input Capacitance	$V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 15 \text{ V}$ Frequency = 1 MHz	-	3800	-	pF
C_{oss}	Output Capacitance		-	2400	-	
C_{rss}	Reverse Transfer Capacitance		-	35	-	
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DS}} = 15 \text{ V}, V_{\text{GEN}} = 10 \text{ V},$ $R_G = 3 \Omega, I_{\text{DS}} = 15 \text{ A}$	-	16.6	-	nS
t_r	Turn-on Rise Time		-	48.2	-	
$t_{\text{d(off)}}$	Turn-off Delay Time		-	66	-	
t_f	Turn-off Fall Time		-	20.4	-	
Gate Charge Characteristics^b						
Q_g	Total Gate Charge	$V_{\text{DS}} = 15 \text{ V}, V_{\text{GS}} = 4.5 \text{ V},$ $I_{\text{DS}} = 15 \text{ A}$	-	31	-	nC
Q_{gs}	Gate-Source Charge		-	8.6	-	
Q_{gd}	Gate-Drain Charge		-	15	-	

Notes :

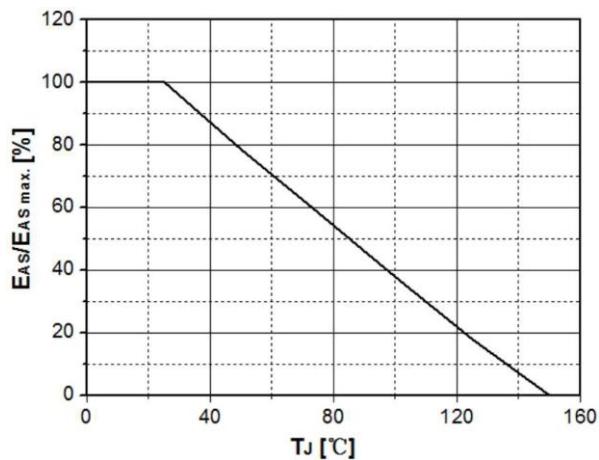
a : Pulse test ; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2 \%$

7. Typical Characteristics

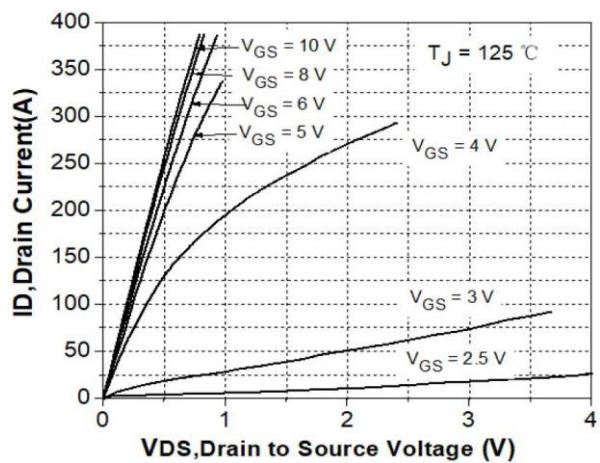
Power Dissipation Derating Curve



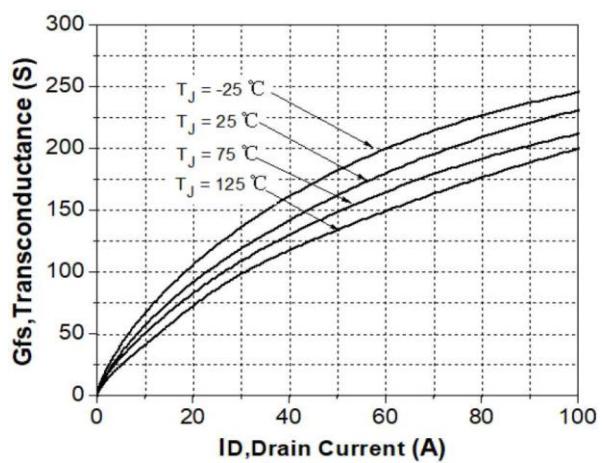
Avalanche Energy Derating Curve vs. Junction Temperature



Typical Output Characteristics

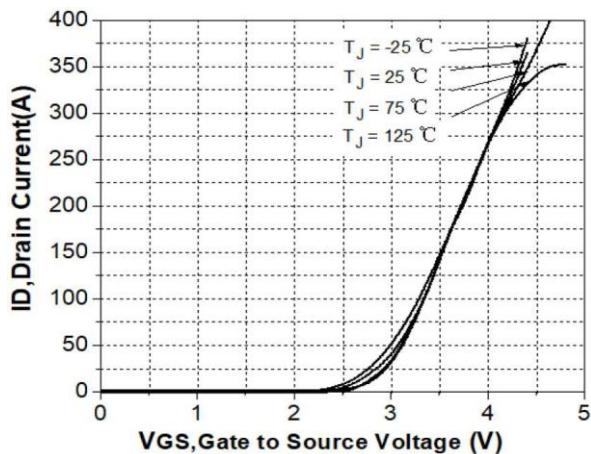


Transconductance vs. Drain Current

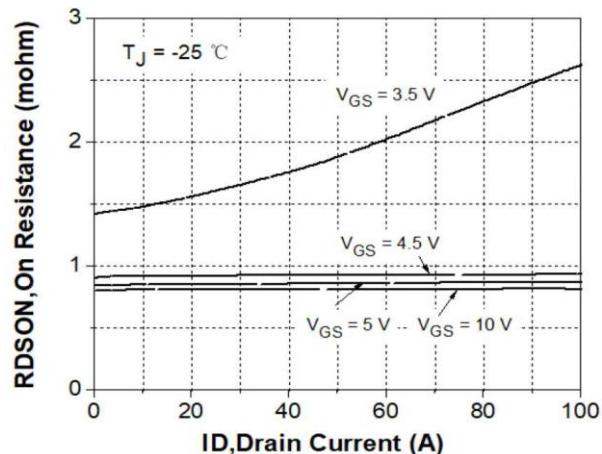


7.Typical Characteristics (Cont.)

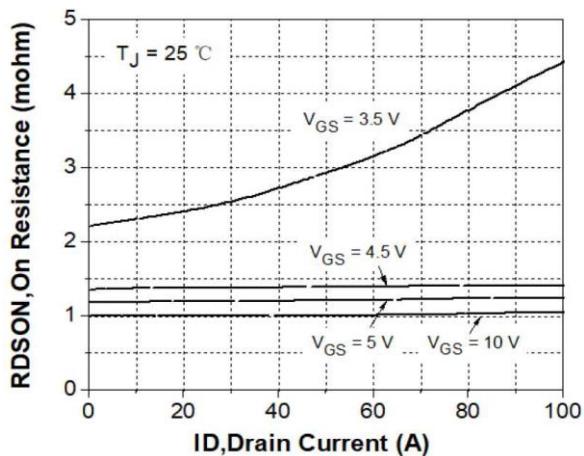
Typical Transfer Characteristics



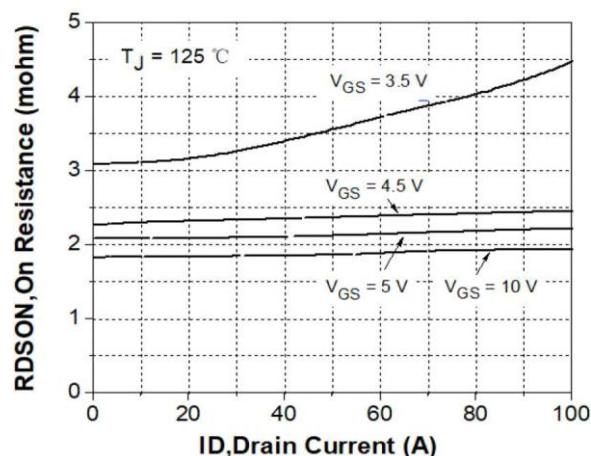
State Resistance vs. Drain Current @-25°C



State Resistance vs. Drain Current @25°C

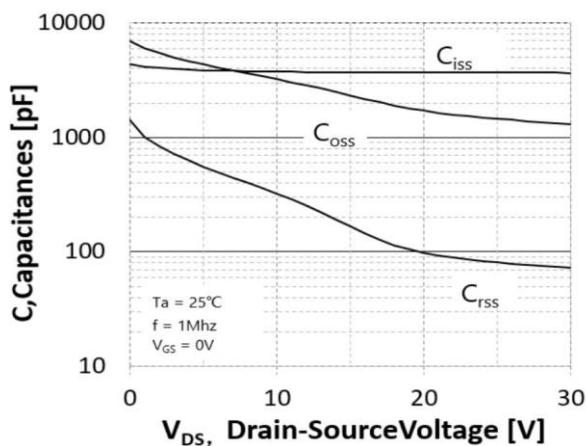


State Resistance vs. Drain Current @125°C

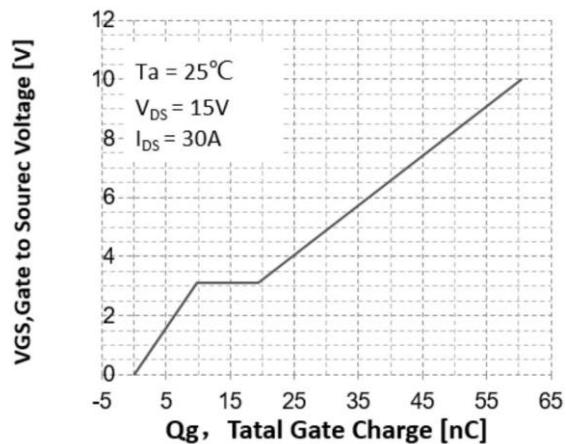


7. Typical Characteristics (Cont.)

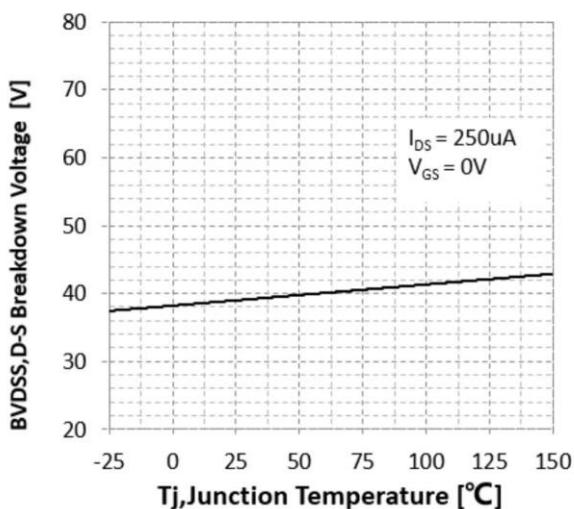
Typical Capacitance vs. Drain Source Voltage



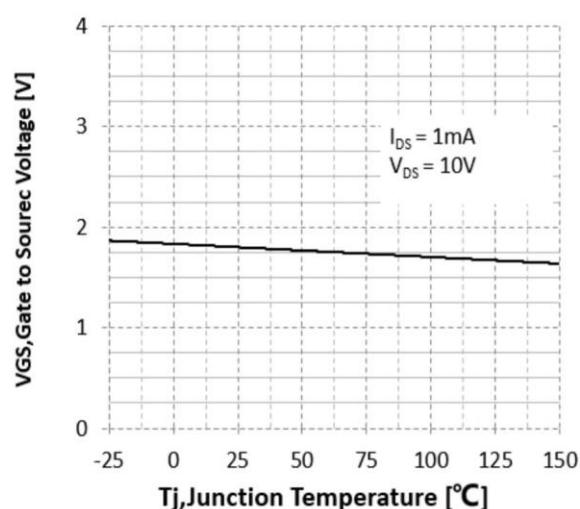
Dynamic Input Characteristics



Breakdown Voltage vs. Junction Temperature

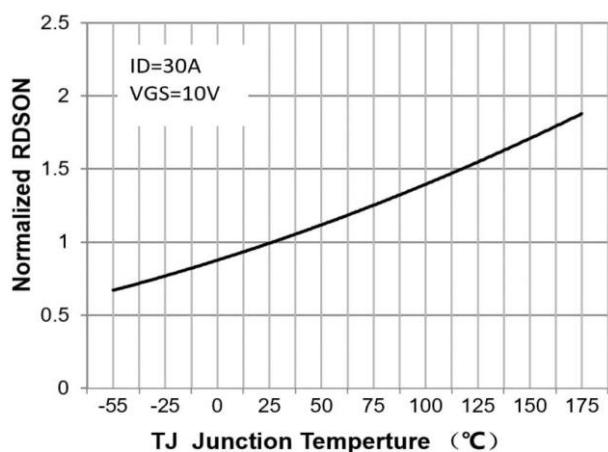


Gate Threshold Voltage vs. Junction Temperature

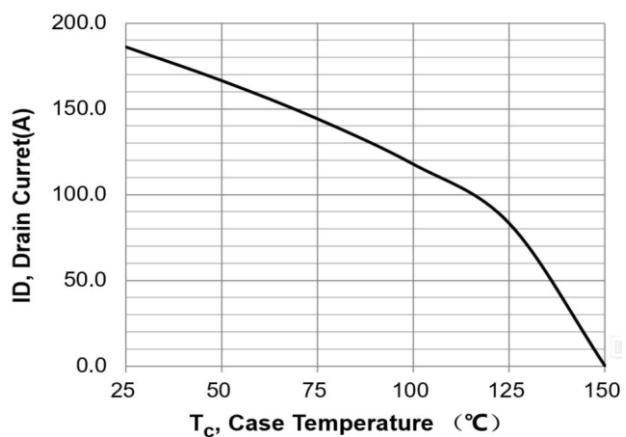


7. Typical Characteristics (Cont.)

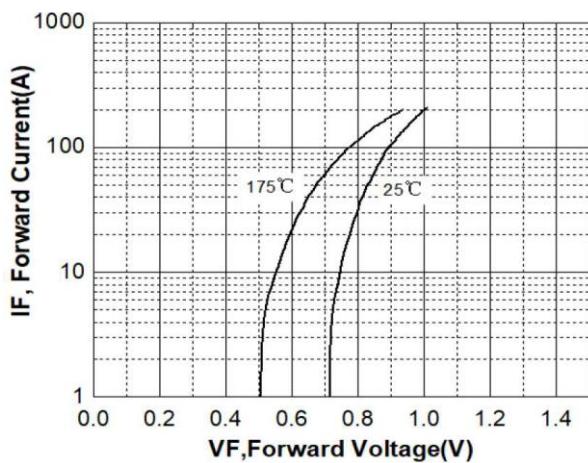
On-Resistance Variation vs. Junction



Maximum Drain Current vs. Case Temperature

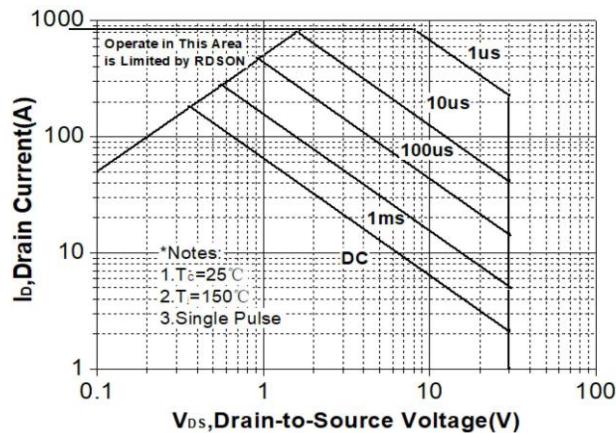


Body Diode Forward Voltage vs. Reverse Drain Current

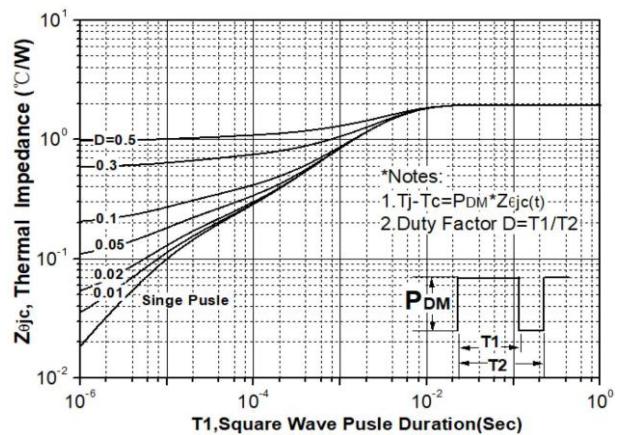


7. Typical Characteristics (Cont.)

Safe Operating Area

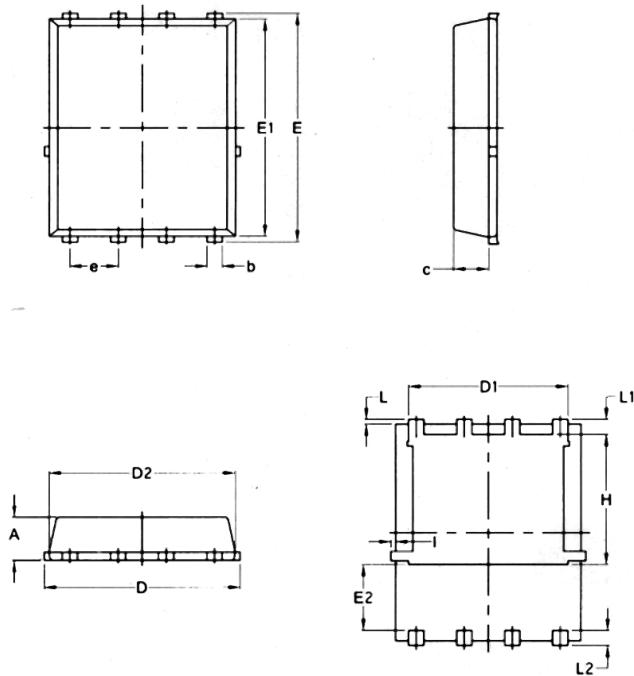


Transient Thermal Response Curve



8. Package Dimensions

PDFN5x6 - 8L Package



SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.970	0.0324	0.0382
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	—	0.0630	—
e	1.27	BSC	0.05	BSC
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	—	0.18	—	0.0070