

# N-Channel Enhancement Mode MOSFET

## 1. Product Information

### Features

- Self-aligned Planar Technology
- Excellent switching performance
- Low avalanche energy

### Applications

- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)

### Quick reference

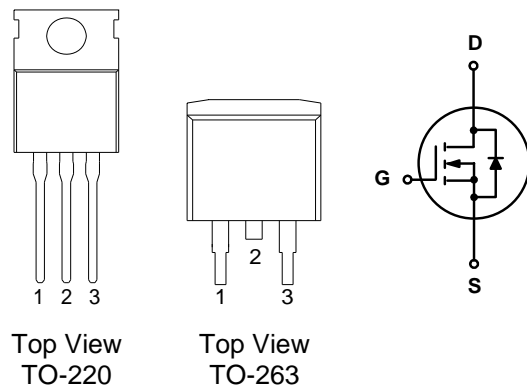
- $V_{DS} = 200V$
- $I_D = 40A$
- $R_{DS(ON)} \leq 65m\Omega @ V_{GS} = 10V$  (Type: 50 m $\Omega$ )

### Pin Description

Pin	Description
1	Gate(G)
2	Drain(D)
3	Source(S)

### Simplified Outline

### Symbol



### Package Marking and Ordering Information

Product Name	Package	Marking	Reel Size	Tape Width	Quantity
KJ40N20C	TO-220-3L	<div style="background-color: black; color: white; padding: 2px; display: inline-block;">40N20 XXXXYY</div> XXXYYY: Date Code	-	-	1000
KJ40N20D	TO-263-3L		-	-	800

## 2. Absolute Maximum Ratings (T<sub>J</sub>=25°C unless otherwise noted)

Symbol	Parameter	Values	Unit
V <sub>DS</sub>	Drain-Source Voltage	200	V
V <sub>GS</sub>	Gate-Source Voltage	±30	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @10V	40	A
I <sub>DM</sub>	Pulsed Drain Current	180	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Power Dissipation	158	W
E <sub>AS</sub>	Single Pulse Avalanche Energy	588	mJ
I <sub>AR</sub>	Avalanche Current	28	A
E <sub>AR</sub>	Repetitive Avalanche Current	15.8	mJ
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55~150	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering Purposes	300	°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	62	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case	1	°C/W

### 3. Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	200	-	-	V
V <sub>GS(th)</sub>	Gate-Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =200V, V <sub>GS</sub> =0V T <sub>C</sub> =25°C	-	-	1	μA
I <sub>GSS</sub>	Gate-body Leakage current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±30V	-	-	±100	nA
R <sub>DS(on)</sub>	Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =14.0A	-	50	65	mΩ
g <sub>fs</sub>	Forward Transconductance S	V <sub>DS</sub> = 40V, I <sub>D</sub> =14.0A	-	24	-	S
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	-	2879	3742	pF
C <sub>oss</sub>	Output Capacitance		-	362	470	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	51	105	
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =160V, I <sub>D</sub> =28A	-	103	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	16	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	53	-	
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> =10V, V <sub>DD</sub> =100V, R <sub>G</sub> =25Ω, I <sub>D</sub> =28A  (Note 4, 5)	-	28	69	ns
t <sub>r</sub>	Turn-on Rise Time		-	251	494	
t <sub>d(off)</sub>	Turn-off Delay Time		-	309	617	
t <sub>f</sub>	Turn-off Fall Time		-	220	412	
I <sub>S</sub>	Diode Forward Current	T <sub>C</sub> =25°C	-	-	28	A
I <sub>SM</sub>	Pulsed Diode Forward Current		-	-	112	A
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =20A, V <sub>GS</sub> =0V	-	-	1.3	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>S</sub> =28A, V <sub>GS</sub> =0V dI <sub>F</sub> /dt=100A/μs	-	218	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	1.91	-	nC

**Notes:**

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The E<sub>AS</sub> data shows Max. rating. L=1.5mH, I<sub>AS</sub>=28A, V<sub>DD</sub>=50V, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The test condition is Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%.
4. The power dissipation is limited by 150°C junction temperature.
5. The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

4. Typical Characteristics

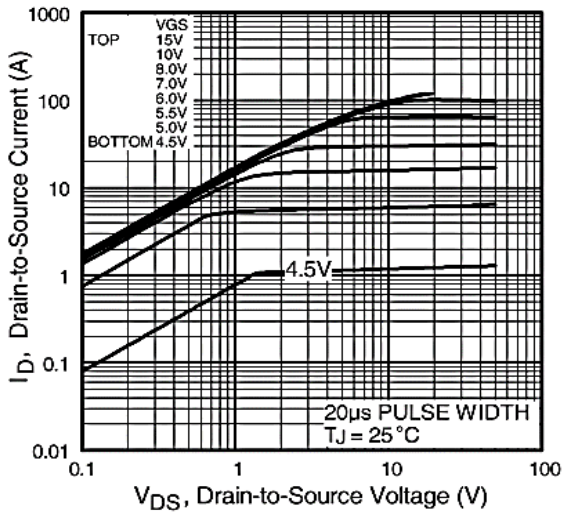


Fig 1. Typical Output Characteristics

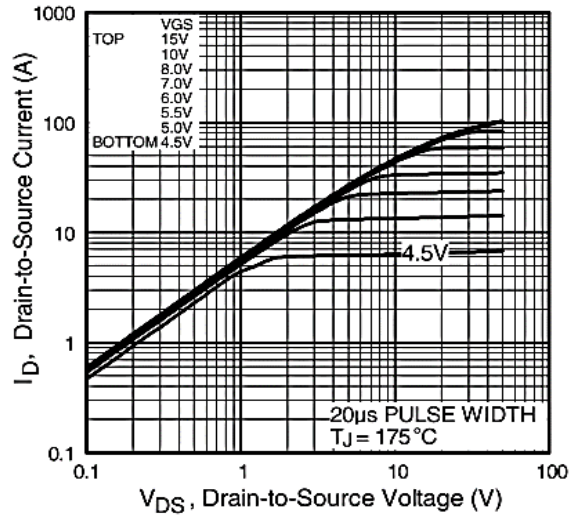


Fig 2. Typical Output Characteristics

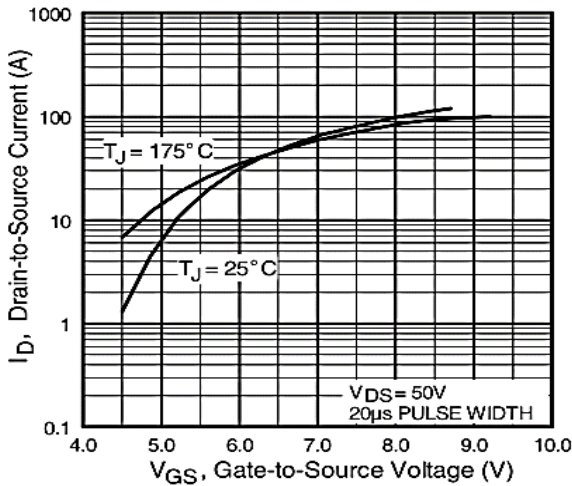


Fig 3. Typical Transfer Characteristics

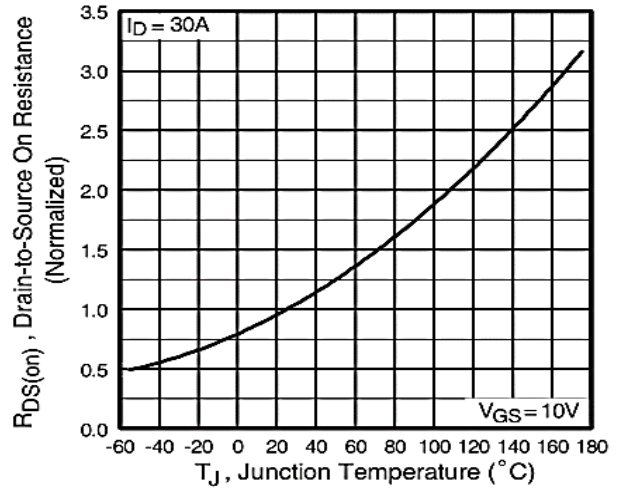


Fig 4. Normalized On-Resistance Vs. Temperature

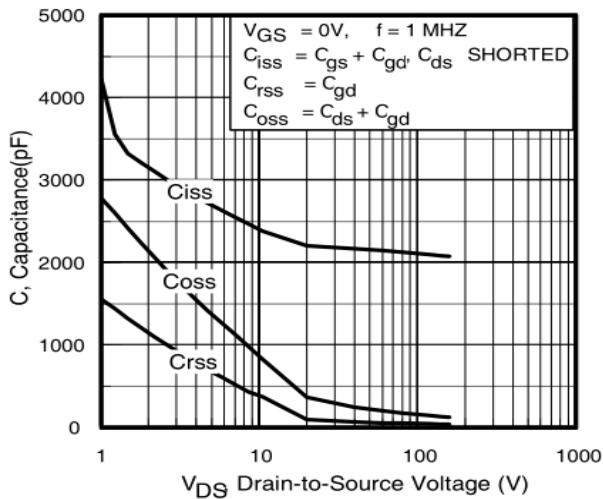


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

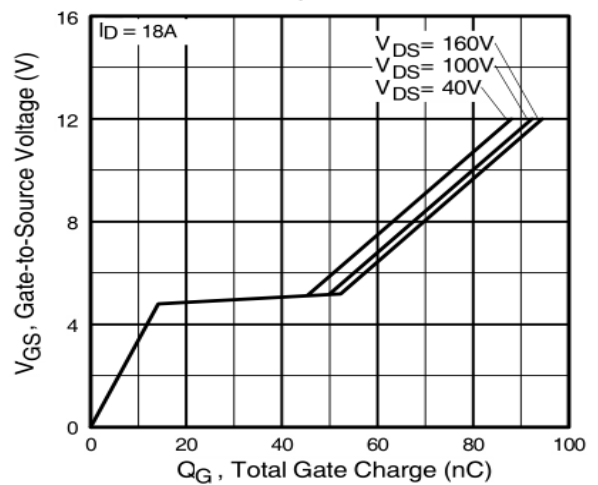
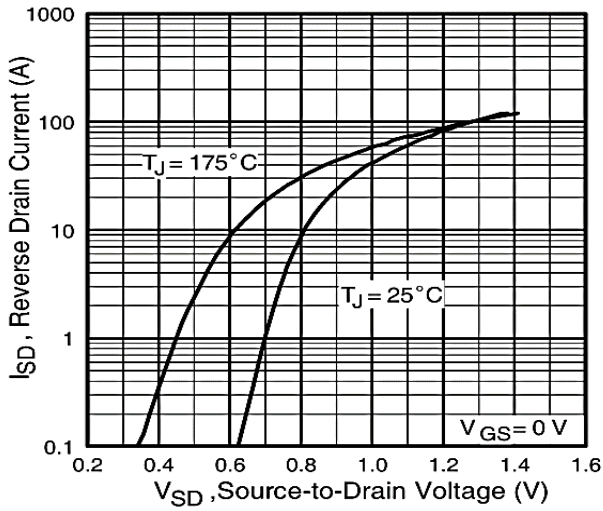
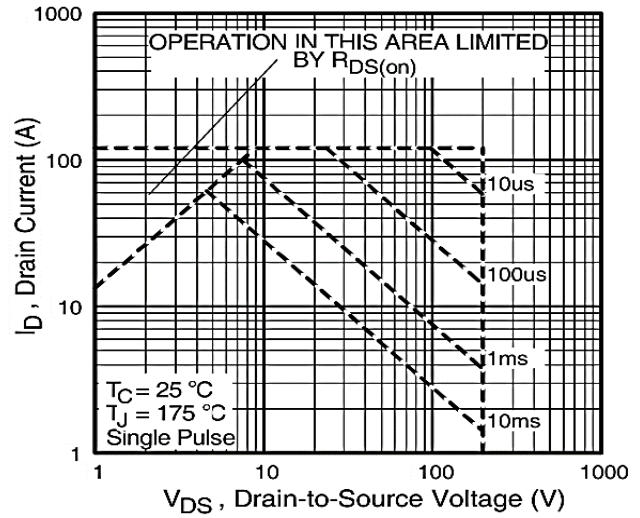


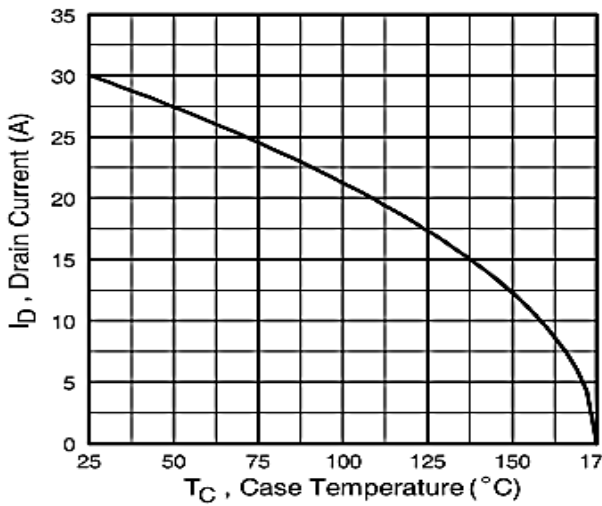
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage



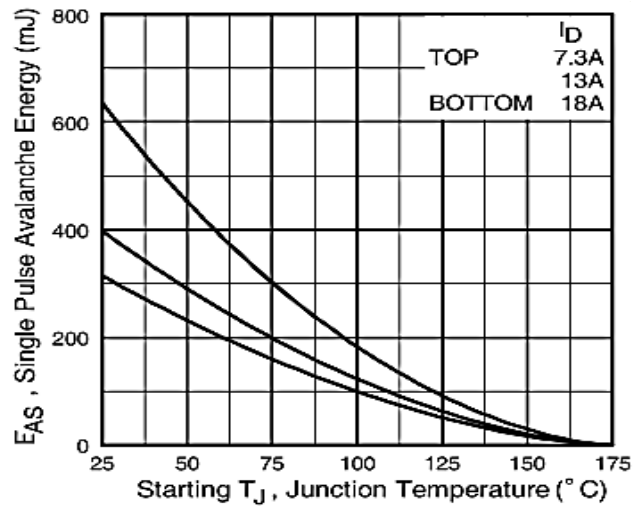
**Fig 7. Typical Source-Drain Diode Forward Voltage**



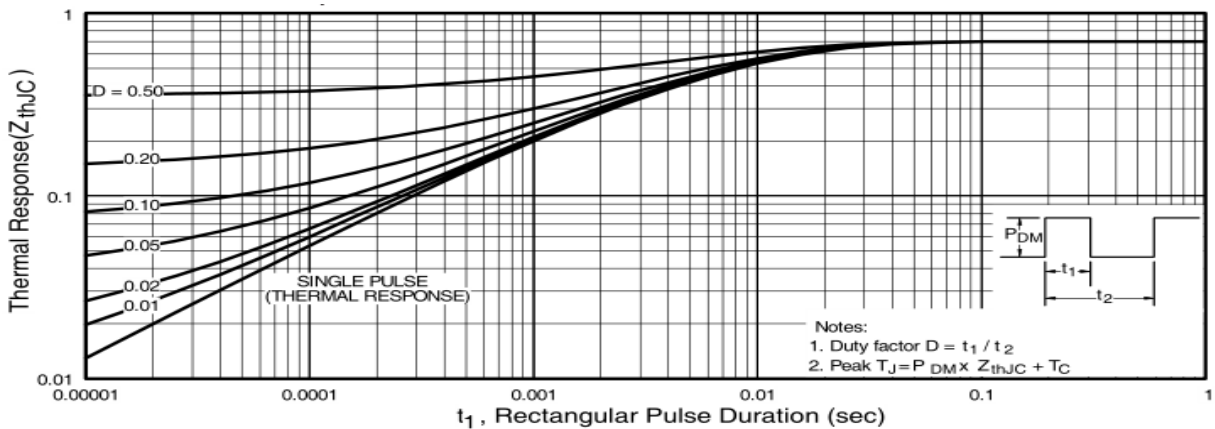
**Fig 8. Maximum Safe Operating Area**



**Fig 9. Maximum Drain Current Vs. Case Temperature**



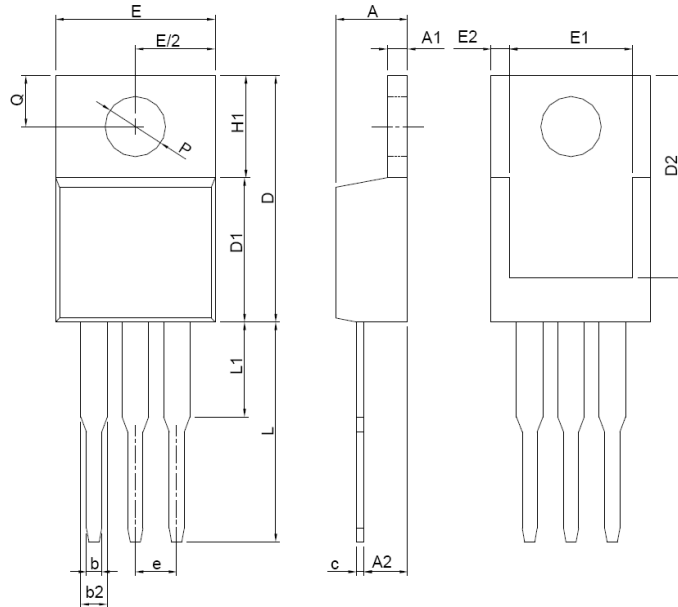
**Fig 12c. Maximum Avalanche Energy Vs. Drain Current**



**Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case**

**5.Package Mechanical Data**

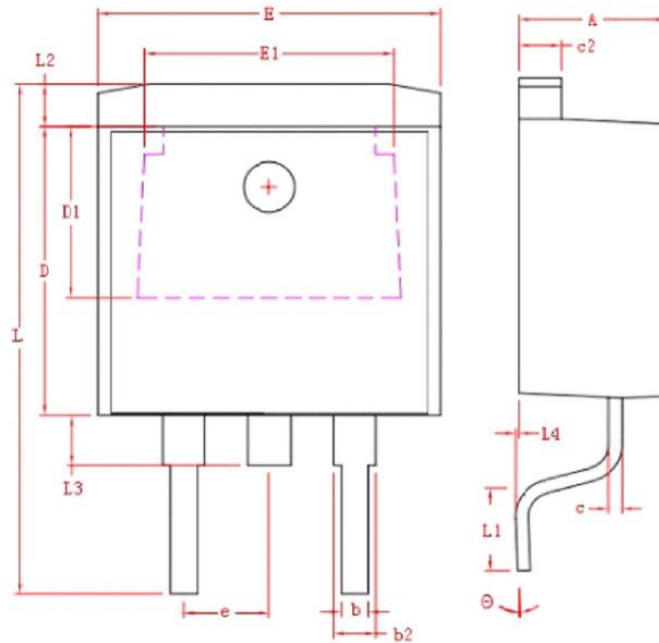
TO-220-3L



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	3.56	4.83
A1	0.51	1.40
A2	2.03	2.92
b	0.38	1.02
b2	1.14	1.78
c	0.36	0.61
D	14.22	16.51
D1	8.38	9.02
D2	12.19	12.88
E	9.65	10.67
E1	6.86	8.89
E2	0.76BSC	
e	2.54BSC	
H1	5.84	6.86
L	12.70	14.73
L1	6.35BSC	
P	3.53	4.09
Q	2.54	3.43

**5.Package Mechanical Data**

TO-263-3L



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	4.40	4.80
b	0.76	1.00
L4	0.00	0.25
C	0.36	0.50
L3	1.50 REF	
L1	2.29	2.79
E	9.80	10.40
E1	7.40 REF	
c2	1.25	1.45
b2	1.17	1.47
D	8.60	9.00
D1	5.10 REF	
e	2.54 REF	
L	14.6	15.8
$\theta$	$0^\circ \pm 3^\circ$	
L2	1.27 REF	