

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Surface-mounted package
- Advanced trench cell design
- Extremely low threshold voltage

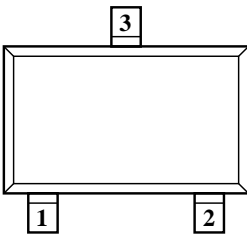
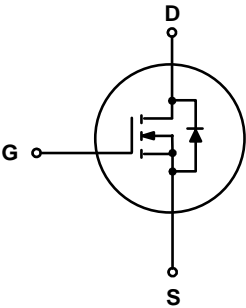
1.2 Applications

- PWM applications
- Load switch

1.3 Quick reference

- $BV \geq 20\text{ V}$
- $I_D \leq 6\text{ A}$
- $R_{DS(ON)} \leq 20\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$
- $R_{DS(ON)} \leq 28\text{ m}\Omega @ V_{GS} = 2.5\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate(G)	 <p>Top View SOT23-3L</p>	
2	Source(S)		
3	Drain(D)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_A = 25\text{ }^\circ\text{C}$	20	-	V
V_{GS}	Gate-Source Voltage	$T_A = 25\text{ }^\circ\text{C}$	-	± 12	V
I_D^*	Drain Current	$T_A = 25\text{ }^\circ\text{C}, V_{GS} = 4.5\text{ V}$	-	6	A
I_{DM}^{**}	Pulsed Drain Current	$T_A = 25\text{ }^\circ\text{C}, V_{GS} = 4.5\text{ V}$	-	24	A
P_{tot}^*	Total Power Dissipation	$T_A = 25\text{ }^\circ\text{C}$	-	1.4	W
T_{stg}	Storage Temperature		- 55	150	$^\circ\text{C}$
T_J	Junction Temperature		-	150	$^\circ\text{C}$
I_S^*	Diode Forward Current	$T_A = 25\text{ }^\circ\text{C}$	-	6	A
$R_{\theta JA}^*$	Thermal Resistance- Junction to Ambient		-	125	$^\circ\text{C} / \text{W}$

Notes :

- * Surface Mounted on 1 in² pad area, $t \leq 10\text{ sec}$
- ** Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

4. Marking Information

Product Name	Marking
KJ2312A	2312

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ2312A	SOT23-3L			3000	

Note: KUAJIEXIN defines " Green " as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_{DS} = 250\text{ }\mu\text{A}$	20	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{DS} = 250\text{ }\mu\text{A}$	0.5	0.7	1	V
I_{DSS}	Drain Leakage Current	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
		$T_J = 85\text{ }^\circ\text{C}$	-	-	30	μA
I_{GSS}	Gate Leakage Current	$V_{GS} = \pm 12\text{ V}, V_{DS} = 0\text{ V}$	-	-	± 100	nA
$R_{DS(ON)}^a$	On-State Resistance	$V_{GS} = 4.5\text{ V}, I_{DS} = 6\text{ A}$	-	15	20	m Ω
		$V_{GS} = 2.5\text{ V}, I_{DS} = 5\text{ A}$	-	19	28	
DRAIN-SOURCE DIODE CHARACTERISTICS						
V_{SD}^a	Diode Forward Voltage	$I_{SD} = 1\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.2	V
Dynamic Characteristics^b						
C_{iss}	Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = 10\text{ V}$ Frequency = 1 MHz	-	900	-	pF
C_{oss}	Output Capacitance		-	220	-	
C_{riss}	Reverse Transfer Capacitance		-	100	-	
$t_d(on)$	Turn-on Delay Time	$V_{DD} = 10\text{ V}, V_{GEN} = 4.5\text{ V},$ $R_G = 6\text{ }\Omega, I_{DS} = 6\text{ A}$	-	10	-	nS
t_r	Turn-on Rise Time		-	11	-	
$t_d(off)$	Turn-off Delay Time		-	35	-	
t_f	Turn-off Fall Time		-	20	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge	$V_{GS} = 4.5\text{ V}, V_{DS} = 10\text{ V},$ $I_{DS} = 6\text{ A}$	-	12	-	nC
Q_{gs}	Gate-Source Charge		-	2.3	-	
Q_{gd}	Gate-Drain Charge		-	1	-	

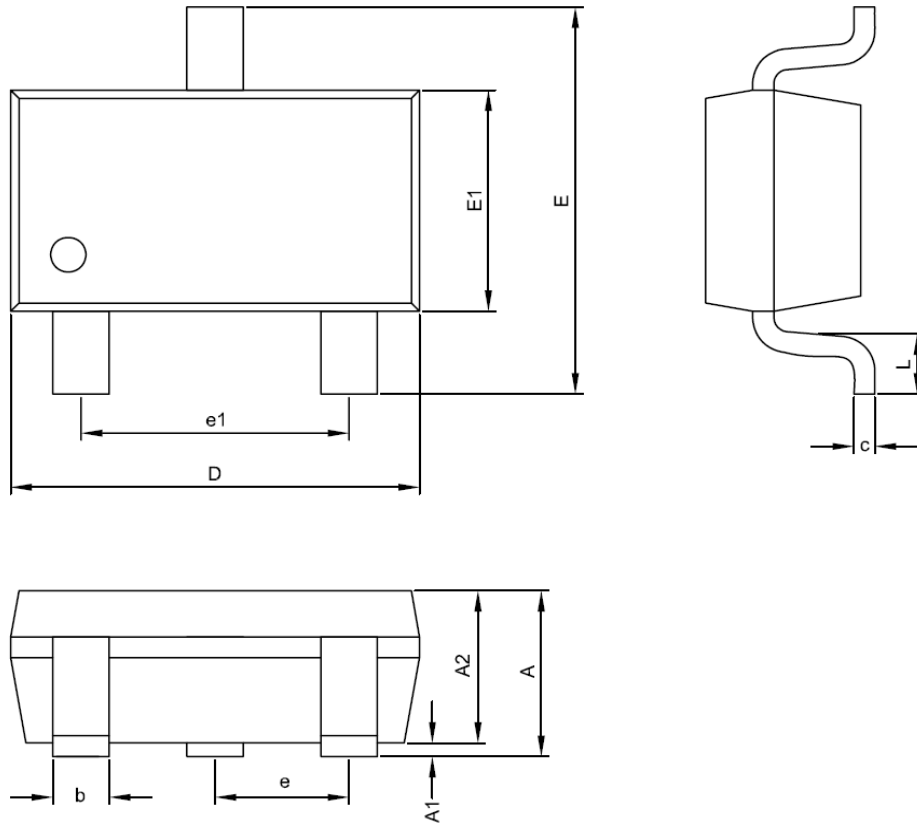
Notes :

- a : Pulse test ; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
- b : Guaranteed by design, not subject to production testing



7. Package Dimensions

SOT23-3L



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.00	1.45
A1	0.00	0.15
A2	1.00	1.30
D	2.70	3.10
E	2.60	3.00
E1	1.50	1.70
c	0.08	0.25
b	0.30	0.50
e	0.95 BSC	
e1	1.90 BSC	
L	0.30	0.60