

## P-Channel Enhancement Mode MOSFET

### 1. Product Information

#### 1.1 Features

- ✓ Advanced trench cell design
- ✓ Lead free product is acquired
- ✓ Low Thermal Resistance
- ✓ Surface mount package

#### 1.2 Applications

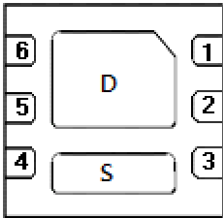
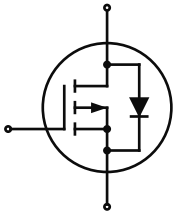
- ✓ PWM applications
- ✓ Load switch

#### 1.3 Quick reference

- ✓  $BV \geq -12\text{ V}$
- ✓  $P_{tot} \leq 18\text{ W}$
- ✓  $I_D \leq -16\text{ A}$
- ✓  $R_{DS(ON)} \leq 23\text{ m}\Omega @ V_{GS} = -2.5\text{ V}$
- ✓  $R_{DS(ON)} \leq 15\text{ m}\Omega @ V_{GS} = -4.5\text{ V}$

### 2. Pin Description

### 3. Pin Description

| Pin     | Description | Simplified Outline                                                                                               | Symbol                                                                                |
|---------|-------------|------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|
| 1,2,5,6 | Drain       | <br>Bottom View<br>DFN2x2-6L |  |
| 3       | Gate        |                                                                                                                  |                                                                                       |
| 4       | Source      |                                                                                                                  |                                                                                       |

## 4. Limiting Values

| Symbol     | Parameter                       | Conditions                         | Min  | Max      | Unit                          |
|------------|---------------------------------|------------------------------------|------|----------|-------------------------------|
| $V_{DS}$   | Drain-Source Voltage            | $T_C = 25\text{ }^{\circ}\text{C}$ | -    | -17      | V                             |
| $V_{GS}$   | Gate-Source Voltage             | $T_C = 25\text{ }^{\circ}\text{C}$ | -    | $\pm 12$ | V                             |
| $I_{D*}$   | Drain Current-continuous        | $T_A = 25\text{ }^{\circ}\text{C}$ | -    | -16      | A                             |
| $I_{D*}$   | Drain Current-continuous        | $T_A = 70\text{ }^{\circ}\text{C}$ | -    | -12      | A                             |
| $P_D$      | Total Power Dissipation         | $T_C = 25\text{ }^{\circ}\text{C}$ | -    | 18       | W                             |
| $P_D$      | Total Power Dissipation         | $T_C = 70\text{ }^{\circ}\text{C}$ | -    | 12       | W                             |
| $T_{stg}$  | Storage Temperature             |                                    | - 55 | 150      | $^{\circ}\text{C}$            |
| $T_J$      | Junction Temperature            |                                    | -    | 150      | $^{\circ}\text{C}$            |
| $I_S$      | Diode Forward Current           | $T_C = 25\text{ }^{\circ}\text{C}$ | -    | -16      | A                             |
| $R_{thJA}$ | Maximum junction-to-ambient     | $T \leq 5s$                        |      | 36       | $^{\circ}\text{C} / \text{W}$ |
| $R_{thJC}$ | Maximum junction-to-case(drain) | Steady state                       | -    | 6.5      | $^{\circ}\text{C} / \text{W}$ |

Notes :

- \* Surface Mounted on 1 in<sup>2</sup> pad area,  $t \leq 10\text{ sec}$
- \*\* Pulse width  $\leq 10\text{ }\mu\text{s}$ , duty cycle  $\leq 1\%$
- \*\*\* Limited by bonding wire

## 4. Marking Information

| Product Name | Marking                                                                                                                                             |
|--------------|-----------------------------------------------------------------------------------------------------------------------------------------------------|
| KJX1216D     | <div style="display: inline-block; border: 1px solid black; padding: 2px;"> <b>JX1216</b><br/> <b>YWWXXX</b> </div> <b>YWW:</b><br><b>Date Code</b> |

## 5. Ordering Code

| Product Name | Package   | Reel Size | Tape width | Quantity | Note |
|--------------|-----------|-----------|------------|----------|------|
| KJX1216D     | DFN2x2-6L |           |            | 3000     |      |

Note: KUAJIJEXIN defines “ Green ” as lead-free ( RoHS compliant ) and halogen free ( Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C )

## 6. Electrical Characteristics (T<sub>C</sub> = 25 °C Unless Otherwise Noted)

| Symbol                                   | Parameter                        | Conditions                                                                                                                  | Min  | Typ   | Max   | Unit |
|------------------------------------------|----------------------------------|-----------------------------------------------------------------------------------------------------------------------------|------|-------|-------|------|
| Static Characteristics                   |                                  |                                                                                                                             |      |       |       |      |
| BV <sub>DSS</sub>                        | Drain-Source Breakdown Voltage   | V <sub>GS</sub> = 0 V, I <sub>D</sub> = -250 μA                                                                             | -12  | -17   | -     | V    |
| V <sub>GS(th)</sub>                      | Gate Threshold Voltage           | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>DS</sub> = -250 μA                                                               | -0.5 | -0.75 | -1.2  | V    |
| I <sub>DSS</sub>                         | Zero Gate Voltage Source Current | V <sub>DS</sub> = -12V, V <sub>GS</sub> = 0 V                                                                               | -    | -     | -1    | μA   |
| I <sub>GSS</sub>                         | Gate Leakage Current             | V <sub>GS</sub> = ± 12 V, V <sub>DS</sub> = 0 V                                                                             | -    | -     | ± 100 | nA   |
| R <sub>DS(ON)</sub>                      | Drain-Source On-State Resistance | V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -8A                                                                               | -    | 13    | 15    | m Ω  |
|                                          |                                  | V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -6 A                                                                             | -    | 18    | 23    | m Ω  |
| gfs                                      | Forward transconductance         | V <sub>GS</sub> = -6 V, I <sub>D</sub> = -7 A                                                                               |      | 32    |       | S    |
| Diode Characteristics                    |                                  |                                                                                                                             |      |       |       |      |
| V <sub>SD</sub>                          | Diode Forward Voltage            | I <sub>SD</sub> = -1.25 A, V <sub>GS</sub> = 0 V                                                                            | -    | -0.7  | -1.2  | V    |
| Dynamic Characteristics <sup>b</sup>     |                                  |                                                                                                                             |      |       |       |      |
| C <sub>iss</sub>                         | Input Capacitance                | V <sub>GS</sub> = 0 V, V <sub>DS</sub> = -6 V<br>Frequency = 1 MHz                                                          | -    | 1300  | -     | pF   |
| C <sub>oss</sub>                         | Output Capacitance               |                                                                                                                             | -    | 380   | -     |      |
| C <sub>rss</sub>                         | Reverse Transfer Capacitance     |                                                                                                                             | -    | 280   | -     |      |
| t <sub>d(on)</sub>                       | Turn-on Delay Time               | V <sub>DS</sub> = -10 V, V <sub>EN</sub> = -4.5 V,<br>R <sub>L</sub> = 1.2 Ω, I <sub>D</sub> = -5 A<br>R <sub>GEN</sub> =1Ω | -    | 11    | -     | nS   |
| t <sub>r</sub>                           | Turn-on Rise Time                |                                                                                                                             | -    | 35    | -     |      |
| t <sub>d(off)</sub>                      | Turn-off Delay Time              |                                                                                                                             | -    | 30    | -     |      |
| t <sub>f</sub>                           | Turn-off Fall Time               |                                                                                                                             | -    | 10    | -     |      |
| Gate Charge Characteristics <sup>b</sup> |                                  |                                                                                                                             |      |       |       |      |
| Q <sub>g</sub>                           | Total Gate Charge                | V <sub>GS</sub> = -6 V, V <sub>DS</sub> = -4.5 V,<br>I <sub>DS</sub> = -9 A                                                 | -    | 13    | -     | nC   |
| Q <sub>gs</sub>                          | Gate-Source Charge               |                                                                                                                             | -    | 5     | -     |      |
| Q <sub>gd</sub>                          | Gate-Drain Charge                |                                                                                                                             | -    | 5     | -     |      |

Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

b : Guaranteed by design, not subject to production testing

## 7. Typical Characteristics

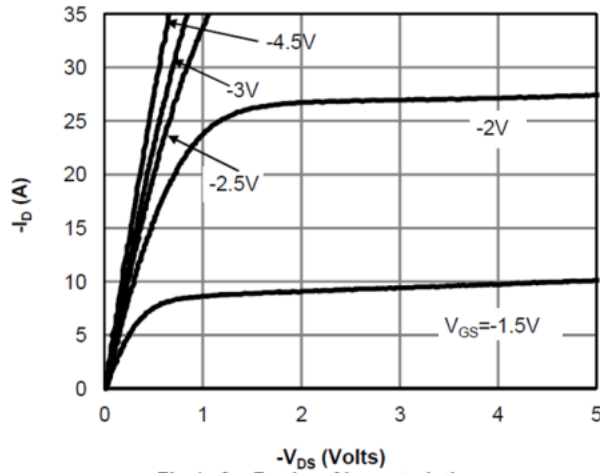


Fig 1: On-Region Characteristics

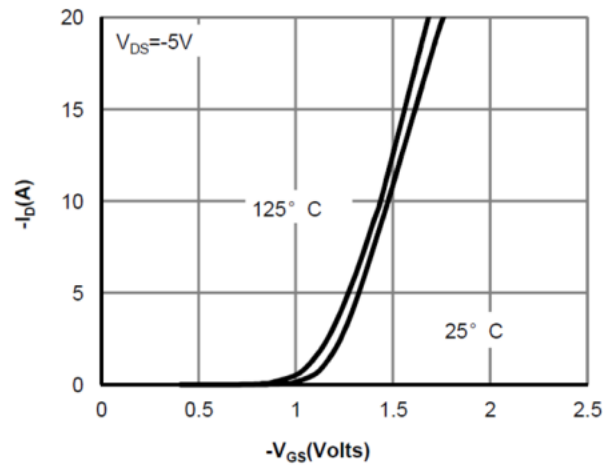


Figure 2: Transfer Characteristics

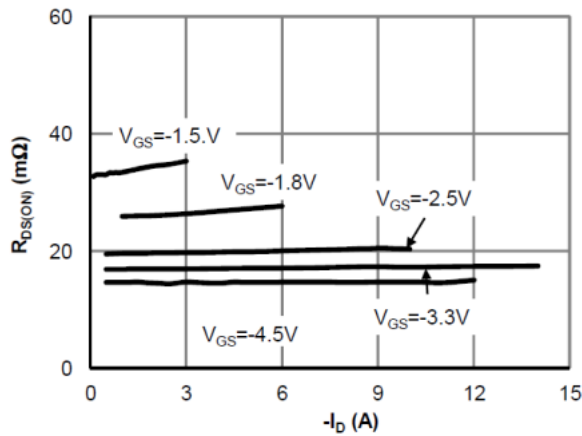


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

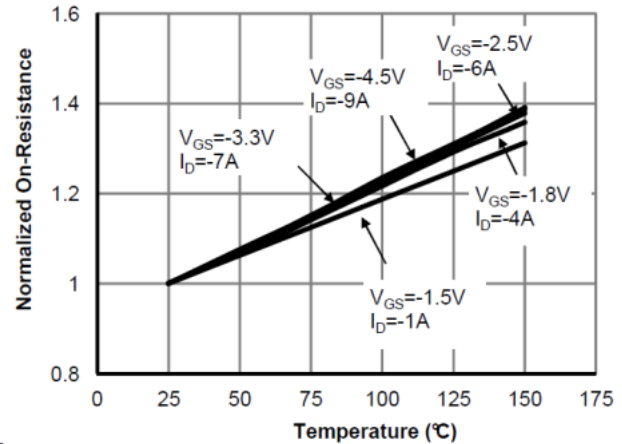


Figure 4: On-Resistance vs. Junction Temperature

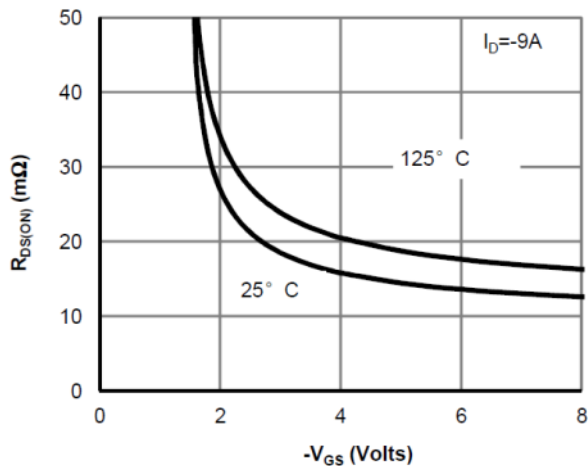


Figure 5: On-Resistance vs. Gate-Source Voltage

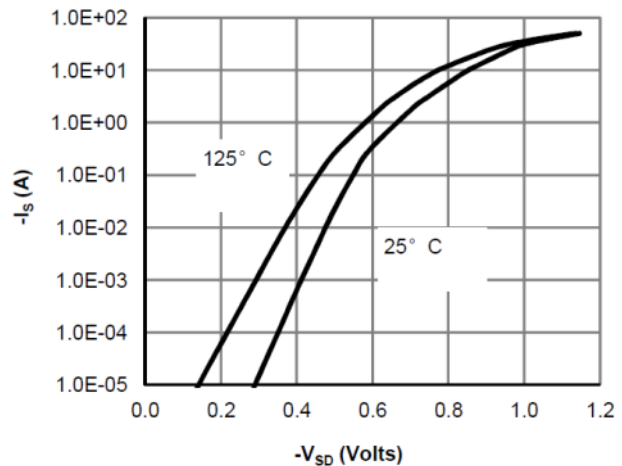
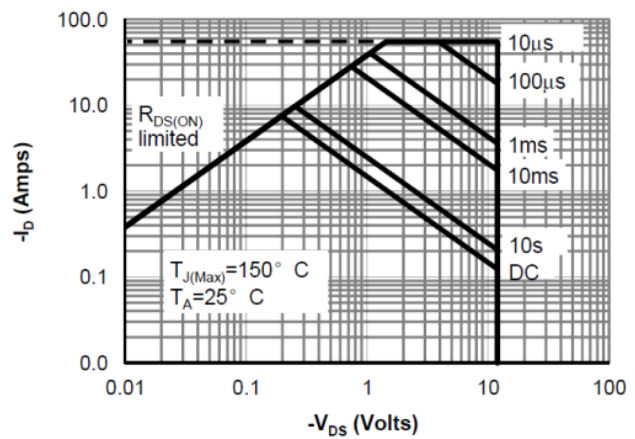
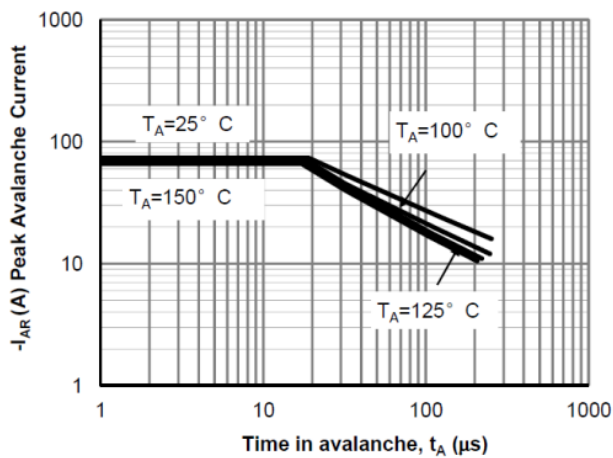
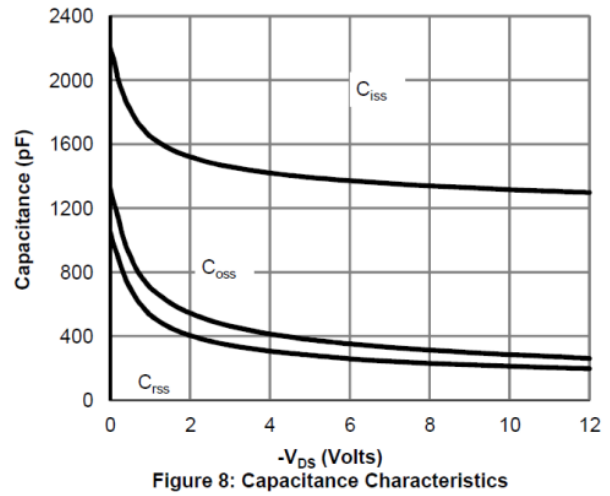
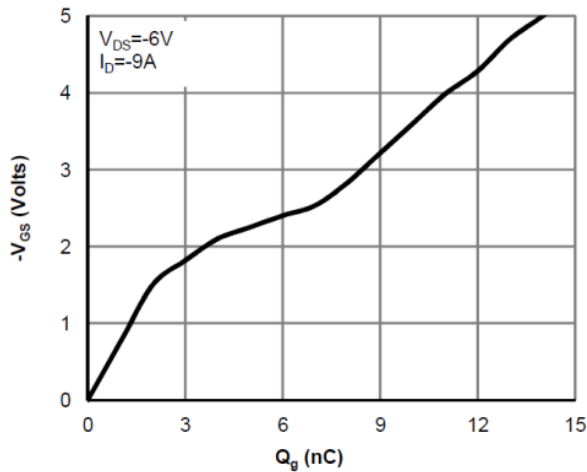


Figure 6: Body-Diode Characteristics

## 7. Typical Characteristics (cont.)



## 7. Typical Characteristics (cont.)

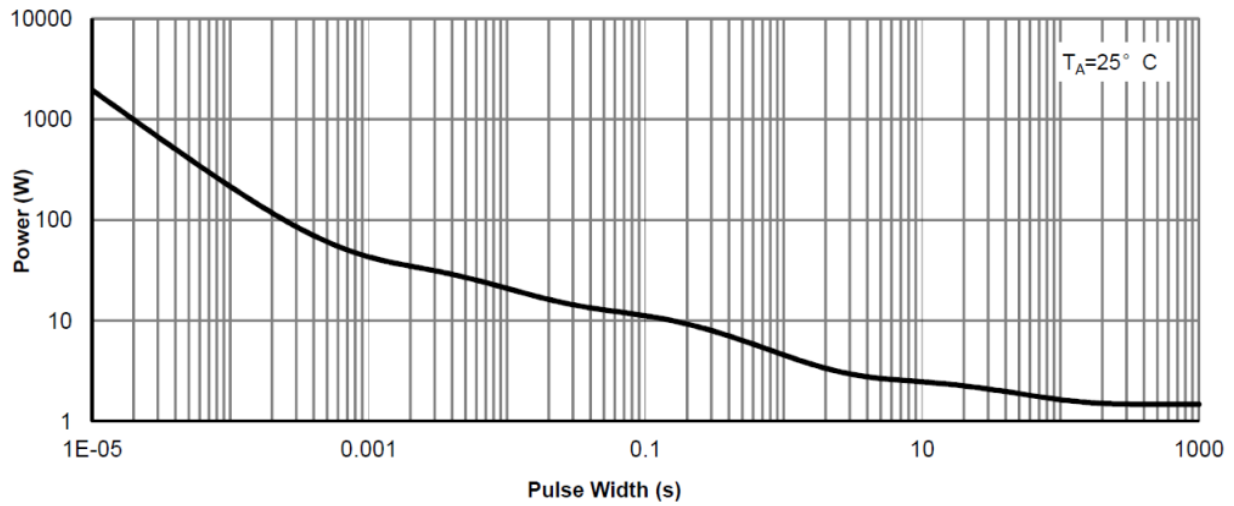


Figure 11: Single Pulse Power Rating Junction-to-Ambient

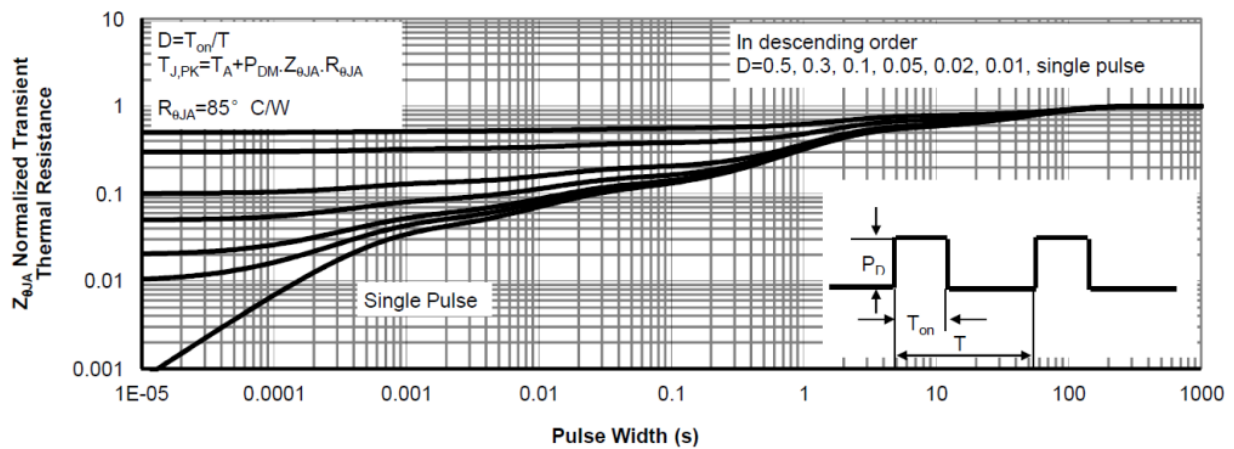
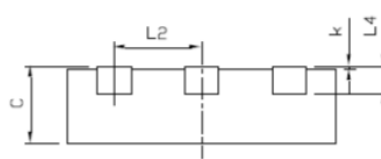
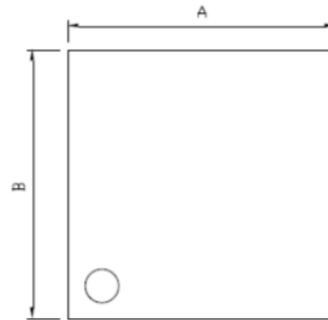
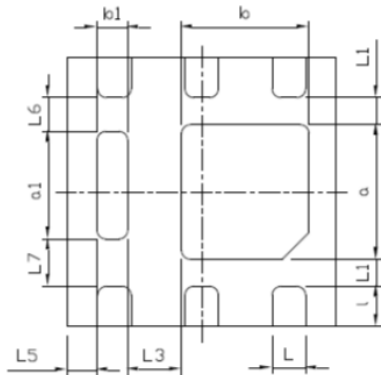


Figure 12: Normalized Maximum Transient Thermal Impedance

## 8.Package Dimensions

### DFN2x2-6L



| Dimensions In Millimeterer |      |       |      |
|----------------------------|------|-------|------|
| Symbol                     | MIN  | TYP   | MAX  |
| A                          | 1.95 | 2.00  | 2.05 |
| B                          | 1.95 | 2.00  | 2.05 |
| C                          | 0.45 | 0.50  | 0.55 |
| L                          | 0.25 | 0.30  | 0.35 |
| L1                         | 0.10 | 0.20  | 0.30 |
| L2                         | —    | 0.65  | —    |
| L3                         | 0.30 | 0.40  | 0.50 |
| L4                         | —    | 0.152 | —    |
| L5                         | 0.12 | 0.22  | 0.32 |
| L6                         | 0.15 | 0.25  | 0.35 |
| L7                         | 0.23 | 0.33  | 0.43 |
| a                          | 0.90 | 1.00  | 1.10 |
| a1                         | 0.72 | 0.82  | 0.92 |
| b                          | 0.85 | 0.95  | 1.05 |
| b1                         | 0.13 | 0.23  | 0.33 |
| l                          | 0.25 | 0.30  | 0.35 |
| k                          | 0.00 | —     | 0.05 |