

N-Channel Enhancement Mode MOSFET

1. Product Information

Features

SGT technology
Excellent $R_{DS(ON)}$
Low gate charge

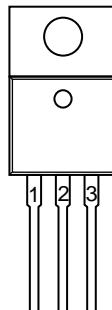
Pin Description

Pin	Description
1	Gate(G)
2	Drain(D)
3	Source(S)

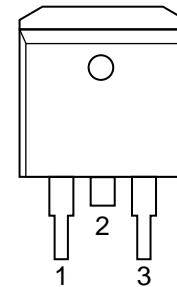
Applications

BMS
UPS
Power management Switches

Simplified Outline

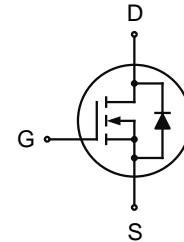


Top View
TO-220



Top View
TO-263

Symbol



Quick reference

$V_{DS} \geq 120V$
 $I_D \leq 200A$
 $R_{DS(ON)} \leq 4.2 \text{ m}\Omega @ V_{GS}=10V$ (Type: $3.7\text{m}\Omega$)

Package Marking and Ordering Information

Product Name	Package	Marking	Reel Size	Tape width	Quantity
KJ200N12C	TO-220	KJ200N12C	-	-	1000
KJ200N12D	TO-263	KJ200N12D	-	-	800

2. Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	120	V
V _{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V$	200	A
$I_D @ T_c=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V$	150	A
I _{DM}	Pulsed Drain Current	600	A
E _{AS}	Single Pulse Avalanche Energy	530	mJ
I _{AS}	Avalanche Current	45	A
$P_D @ T_c=25^\circ\text{C}$	Total Power Dissipation ⁴	240	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C
R _{θJA}	Thermal Resistance Junction-Ambient	0.75	°C/W
R _{θJC}	Thermal Resistance Junction-Case	62	°C/W

3. Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Type	Max.	Unit
VDSS	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu\text{A}$	120	135	-	V
IGSS	Gate-body Leakage current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	± 100	nA
IDSS	Zero Gate Voltage Drain Current $T_J=25^\circ\text{C}$	$V_{DS} = 120V, V_{GS} = 0V$	-	-	1	μA
IDSS	Zero Gate Voltage Drain Current $T_J=100^\circ\text{C}$		-	-	100	
VGS(th)	Gate-Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2.0	2.9	4.0	V
RDS(on)	Drain-Source on-Resistance ²	$V_{GS} = 10V, I_D = 20A$	-	3.7	4.2	$\text{m}\Omega$
RDS(on)	Drain-Source on-Resistance ²	$V_{GS} = 6V, I_D = 20A$		4.3	5.8	$\text{m}\Omega$
Ciss	Input Capacitance	VGS = 0V, VDS = 60V, f = 250kHz	-	5240	-	pF
Coss	Output Capacitance		-	739	-	
Crss	Reverse Transfer Capacitance		-	12	-	
R _g	Gate Resistance	$V_{GS} = 0V, V_{DS} = 0V, f = 1\text{MHz}$	-	1.7	-	Ω
Q _g	Total Gate Charge	VDD = 60V, ID = 45A, VGS = 0 to 10V	-	19	-	nC
Qgs	Gate-Source Charge		-	11	-	
Qgd	Gate-Drain Charge		-	75	-	
td(on)	Turn-on Delay Time	VDD = 60V, VGS = 10V, ID = 45A, RG = 10 Ω	-	59	-	ns
t _r	Rise Time		-	41	-	
td(off)	Turn-off Delay Time		-	96	-	
t _f	Fall Time		-	33	-	
VSD	Diode Forward Voltage ²	$I_F = 20A, V_{GS} = 0V$	-	0.8	1.2	V
IS	Continuous Source Current ^{1.5}	$V_G = V_D = 0V$, Force Current	-	-	200	A
trr	Body Diode Reverse Recovery Time	VR = 60V $I_F = 35A$, dI/dt = 100A/ μs	-	70	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	200	-	nC

Notes:

- 1、The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3、The EAS data shows Max. rating . The test condition is $V_{DD}=50V$, $V_{GS}=10V$, $L=0.5\text{mH}$, $I_{AS}=45A$
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

4. Typical Characteristics

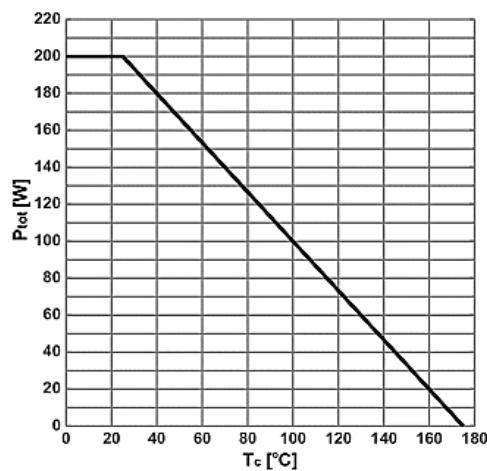


Figure 1. Power dissipation

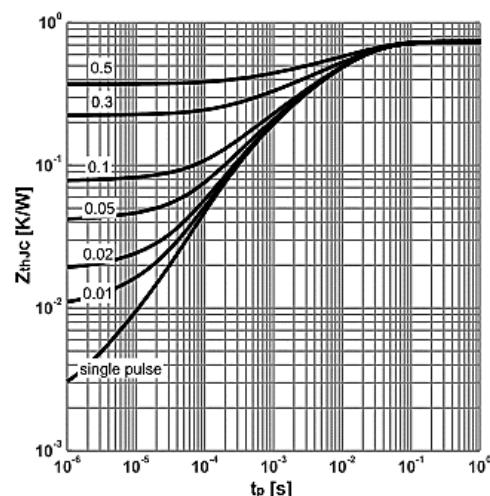


Figure 2. Max. transient thermal impedance

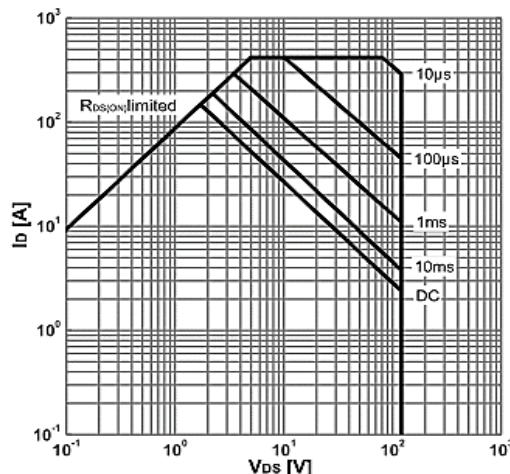


Figure 3. Safe operating area

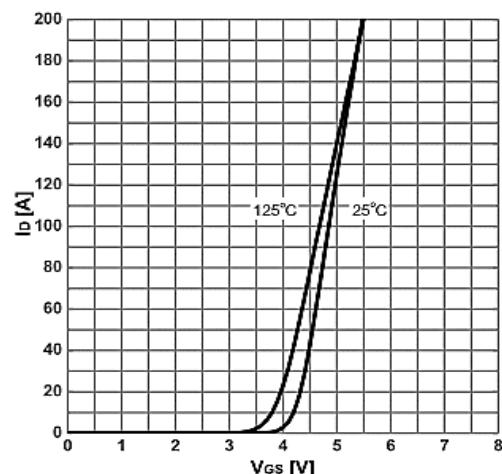


Figure 4. Type. transfer characteristics

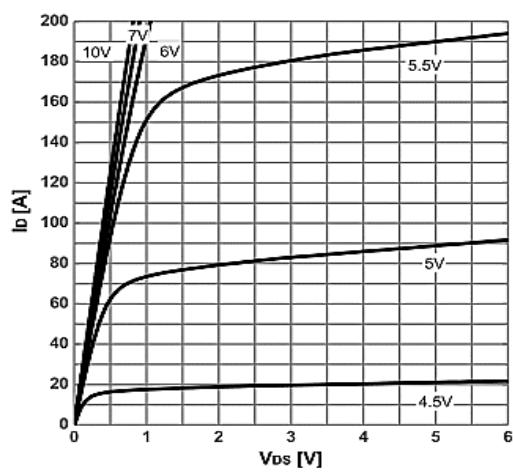


Figure 5. Typ. output characteristics(Tj 25°C)

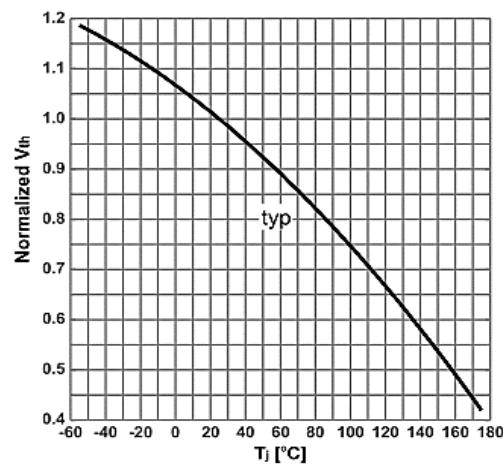


Figure 6. Typ. output characteristics(Tj 125°C)

4. Typical Characteristics (cont.)

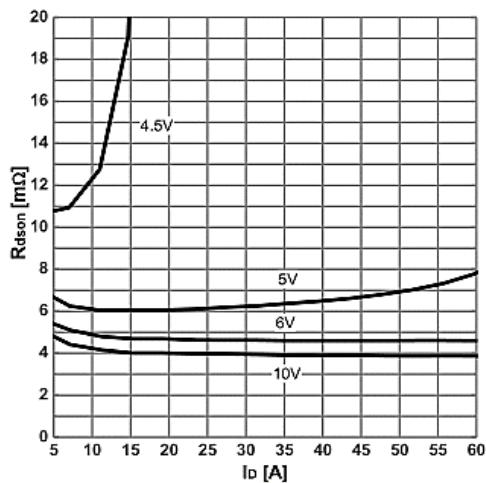


Figure 7. On-state resistance vs. Drain current

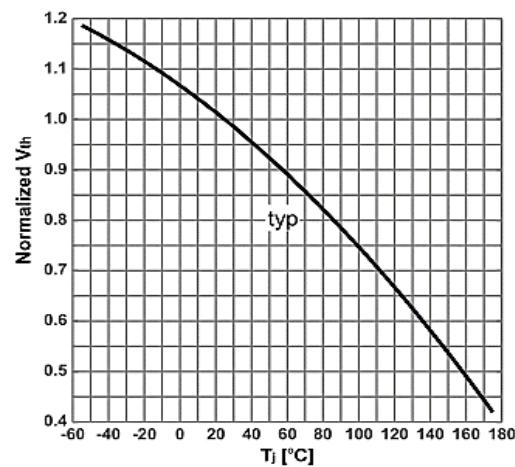


Figure 6. Gate threshold voltage vs. Junction Temperature

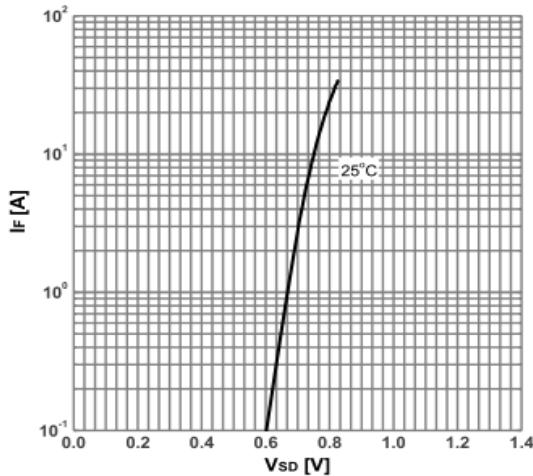


Figure 9. Forward characteristics of reverse diode

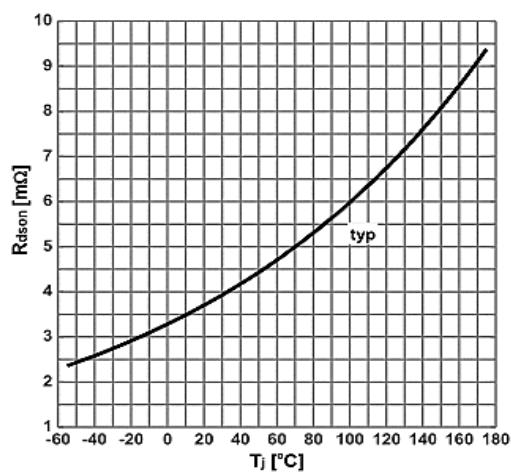


Figure 8. On-state resistance vs. Junctiontemperature

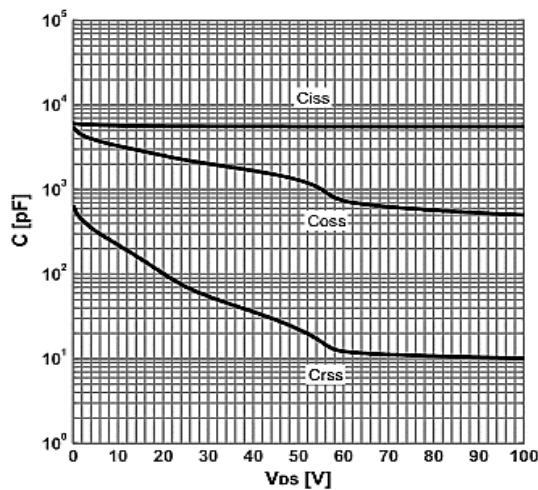


Figure9 Typ. capacitances

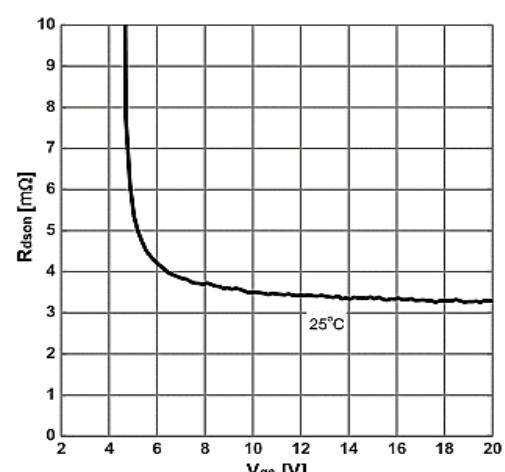


Figure11. On-state resistance vs. Vgs characteristics

4. Typical Characteristics (cont.)

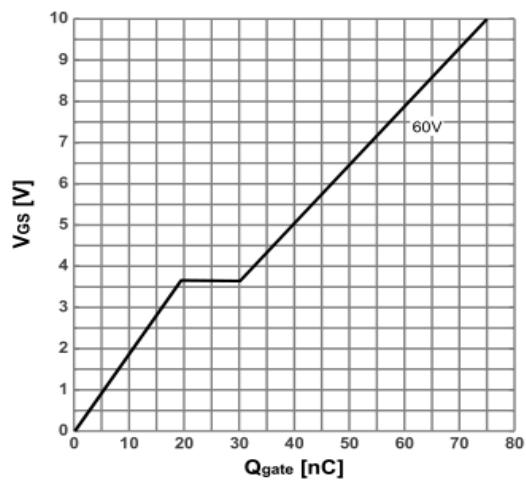
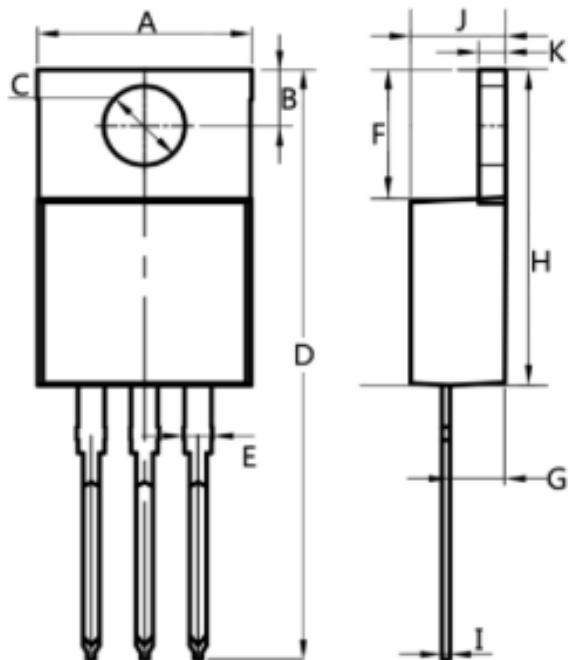


Figure 13: Typ. gate charge

5. Package Mechanical Data

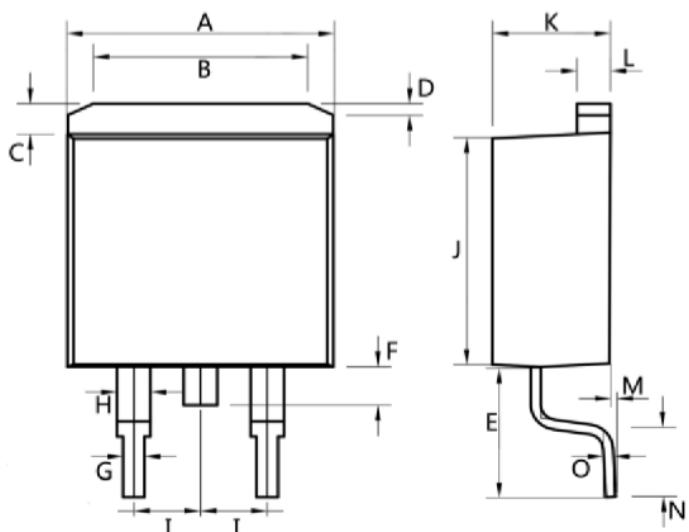
TO-220 Package



Dim.	Min.	Max.
A	10.0	10.4
B	2.5	3.0
C	3.5	4.0
D	28.0	30.0
E	1.1	1.5
F	6.2	6.6
G	2.9	3.3
H	15.0	16.0
I	0.35	0.45
J	4.3	4.7
K	1.2	1.4

All Dimensions in millimeter

TO-263 Package



Dim.	Min.	Max.
A	10.0	10.5
B	7.25	7.75
C	1.3	1.5
D	0.55	0.75
E	5.0	6.0
F	1.4	1.6
G	0.75	0.95
H	1.15	1.35
I	Typ 2.54	
J	8.4	8.6
K	4.4	4.6
L	1.25	1.45
M	0.02	0.1
N	2.4	2.8
O	0.35	0.45

All Dimensions in millimeter