

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Surface-mounted package
- Advanced trench cell design
- Extremely low threshold voltage
- ESD protected

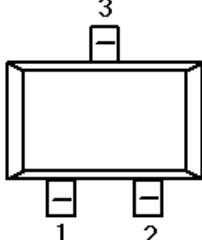
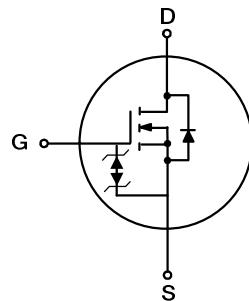
1.2 Applications

- Portable appliances

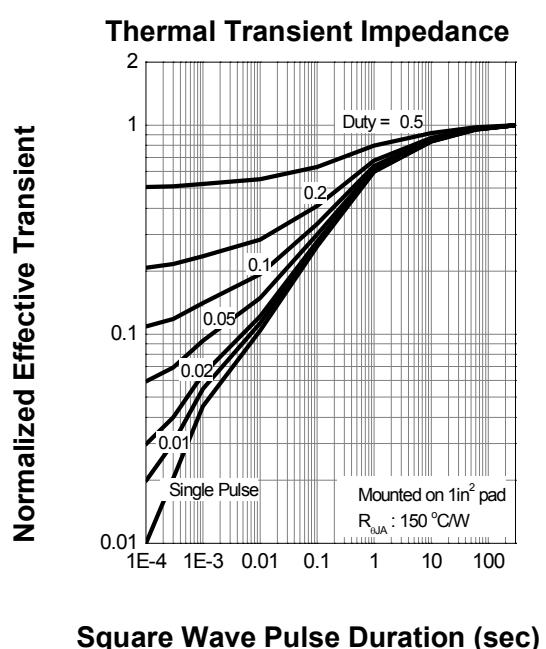
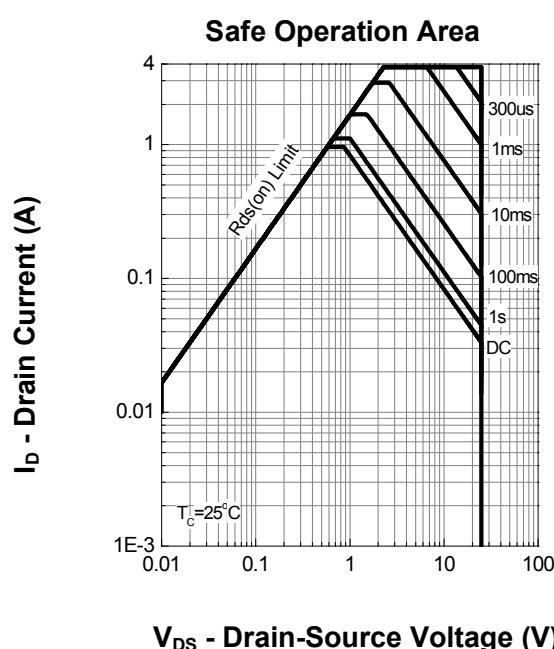
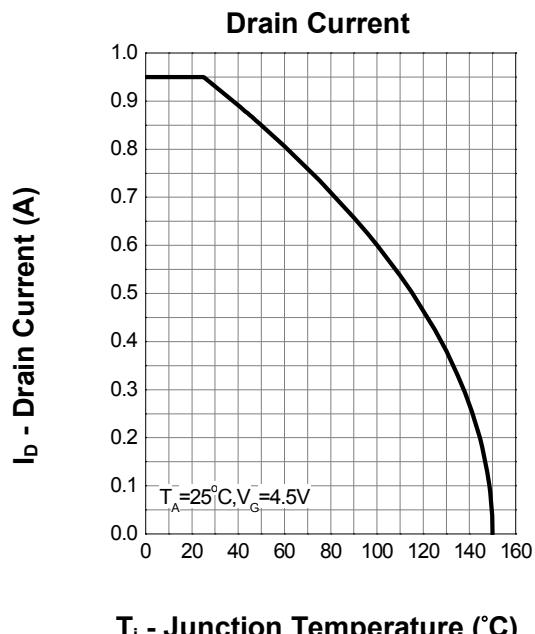
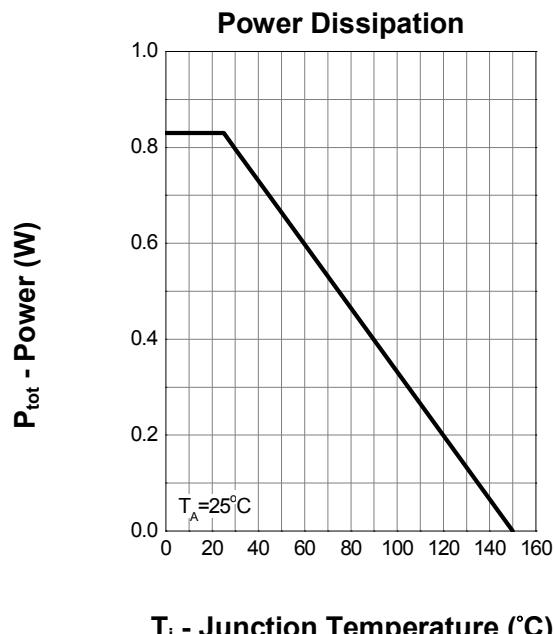
1.3 Quick reference

- $BV \geq 30 \text{ V}$
- $P_{\text{tot}} \leq 0.27 \text{ W}$
- $I_D \leq 0.8 \text{ A}$
- $R_{DS(ON)} \leq 0.8 \Omega @ V_{GS} = 4.5 \text{ V}$
- $R_{DS(ON)} \leq 0.9 \Omega @ V_{GS} = 2.5 \text{ V}$
- $R_{DS(ON)} \leq 1.3 \Omega @ V_{GS} = 1.8 \text{ V}$
- $R_{DS(ON)} \leq 1.8 \Omega @ V_{GS} = 1.5 \text{ V}$
- $R_{DS(ON)} \leq 3.0 \Omega @ V_{GS} = 1.2 \text{ V}$

2. Pin Description

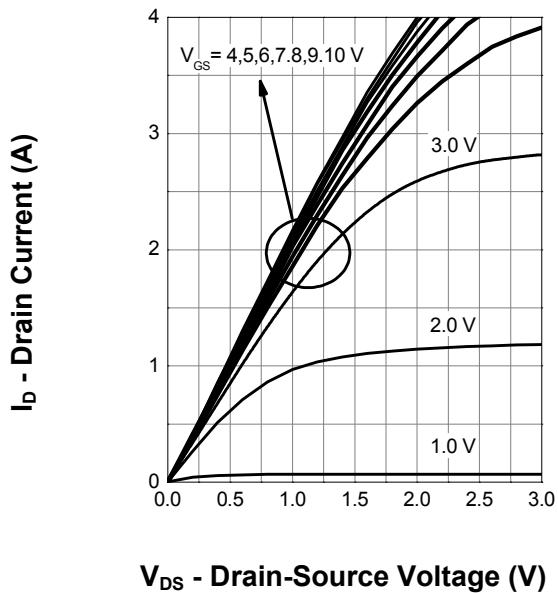
Pin	Description	Simplified Outline	Symbol
1	Gate(G)		
2	Source(S)		
3	Drain(D)	 Top View SOT-723	

7. Typical Characteristics

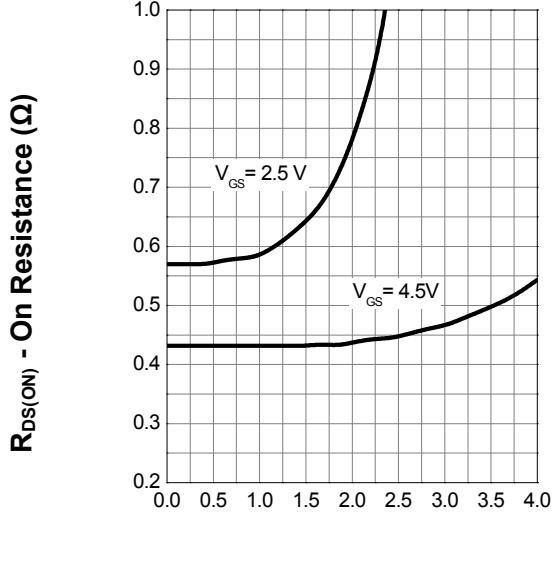


7. Typical Characteristics (cont.)

Output Characteristics



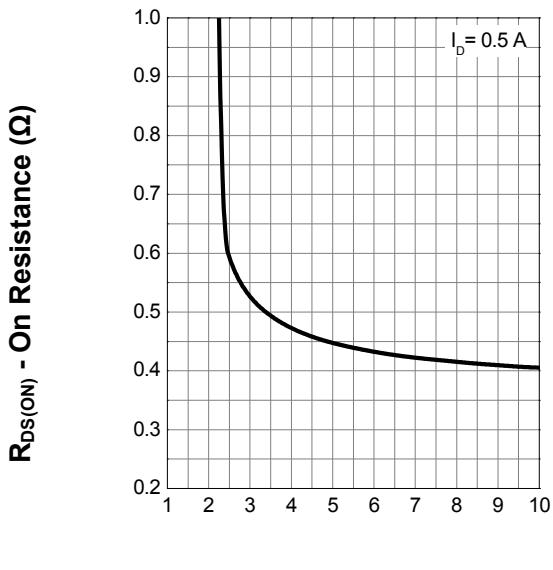
Drain-Source On Resistance



V_{DS} - Drain-Source Voltage (V)

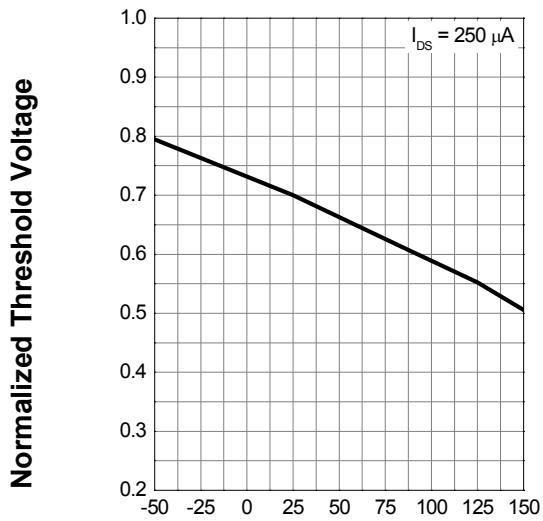
I_D - Drain Current (A)

Transfer Characteristics



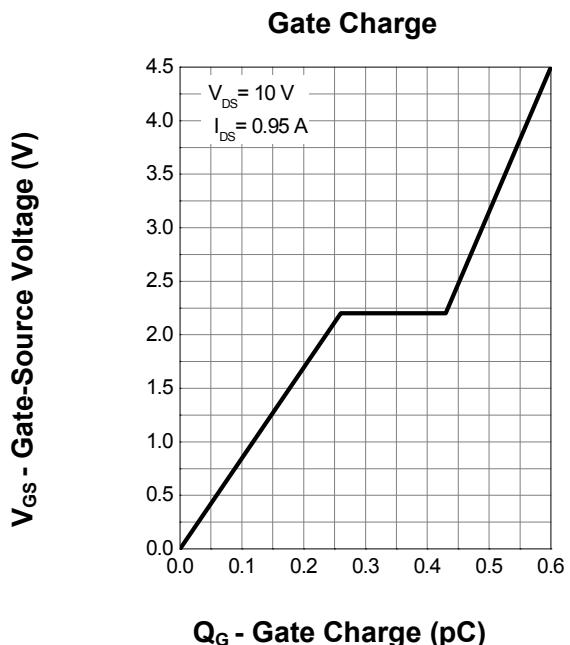
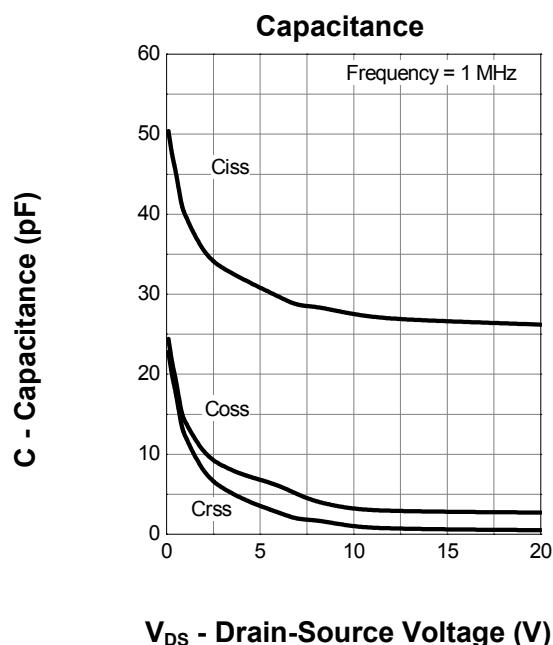
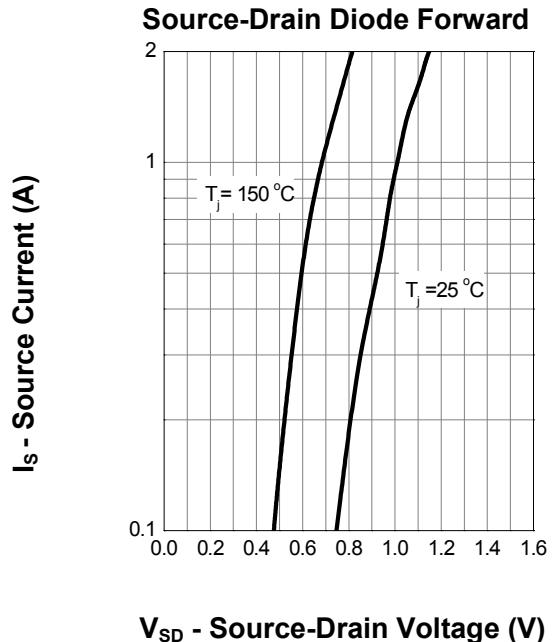
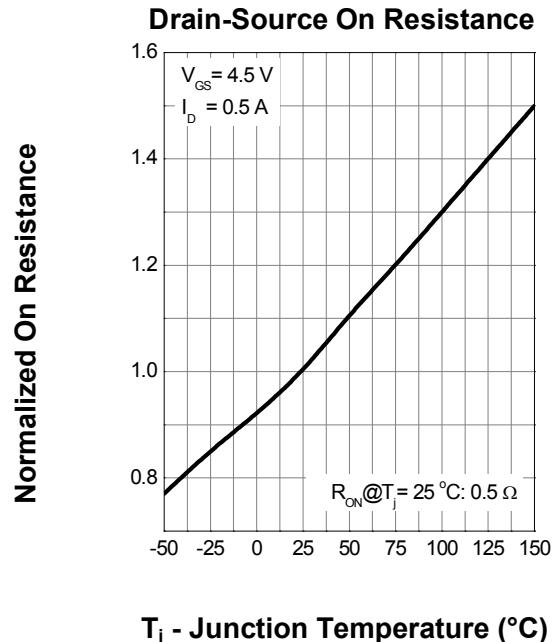
V_{GS} - Gate-Source Voltage (V)

Gate Threshold Voltage



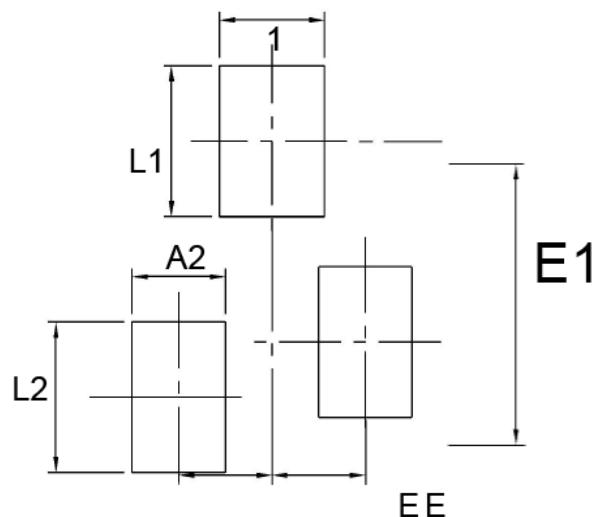
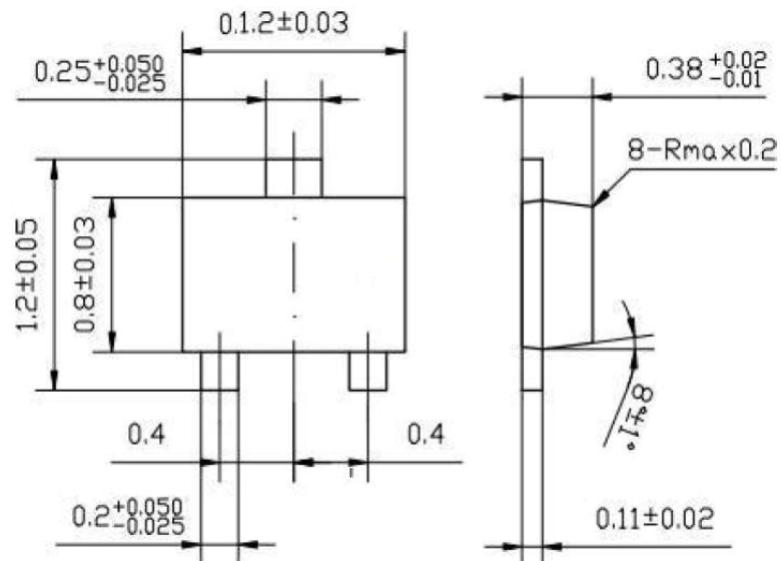
T_J - Junction Temperature (°C)

7. Typical Characteristics (cont.)



8. Package Dimensions

SOT-723 Package



Symbol	A1	A2	L1	L2	E1	E
Dimensions	0.45mm	0.40mm	0.63mm	0.63mm	1.17mm	0.40mm