

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ✓ Advanced trench cell design
- ✓ Low Thermal Resistance
- ✓ Super Trench

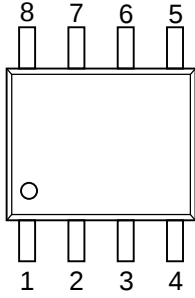
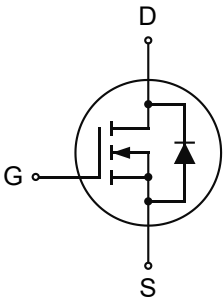
1.2 Applications

- ✓ Motor drivers
- ✓ DC-DC Converter

1.3 Quick reference

- ✓ $BV \geq 100V$
- ✓ $P_{tot} \leq 2W$
- ✓ $I_D \leq 12A$
- ✓ $R_{DS(ON)} \leq 12m\Omega @ V_{GS} = 10V$
- ✓ $R_{DS(ON)} \leq 18m\Omega @ V_{GS} = 4.5V$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source(S)	 Top View SOP8	
4	Gate(G)		
5,6,7,8	Drain(D)		

3. Limiting Values

Symbol	Parameter		Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage		$T_C=25^{\circ}\text{C}$	100	-	V
V_{GS}	Gate-Source Voltage		$T_C=25^{\circ}\text{C}$	-	± 20	V
I_{D^*}	Drain Current (DC)		$T_C=25^{\circ}\text{C}, V_{GS}=10\text{V}$	-	12	A
I_{D^*}	Drain Current (DC)		$T_C=100^{\circ}\text{C}, V_{GS}=10\text{V}$	-	9	A
I_{DM}^{***}	Drain Current (Pulsed)		$T_C=25^{\circ}\text{C}, V_{GS}=10\text{V}$	-	40	A
P_{tot}^*	Total Power Dissipation		$T_C=25^{\circ}\text{C}$	-	2	W
T_{stg}	Storage Temperature			- 55	150	$^{\circ}\text{C}$
T_J	Junction Temperature			-	150	$^{\circ}\text{C}$
I_S	Diode Forward Current		$T_C=25^{\circ}\text{C}$	-	12	A
I_{AS}	Avalanche Current			-	20	A
E_{AS}^*	Single Pulsed Avalanche Energy		$V_{DD}=50\text{V}, L=1.0\text{mH}$	-	55	mJ
V_{spike}	V_{DS} Spike	10us	$V_{DD}=50\text{V}, L=0.1\text{mH}$	-	120	V
$R_{\theta JA}^*$	Thermal Resistance-Junction to Ambient			-	56	$^{\circ}\text{C/W}$

Notes:

- * Surface Mounted on 1 in² pad area, $t \leq 10$ sec
- ** Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- *** Limited by bonding wire

4. Marking Information

Product Name	Marking
KJ1210SM	1210M YWWXXX YWWXXX: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ1210SM	SOP8			3000	

Note: KUAJIEXIN defines “ Green ” as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	100	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1	-	3	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} =80V, V _{GS} =0V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^a	Drain-Source On-State Resistance	V _{GS} =10V, I _D =20A	-	10.5	12	mΩ
		V _{GS} =4.5V, I _D =10A	-	16	18	mΩ
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} =20A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{DS} =20A, V _{GS} =0V dI _{SD} /dt=100A/μs	-	42	-	ns
Q _{rr}	Reverse Recovery Charge		-	36	-	nC
Dynamic Characteristics ^b						
C _{iSS}	Input Capacitance	V _{GS} =0V, V _{DS} =50V, Frequency=1MHz	-	1187	-	pF
C _{oSS}	Output Capacitance		-	234	-	
C _{rSS}	Reverse Transfer Capacitance		-	41	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =50V, V _{GEN} =10V, R _G =3.9Ω, R _L =2.5Ω, I _{DS} =20A	-	7.1	-	ns
t _r	Turn-on Rise Time		-	22	-	
t _{d(off)}	Turn-off Delay Time		-	20	-	
t _f	Turn-off Fall Time		-	23	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =50V, I _{DS} =30A	-	25	-	nC
Q _{gs}	Gate-Source Charge		-	6.1	-	
Q _{gd}	Gate-Drain Charge		-	6.5	-	

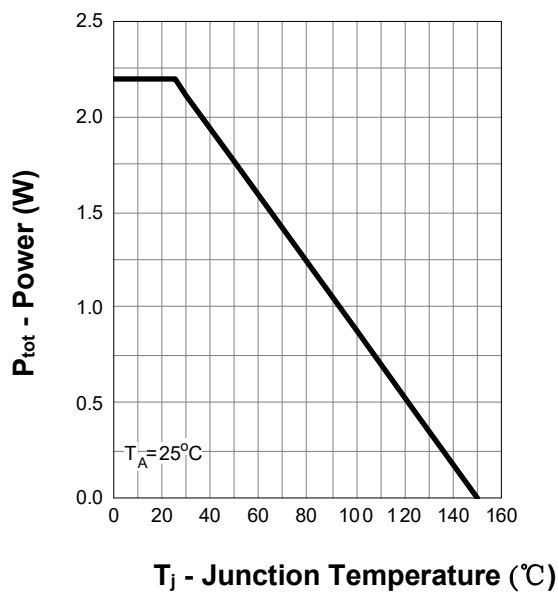
Notes:

a: Pulse test; pulse width ≤ 300μs, duty cycle ≤ 2%

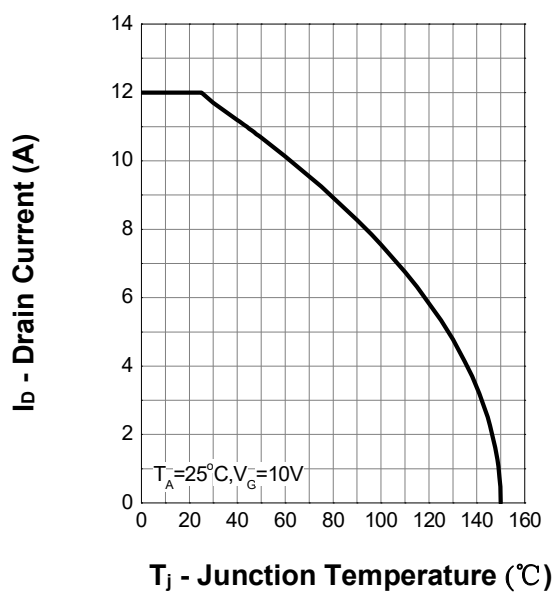
b: Guaranteed by design, not subject to production testing

7. Typical Characteristics

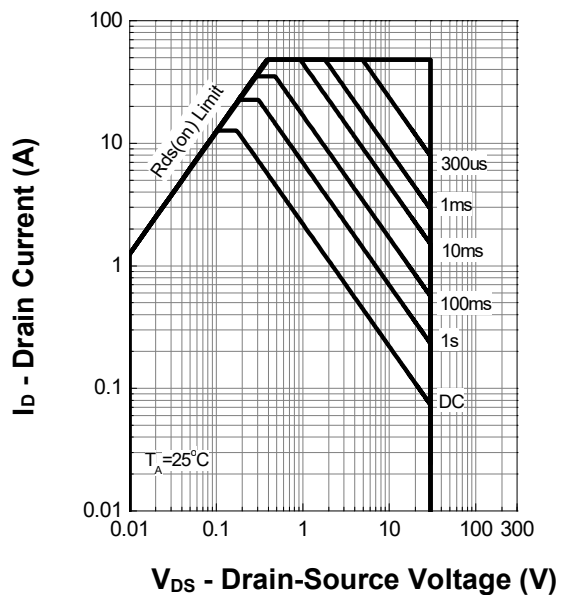
Power Dissipation



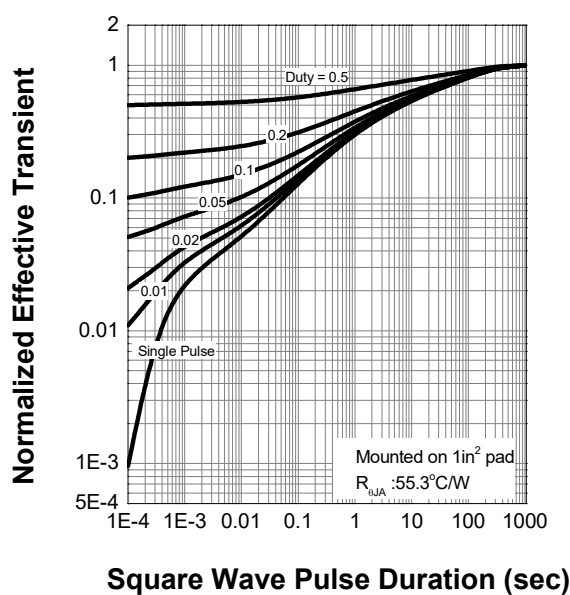
Drain Current



Safe Operation Area

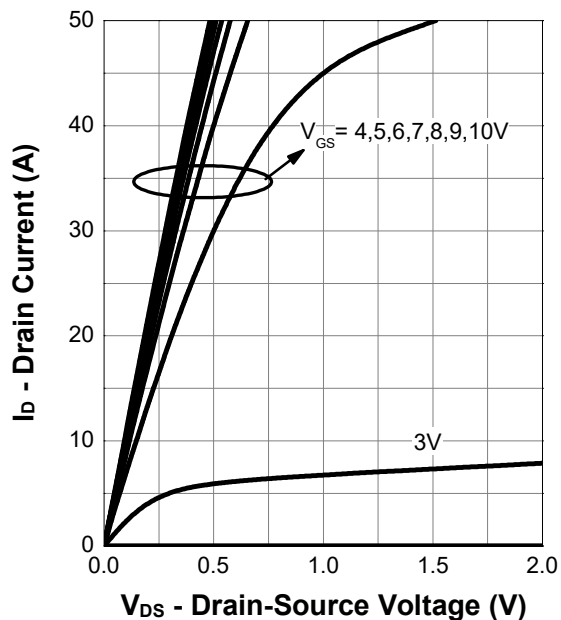


Thermal Transient Impedance

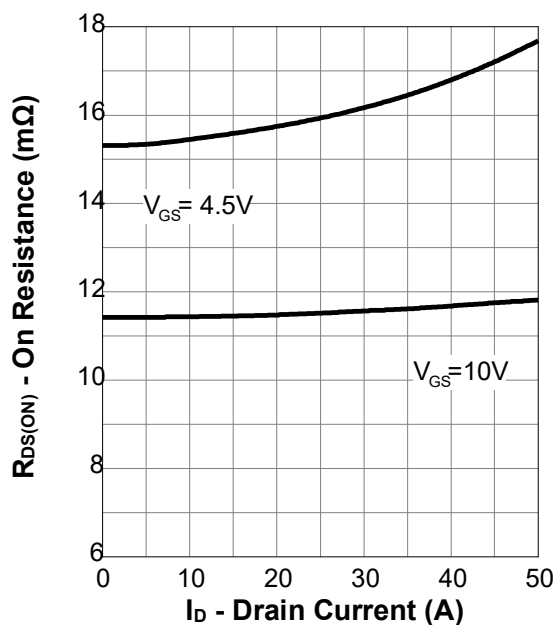


7. Typical Characteristics (cont.)

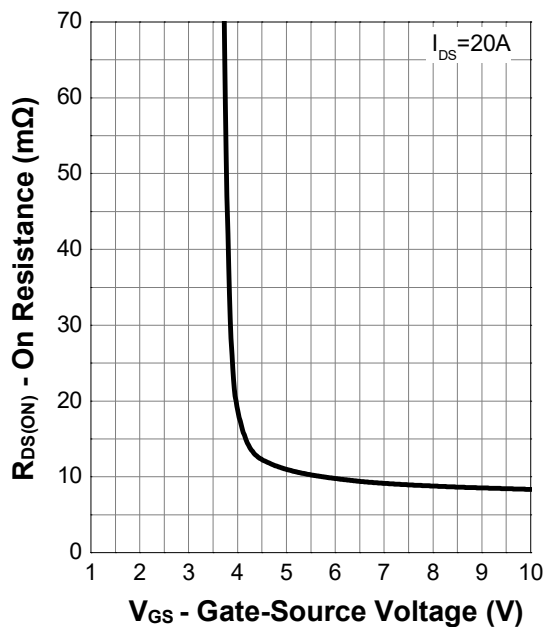
Output Characteristics



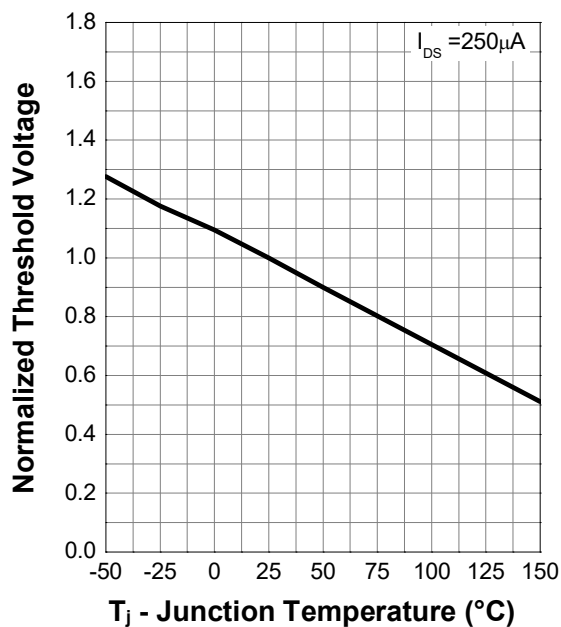
On Resistance



Transfer Characteristics

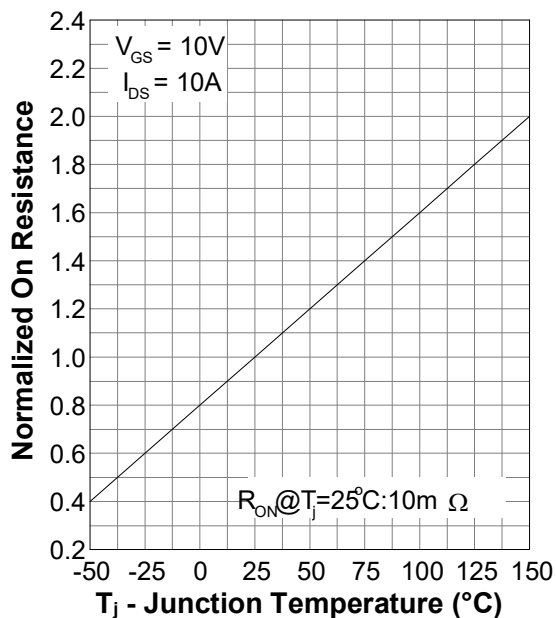


Normalized Threshold Voltage

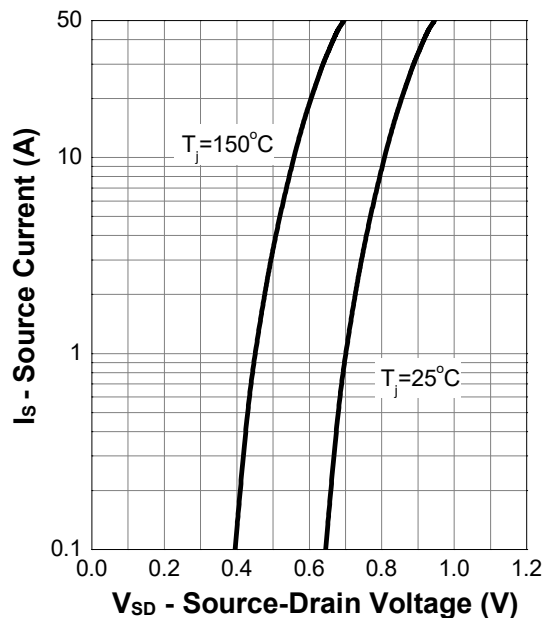


7. Typical Characteristics (cont.)

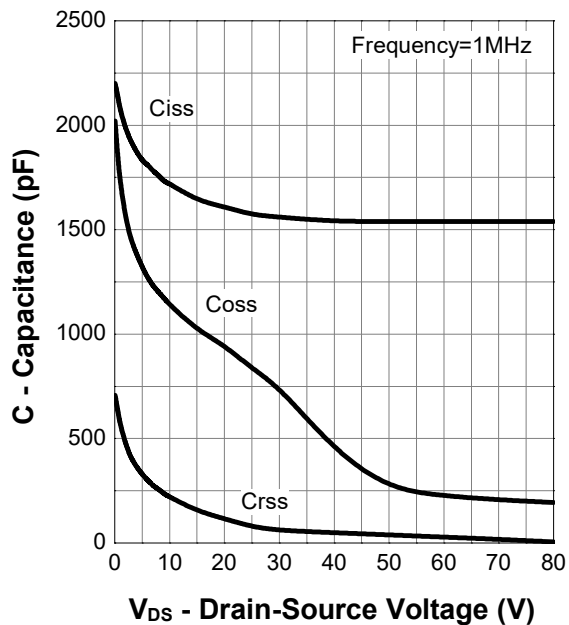
Normalized On Resistance



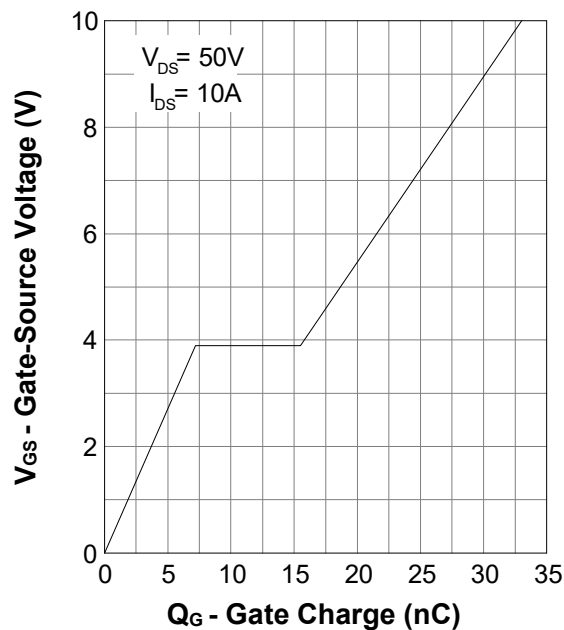
Diode Forward Current



Capacitance



Gate Charge



8. Package Dimensions

PDFN5x6-8L Package

