

P-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Advanced trench cell design
- ☒ Extremely low threshold voltage

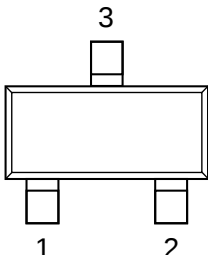
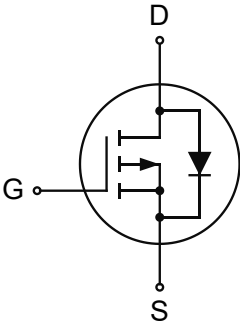
1.2 Applications

- ☒ Portable appliances
- ☒ Load switch

1.3 Quick reference

- ☒ $BV \geq -20V$
- ☒ $P_{tot} \leq 1.1W$
- ☒ $I_D \leq -4.2 A$
- ☒ $R_{DS(ON)} \leq 50m\Omega @ V_{GS} = -4.5V$
- ☒ $R_{DS(ON)} \leq 60m\Omega @ V_{GS} = -2.5V$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate(G)	 Top View SOT23	
2	Source(S)		
3	Drain(D)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_A=25^{\circ}\text{C}$	-20	-	V
V_{GS}	Gate-Source Voltage	$T_A=25^{\circ}\text{C}$	-	± 12	V
I_D^*	Drain Current	$T_A=25^{\circ}\text{C}$, $V_{GS}=4.5\text{V}$	-	-4.2	A
$I_{DM}^{*,**}$	Pulsed Drain Current	$T_A=25^{\circ}\text{C}$, $V_{GS}=4.5\text{V}$	-	-14	A
P_{tot}^*	Total Power Dissipation	$T_A=25^{\circ}\text{C}$	-	1.7	W
		$T_A=100^{\circ}\text{C}$	-	1.1	
T_{stg}	Storage Temperature		- 55	150	$^{\circ}\text{C}$
T_J	Junction Temperature		-	150	$^{\circ}\text{C}$
I_S^*	Diode Forward Current	$T_A=25^{\circ}\text{C}$	-	-2.2	A
$R_{\theta JA}^*$	Thermal Resistance-Junction to Ambient		-	150	$^{\circ}\text{C/W}$

Notes:

* Surface Mounted on 1 in² pad area, $t \leq 10$ sec

** Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$

4. Marking Information

Product Name	Marking
KJ2305S	2305

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ2305S	SOT23			3000	

Note: KUAJIEXIN defines “Green” as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics (T_A=25°C Unless Otherwise Noted)

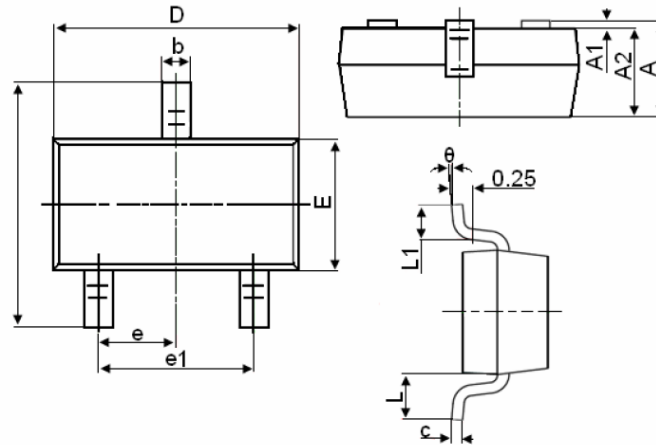
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	-20	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	-0.5	-0.6	-1.0	V
I _{DSS}	Drain Leakage Current	V _{DS} =-20V, V _{GS} =0V	-	-	-1	μA
		T _J = 85°C	-	-	-30	μA
I _{GSS}	Gate Leakage Current	V _{GS} =±12V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} =-4.5V, I _{DS} =-4.2A	-	38	50	mΩ
		V _{GS} =-2.5V, I _{DS} =-3A	-	46	60	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} =-1.25A, V _{GS} =0V	-	0.81	1.2	V
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-10V Frequency=1MHz	-	740	-	pF
C _{oss}	Output Capacitance		-	290	-	
C _{rss}	Reverse Transfer Capacitance		-	190	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =-10V, V _{GEN} =4.5V, R _G =60Ω, R _L =10Ω, I _{DS} =-2.8A	-	12.5	-	ns
t _r	Turn-on Rise Time		-	35	-	
t _{d(off)}	Turn-off Delay Time		-	30	-	
t _f	Turn-off Fall Time		-	10	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} =-10V, V _{GS} =-4.5V, I _{DS} =-2A	-	6.1	-	nC
Q _{gs}	Gate-Source Charge		-	1.7	-	
Q _{gd}	Gate-Drain Charge		-	1.2	-	

Notes:

- a. Pulse test; pulse width ≤ 300μs, duty cycle ≤ 2%
- b. Guaranteed by design, not subject to production testing

7. Package Dimensions

SOT23 Package



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°