

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Advanced trench cell design
- ☒ Super Trench

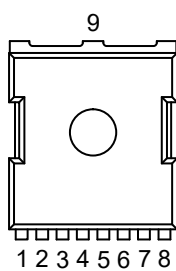
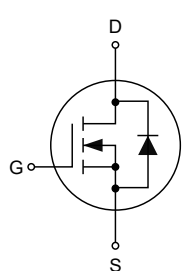
1.2 Applications

- ☒ BMS system
- ☒ High power inverter system
- ☒ LCDM appliances

1.3 Quick reference

- ☒ $BV \geq 85V$
- ☒ $R_{DS(ON)} \leq 4.2m\Omega @ V_{GS} = 10V$
- ☒ $P_{tot} \leq 156W$
- ☒ $I_D \leq 150A$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate(G)	 Top View TOLL-8L	
2,3,4,5,6,7,8	Source(S)		
9	Drain(D)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C=25^{\circ}\text{C}$	85	-	V
V_{GS}	Gate-Source Voltage	$T_C=25^{\circ}\text{C}$	-	± 20	V
I_D^{***}	Drain Current (DC)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{V}$	-	150	A
		$T_C=100^{\circ}\text{C}, V_{GS}=10\text{V}$	-	102	A
$I_{DM}^{*,***}$	Drain Current (Pulsed)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{V}$	-	480	A
P_{tot}^*	Drain power dissipation	$T_C=25^{\circ}\text{C}$	-	156	W
T_{stg}	Storage Temperature		- 55	150	$^{\circ}\text{C}$
T_J	Junction Temperature		-	150	$^{\circ}\text{C}$
I_S	Continuous-Source Current	$T_C=25^{\circ}\text{C}$	-	150	A
E_{AS}^*	Single Pulsed Avalanche Energy	$I_{AS}=30\text{A}, L=0.5\text{mH}, T_J=25^{\circ}\text{C}$	-	266	mJ
$R_{\theta JA}^*$	Thermal Resistance-Junction to Ambient		-	62.5	$^{\circ}\text{C/W}$
$R_{\theta JC}^*$	Thermal Resistance-Junction to Case		-	0.72	

Notes:

- * Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- ** Mounted on Large Heat Sink
- *** Limited by bonding wire

4. Marking Information

Product Name	Marking
KJ04N08T	

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ04N08T	TOLL-8L	-	-	2000	

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C)

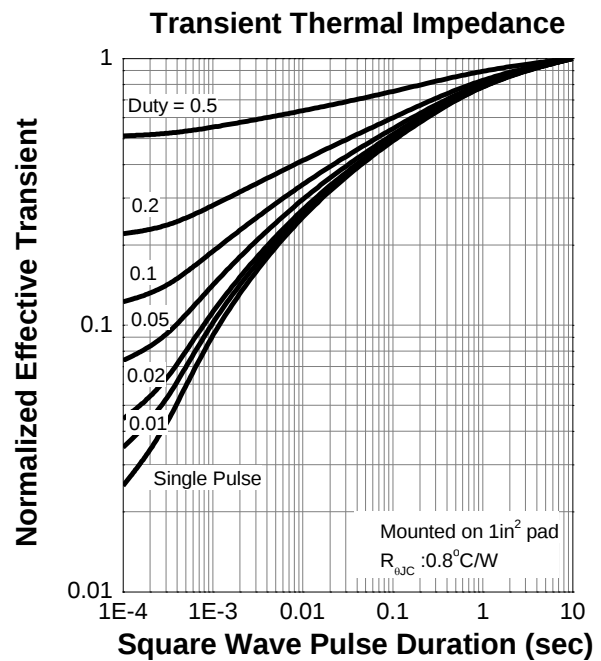
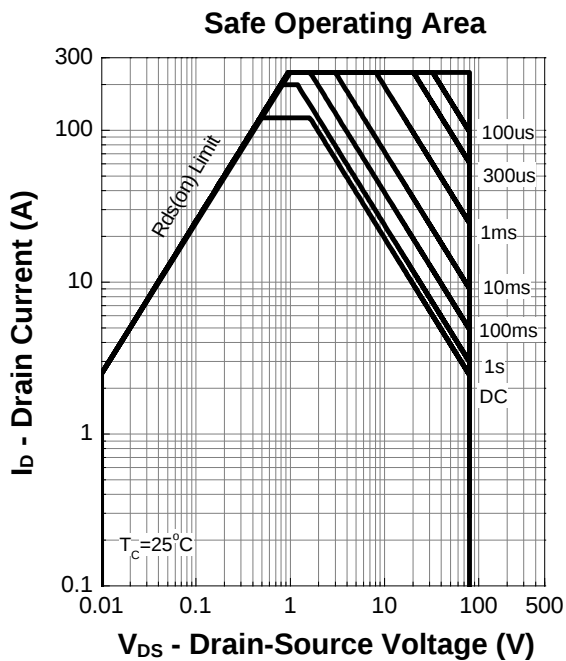
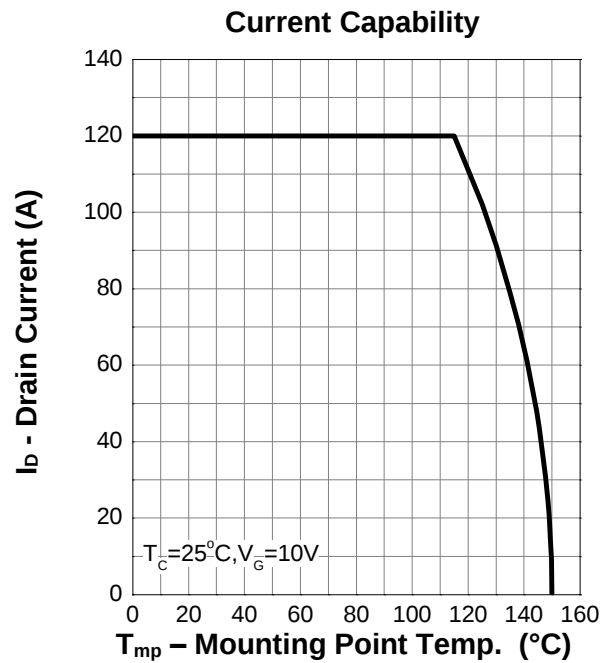
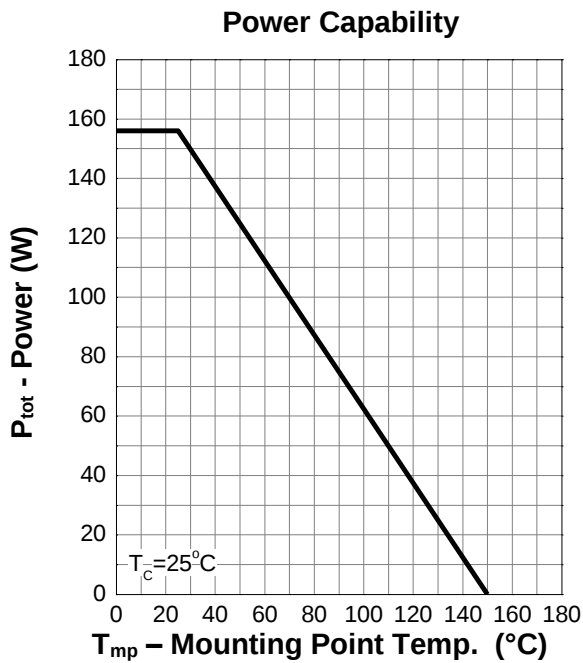
6. Electrical Characteristics (T_A=25°C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	85	95	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2.0	-	4.0	V
I _{DSS}	Drain Leakage Current	V _{DS} =80V, V _{GS} =0V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} =10V, I _{DS} =30A	-	3.6	4.2	mΩ
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{DS} =20A, V _{GS} =0V	-	-	1.2	V
t _{rr}	Reverse Recovery Time	I _{DS} =20A, V _{GS} =0V, dI _{SD} /dt=100A/μs	-	60	-	ns
Q _{rr}	Reverse Recovery Charge		-	156	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =40V Frequency=1MHz	-	4021	-	pF
C _{oss}	Output Capacitance		-	637	-	
C _{rss}	Reverse Transfer Capacitance		-	22	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =40V, V _{GEN} =10V, R _G =4.5Ω, R _L =2Ω, I _{DS} =20A	-	47	-	ns
t _r	Turn-on Rise Time		-	36	-	
t _{d(off)}	Turn-off Delay Time		-	43	-	
t _f	Turn-off Fall Time		-	20	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} =40V, V _{GS} =10V, I _{DS} =20A	-	80	-	nC
Q _{gs}	Gate-Source Charge		-	23	-	
Q _{gd}	Gate-Drain Charge		-	24	-	

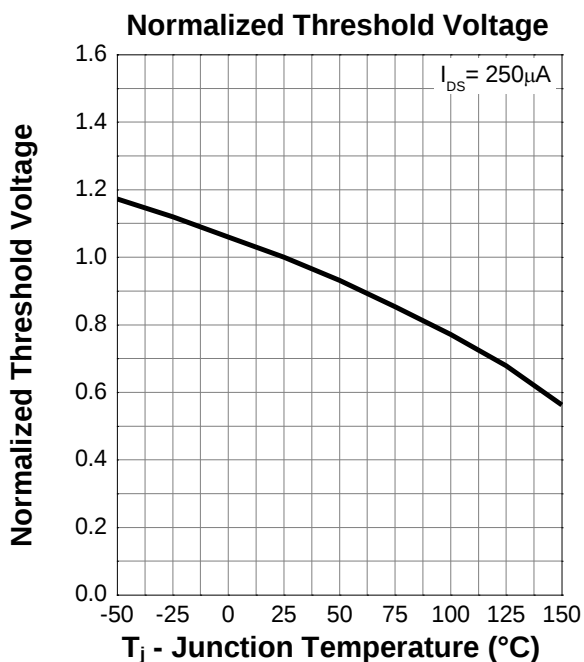
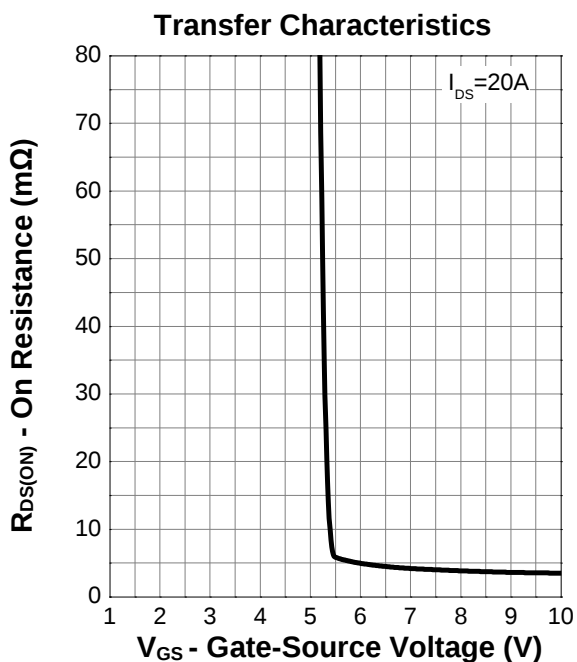
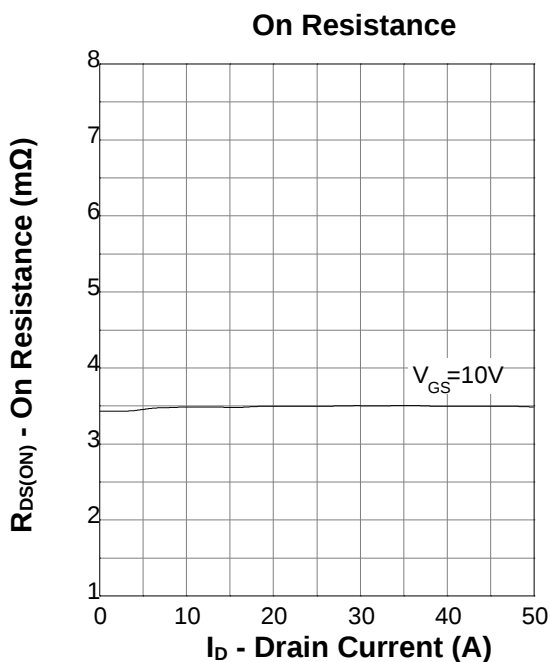
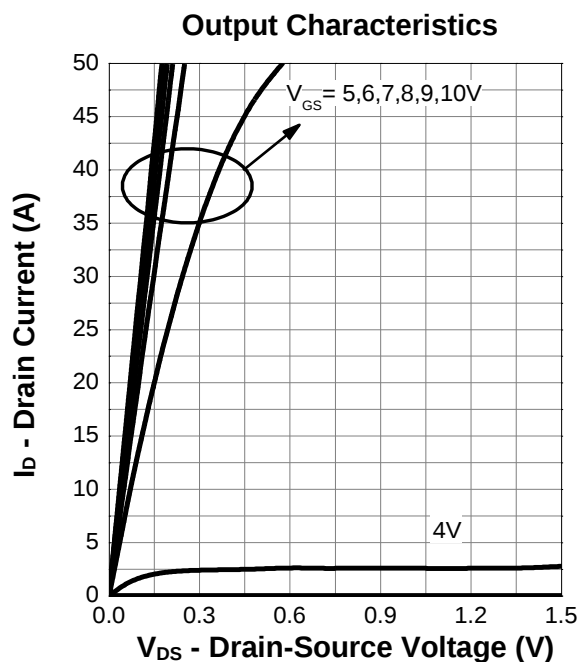
Notes:

- Pulse test; pulse width ≤ 300μs, duty cycle ≤ 2%
- Guaranteed by design, not subject to production testing

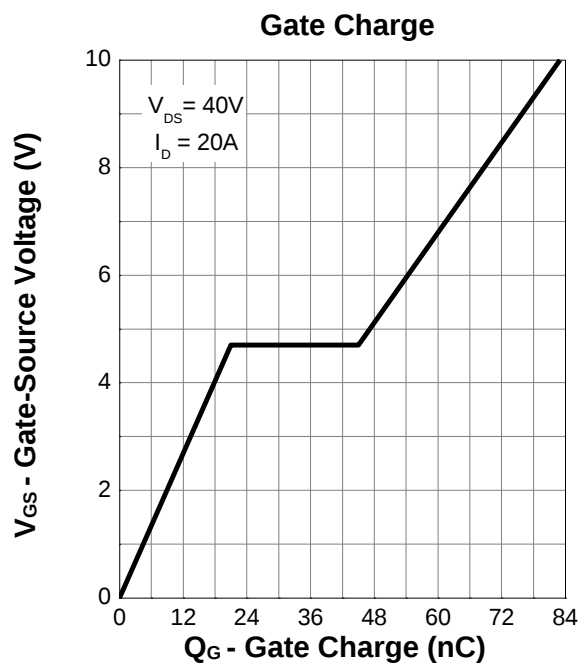
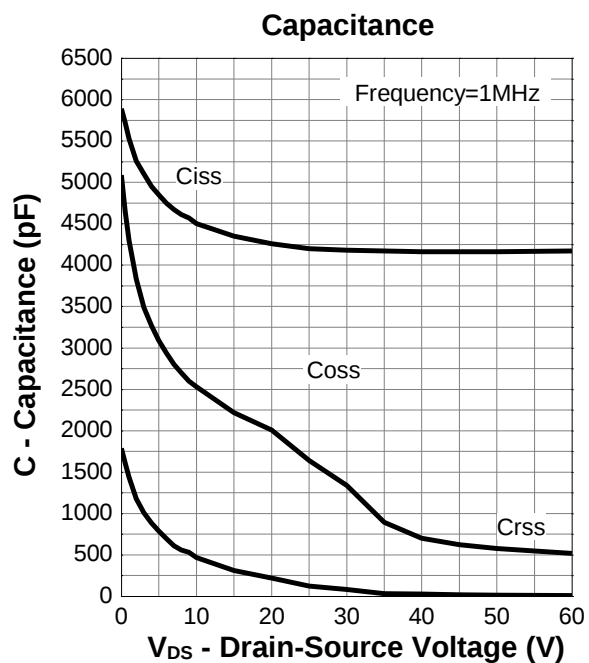
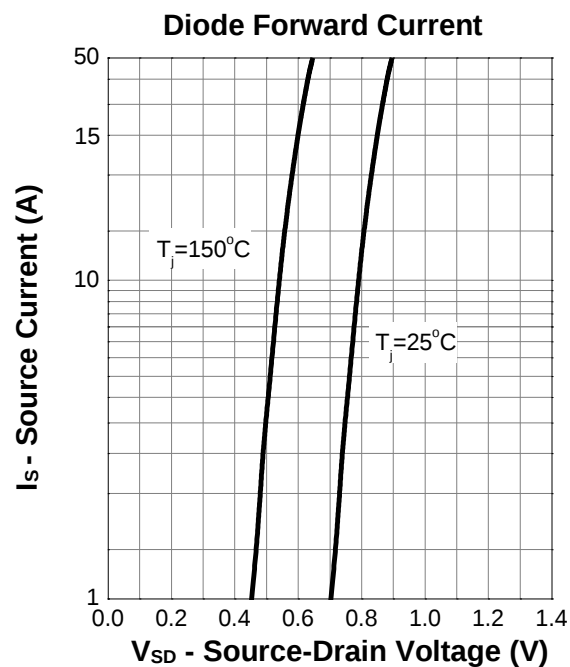
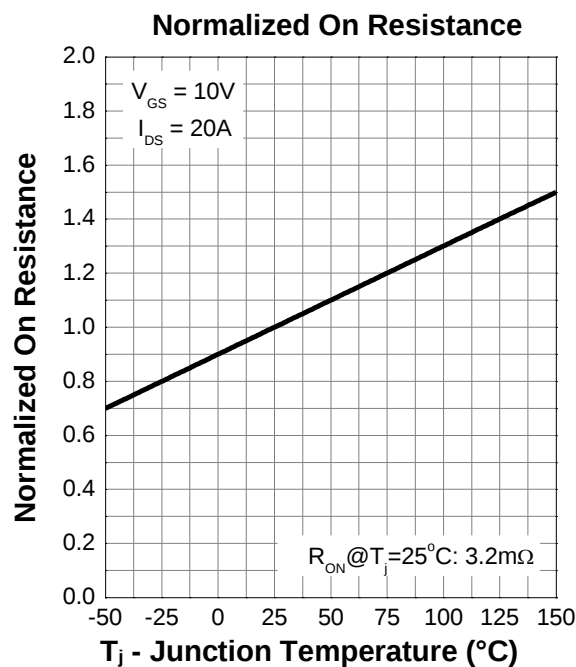
7. Typical Characteristics



7. Typical Characteristics (Cont.)

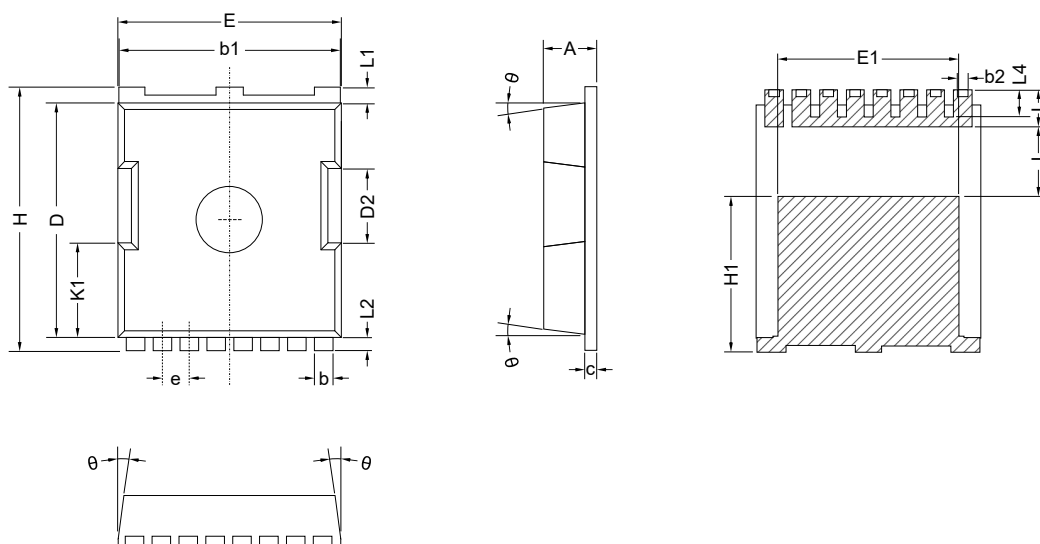


7. Typical Characteristics (Cont.)



8. Package Dimensions

TOLL-8L Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	2.20	2.40
b	0.90	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20BSC	
H	11.48	11.88
H1	6.75	7.15
N	8.00	
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
θ	4°	10°