

P-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Extremely low threshold voltage
- ☒ Advanced trench cell design

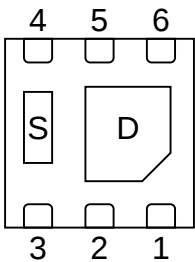
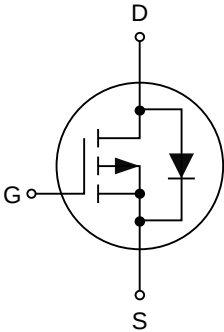
1.2 Applications

- ☒ Portable appliances

1.3 Quick reference

- ☒ $BV \geq -100V$
- ☒ $R_{DS(ON)} \leq 1\Omega @ V_{GS} = -10V$
- ☒ $P_{tot} \leq 2W$
- ☒ $R_{DS(ON)} \leq 1.45\Omega @ V_{GS} = -6V$
- ☒ $I_D \leq -1.5A$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,5,6	Drain(D)	 Bottom View DFN2x2-6L	
3	Gate(G)		
4	Source(S)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_A=25^{\circ}\text{C}$	-100	-	V
V_{GS}	Gate-Source Voltage	$T_A=25^{\circ}\text{C}$	-	± 20	V
I_D^*	Drain Current	$T_A=25^{\circ}\text{C}, V_{GS}=-10\text{V}$	-	-1.5	A
$I_{DM}^{*,**}$	Pulsed Drain Current	$T_A=25^{\circ}\text{C}, V_{GS}=-10\text{V}$	-	-0.8	A
P_{tot}^*	Total Power Dissipation	$T_A=25^{\circ}\text{C}$	-	2	W
		$T_A=100^{\circ}\text{C}$	-	1.2	
T_{stg}	Storage Temperature		-55	150	$^{\circ}\text{C}$
T_J	Junction Temperature		-	150	$^{\circ}\text{C}$
I_S^*	Diode Forward Current	$T_A=25^{\circ}\text{C}$	-	-1.5	A
$R_{\theta JA}^*$	Thermal Resistance-Junction to Ambient		-	90	$^{\circ}\text{C/W}$

Notes:

- * Surface Mounted on 1 in² pad area, $t \leq 10$ sec
- ** Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- *** Limited by bonding wire

4. Marking Information

Product Name	Marking
KJ1KP10N	1KP10 YWWXXX YWW: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ1KP10N	DFN2x2-6L			-	

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C)

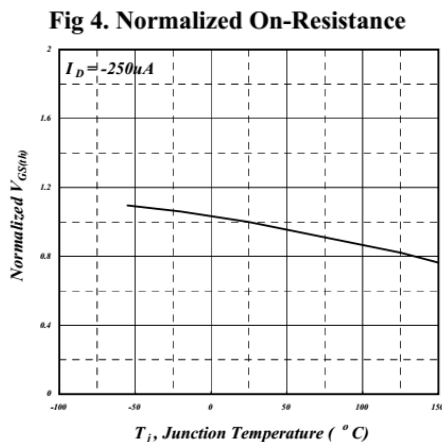
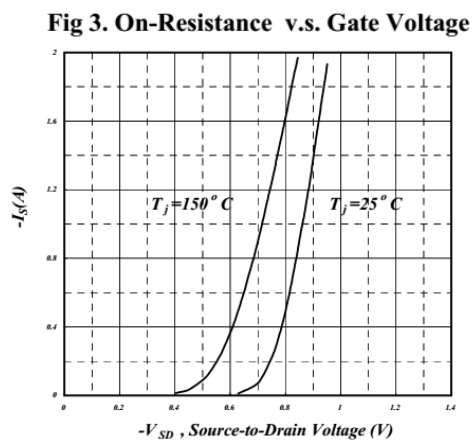
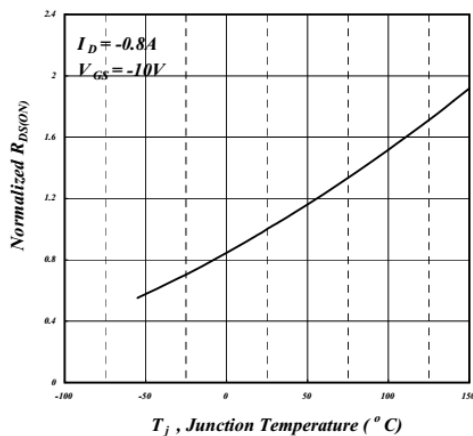
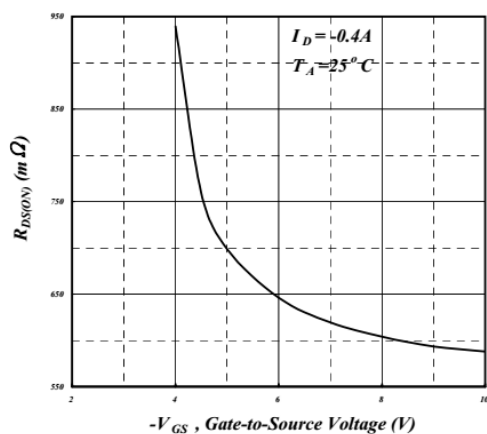
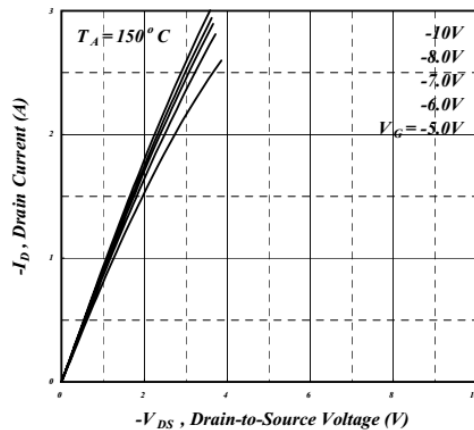
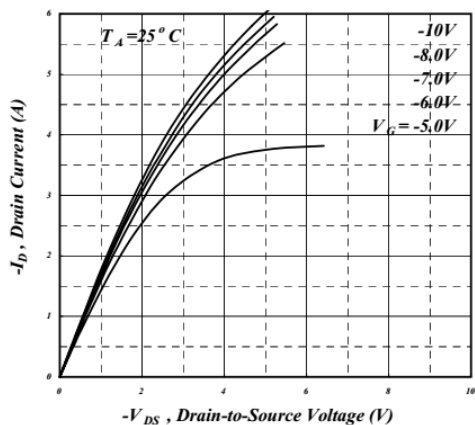
6. Electrical Characteristics (T_A=25°C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-100	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-2	-	-4	V
I _{DSS}	Drain Leakage Current	V _{DS} =-80V, V _{GS} =0V	-	-	-25	uA
		T _J =85°C	-	-	-30	μA
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} =-10V, I _{DS} =-0.8A	-	-	1	Ω
		V _{GS} =-6V, I _{DS} =-0.4A	-	-	1.45	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} =-0.8A, V _{GS} =0V	-	-	-1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} =-1A, dI _{SD} /dt=100A/μs	-	24	-	ns
Q _{rr}	Reverse Recovery Charge		-	27	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-50V, Frequency=1MHz	-	325	670	pF
C _{oss}	Output Capacitance		-	22	-	
C _{rss}	Reverse Transfer Capacitance		-	19	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =-50V, V _{GEN} =-10V, R _G =3.3Ω, I _{DS} =-1A	-	8	-	ns
t _r	Turn-on Rise Time		-	4	-	
t _{d(off)}	Turn-off Delay Time		-	14	-	
t _f	Turn-off Fall Time		-	3	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{GS} =-10V, V _{DS} =-50V, I _{DS} =-1A	-	7.8	-	nC
Q _{gs}	Gate-Source Charge		-	1.8	-	
Q _{gd}	Gate-Drain Charge		-	1.8	-	

Notes:

- Pulse test; pulse width ≤ 300μs, duty cycle ≤ 2%
- Guaranteed by design, not subject to production testing

7. Typical Characteristics



7. Typical Characteristics (cont.)

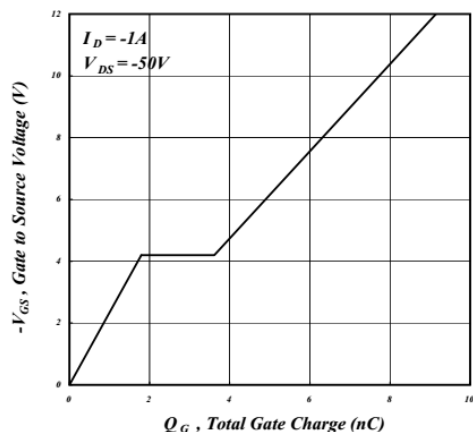


Fig 7. Gate Charge Characteristics

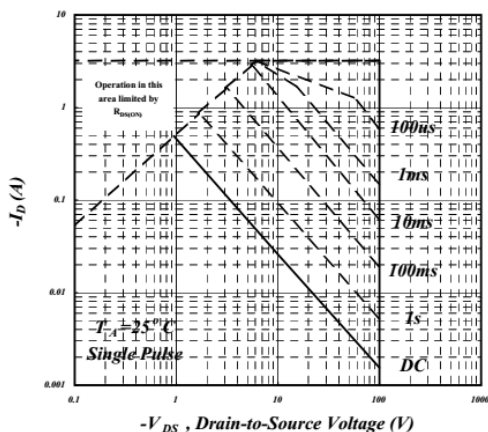


Fig 9. Maximum Safe Operating Area

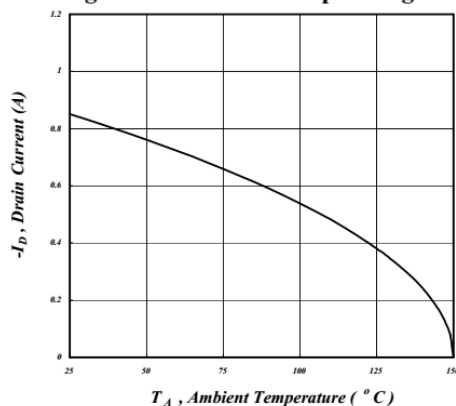


Fig 11. Drain Current v.s. Ambient Temperature

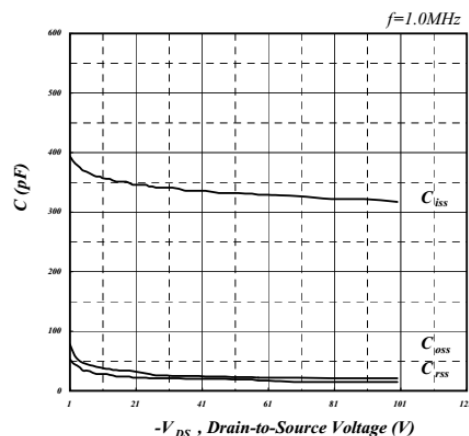


Fig 8. Typical Capacitance Characteristics

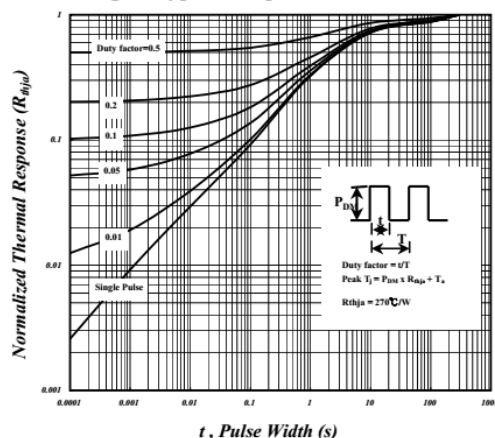


Fig 10. Effective Transient Thermal Impedance

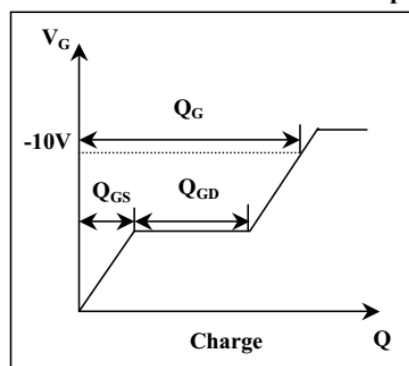


Fig 12. Gate Charge Waveform

7. Typical Characteristics (cont.)

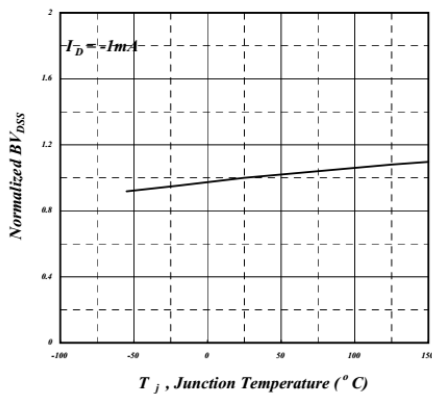


Fig 13. Normalized BV_{DSS} v.s. Junction Temperature

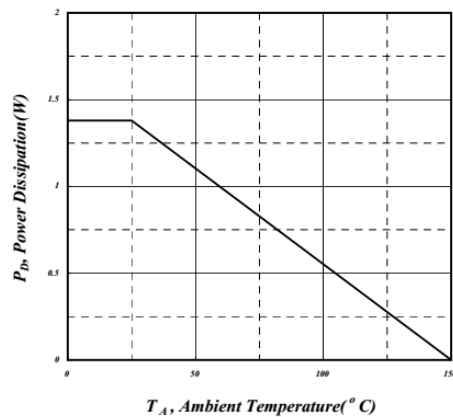


Fig 14. Total Power Dissipation

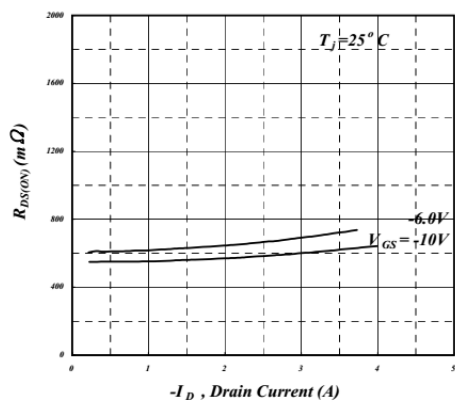
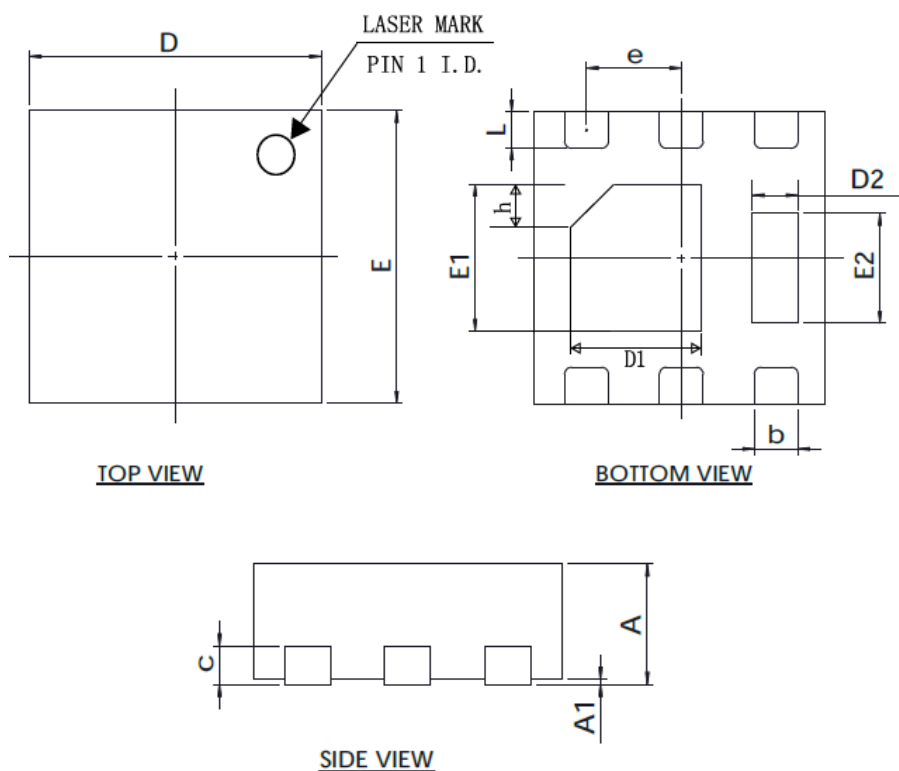


Fig 15. Typ. Drain-Source on State Resistance

8.Package Dimensions

DFN2x2-6L



Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	NA	0.02	0.05
b	0.20	0.27	0.34
c	0.18	0.20	0.25
D	1.95	2.00	2.07
E	1.95	2.00	2.07
D1	0.80	0.90	1.00
E1	0.90	1.00	1.10
D2	0.20	0.30	0.40
E2	0.65	0.75	0.85
L	0.20	0.25	0.35
h	0.20	0.25	0.30
e	0.65BSC		