

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Surface-mounted package
- Advanced trench cell design

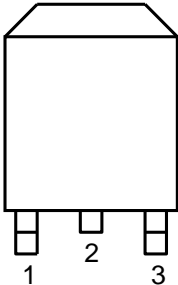
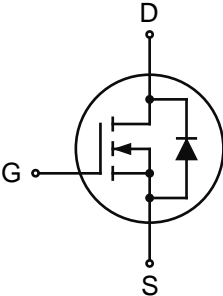
1.2 Applications

- LCD TV appliances
- High power inverter system
- LCDM appliances

1.3 Quick reference

- $BV \cong 100\text{ V}$
- $R_{DS(ON)} \leq 18\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- $P_{tot} \cong 50\text{ W}$
- $R_{DS(ON)} \leq 25\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$
- $I_D \cong 30\text{ A}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate(G)	 <p>Top View TO-252</p>	
2	Drain(D)		
3	Source(S)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C = 25 °C	100	-	V
V _{GS}	Gate-Source Voltage	T _C = 25 °C	-	±20	V
I _D *	Drain Current (DC)	T _C = 25 °C, V _{GS} = 10 V	-	30	A
I _D *	Drain Current (DC)	T _C = 100 °C, V _{GS} = 10 V	-	15	A
I _{DM} ***	Drain Current (Pulsed)	T _C = 25 °C, V _{GS} = 10 V	-	96	A
P _{tot}	Drain Power Dissipation	T _C = 25 °C	-	50	W
T _{stg}	Storage Temperature		-55	150	°C
T _J	Junction Temperature		-	150	°C
I _S	Continuous-Source Current	T _C = 25 °C	-	30	A
R _{θJA} **	Thermal Resistance-Junction to Ambient		-	37	°C/W
R _{θJC} **	Thermal Resistance-Junction to Case		-	1.2	

Notes:

- * Pulse width ≤ 300 μs, duty cycle ≤ 2 %
- ** Mounted on Large Heat Sink
- *** Limited by bonding wire

4. Marking Information

Product Name	Marking
KJ30N10KGS	<div style="display: inline-block; background-color: black; color: white; padding: 2px;">30N10S YWWWXX</div> YWWWX: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ30N10KGS	TO-252	-	-	2500	

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C)

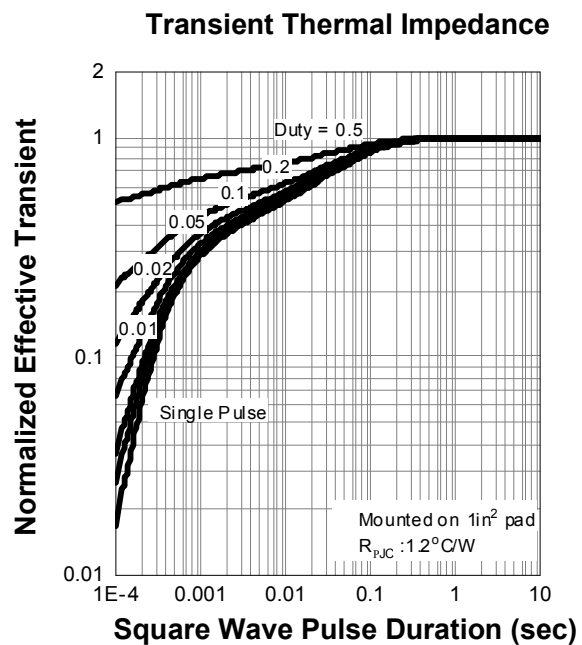
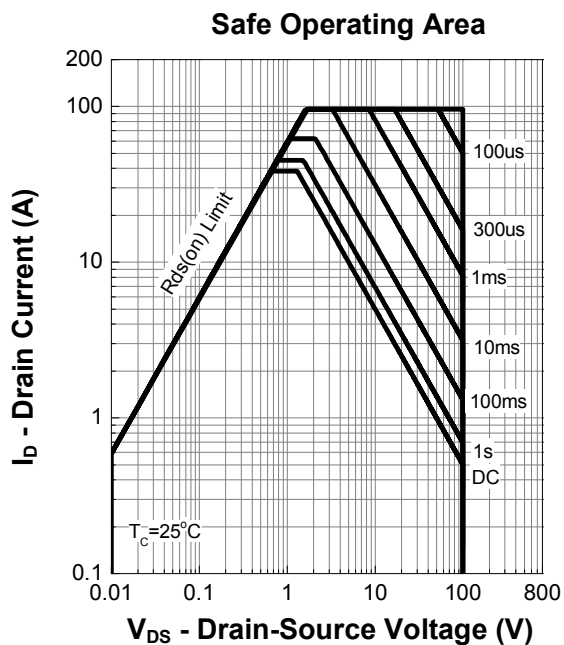
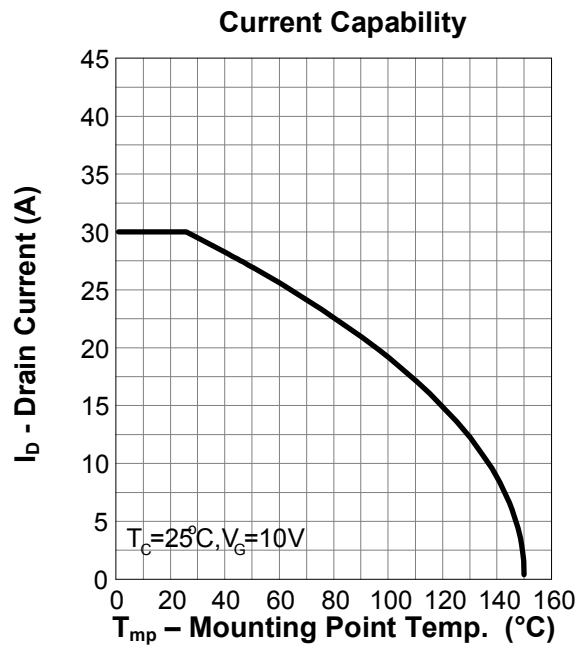
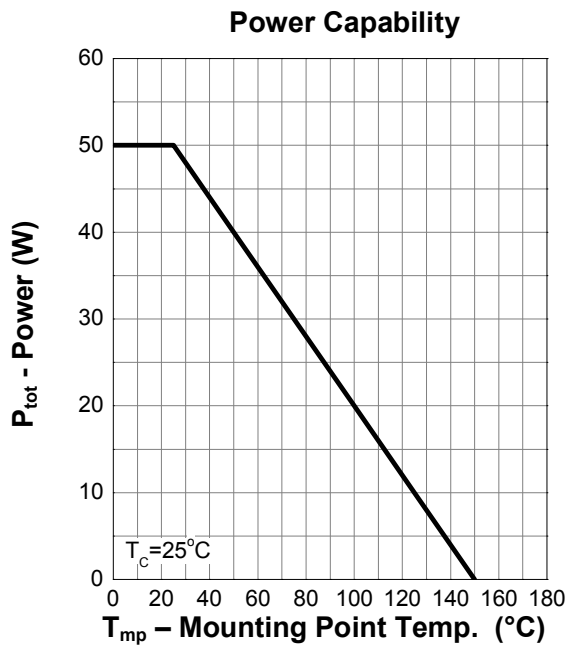
6. Electrical Characteristics (T_A=25°C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _{DS} = 250 μA	100	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = 250 μA	1.0	1.6	1.8	V
I _{DSS}	Drain Leakage Current	V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{GS} = 0 V, V _{GS} = ± 20 V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} = 10 V, I _{DS} = 20 A	-	15	18	mΩ
		V _{GS} = 4.5 V, I _{DS} = 10 A	-	21	25	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = 20 A, V _{GS} = 0 V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{DS} = 20 A, V _{GS} = 0 V, di _{SD} /dt = 100 A/μs	-	45	-	ns
Q _{rr}	Reverse Recovery Charge		-	59	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 50 V, Frequency = 1 MHz	-	822	-	pF
C _{oss}	Output Capacitance		-	310	-	
C _{rss}	Reverse Transfer Capacitance		-	25	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = 50 V, V _{GEN} = 10 V, R _G = 4.5 Ω, R _L = 2.5 Ω, I _{DS} = 20 A	-	15	-	ns
t _r	Turn-on Rise Time		-	3.2	-	
t _{d(off)}	Turn-off Delay Time		-	30	-	
t _f	Turn-off Fall Time		-	7.6	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} = 50 V, V _{GS} = 10 V, I _{DS} = 20 A	-	22	-	nC
Q _{gs}	Gate-Source Charge		-	6.2	-	
Q _{gd}	Gate-Drain Charge		-	5.3	-	

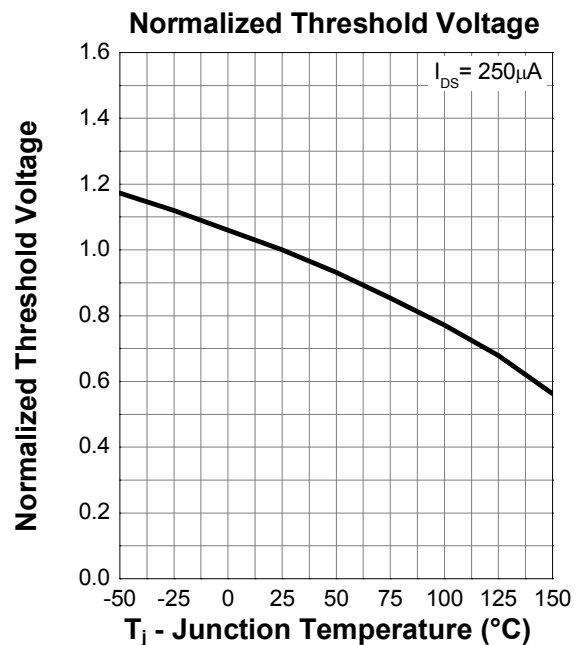
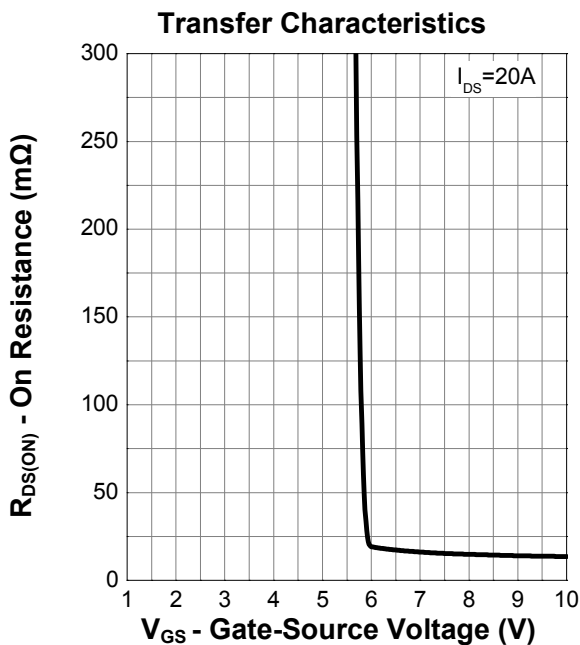
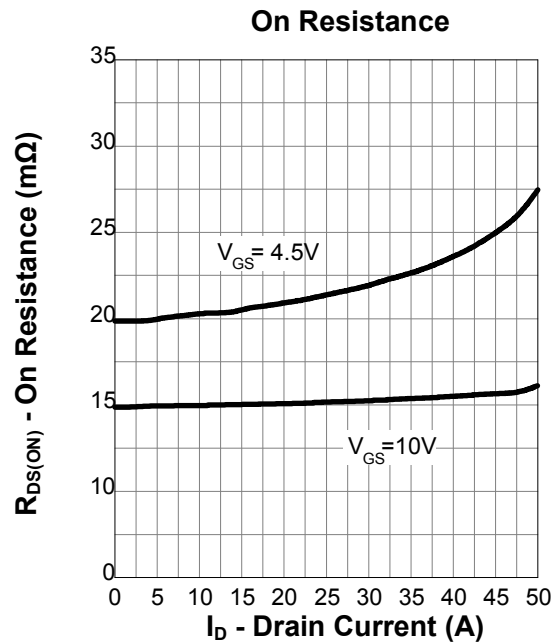
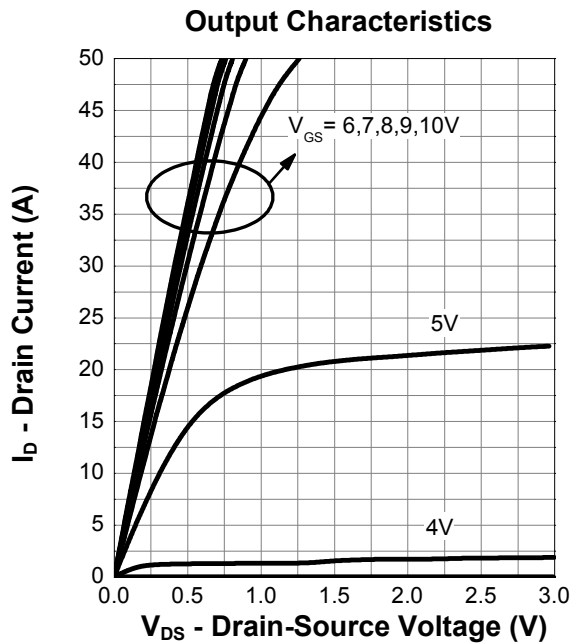
Notes:

- Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2%
- Guaranteed by design, not subject to production testing

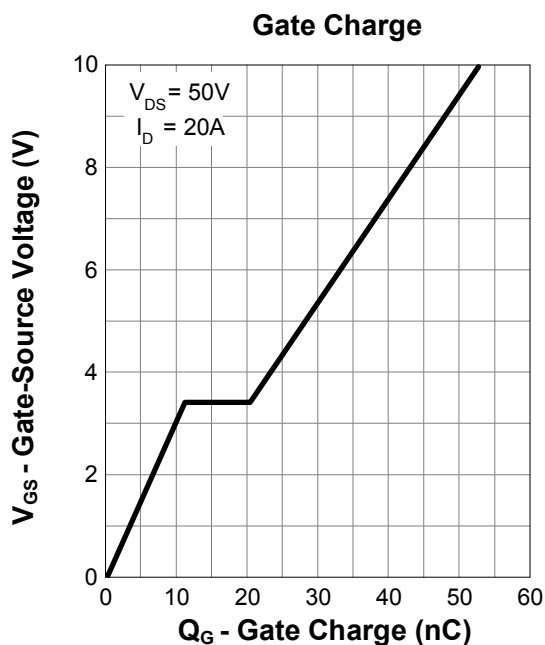
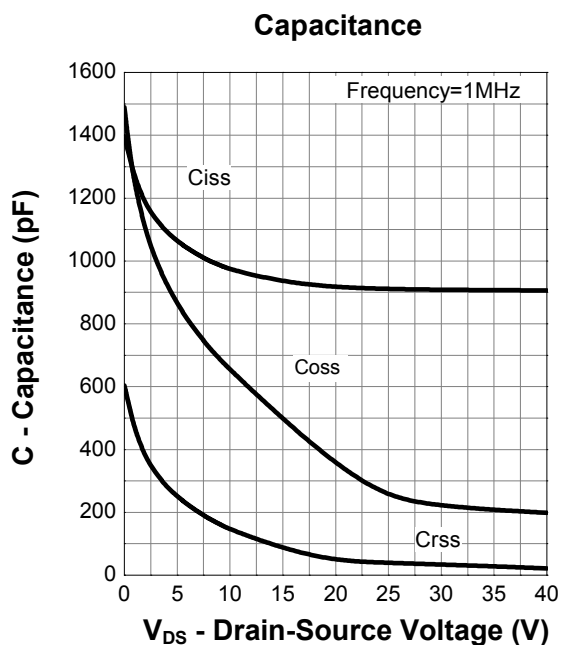
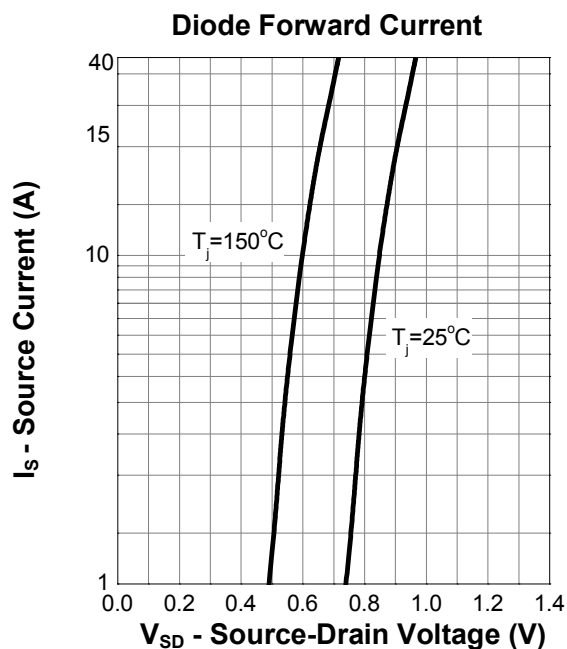
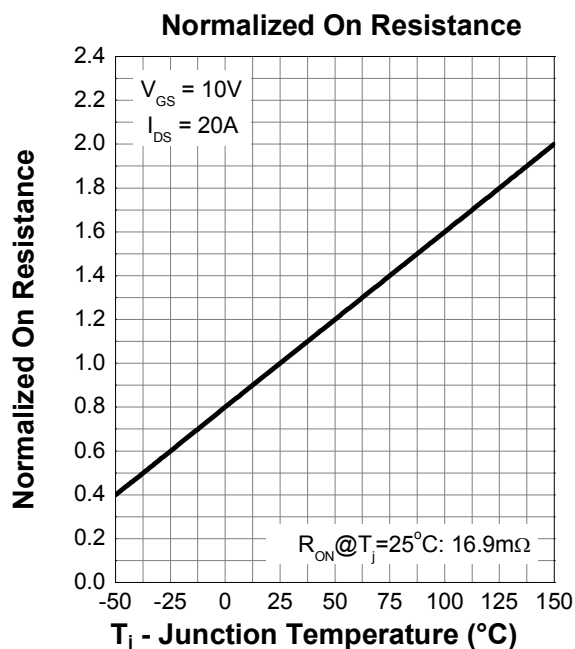
7. Typical Characteristics



7. Typical Characteristics (cont.)

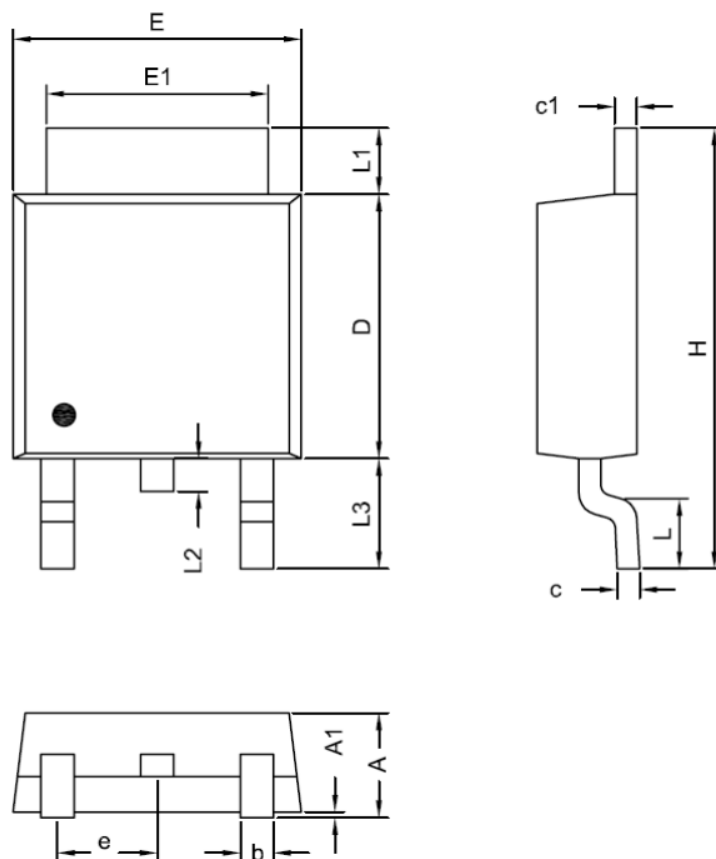


7. Typical Characteristics (cont.)



8. Package Dimensions

TO-252 Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	2.19	2.38
A1	0.02	0.13
D	5.30	6.40
E	6.35	6.80
E1	5.20	5.50
c	0.40	0.60
c1	0.40	0.60
b	0.55	0.85
e	2.30 BCS	
L	1.00	1.80
L1	0.70	1.80
L2	0.70 BCS	
L3	2.40	2.80
H	9.20	10.40