

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Advanced trench cell design
- ☒ Super Trench
- ☒ Surface-mounted package

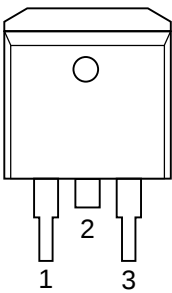
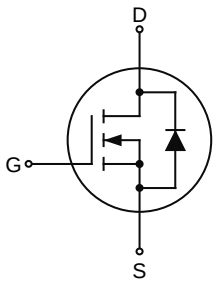
1.2 Applications

- ☒ Motor drivers
- ☒ DC-DC Converter

1.3 Quick reference

- ☒ $BV \geq 40V$
- ☒ $R_{DS(ON)} \leq 1.8m\Omega @ V_{GS} = 10V$
- ☒ $P_{tot} \leq 147W$
- ☒ $I_D \leq 120A$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate(G)	 Top View TO-263	
2	Drain(D)		
3	Source(S)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C=25^{\circ}C$	40	-	V
V_{GS}	Gate-Source Voltage	$T_C=25^{\circ}C$	-	± 20	V
$I_D^{*,***}$	Drain Current (DC)	$T_C=25^{\circ}C, V_{GS}=10V$	-	150	A
$I_{DM}^{**,***}$	Drain Current (Pulsed)	$T_C=25^{\circ}C, V_{GS}=10V$	-	800	A
P_{tot}^*	Drain power dissipation	$T_C=25^{\circ}C$	-	147	W
T_{stg}	Storage Temperature		- 55	150	$^{\circ}C$
T_J	Junction Temperature		-	150	$^{\circ}C$
I_S	Continuous-Source Current	$T_C=25^{\circ}C$	-	150	A
E_{AS}	Single Pulsed Avalanche Energy	$V_{DD}=40V, L=1.0mH$	-	1052	mJ
$R_{\theta JA}^*$	Thermal Resistance-Junction to Ambient		-	43	$^{\circ}C/W$
$R_{\theta JC}^*$	Thermal Resistance-Junction to Case		-	0.85	

Notes:

- * Surface Mounted on minimum footprint pad area.
- ** Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- *** Maximum current rating is package limited.

4. Marking Information

Product Name	Marking
KJ014N04D	014N04 YWWXXX YWWXXX: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ014N04D	TO-263	-	-	800	

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics (T_A=25°C Unless Otherwise Noted)

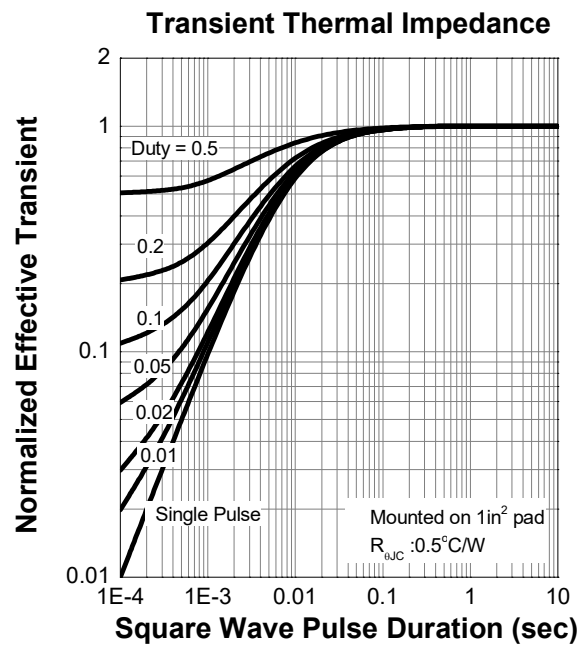
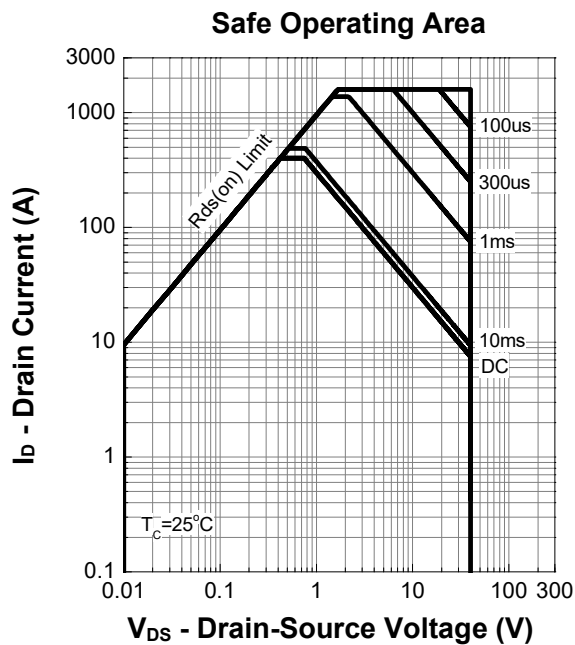
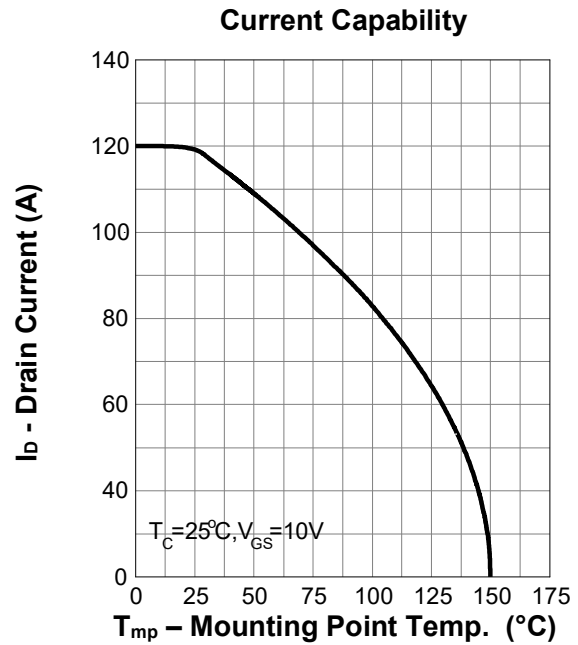
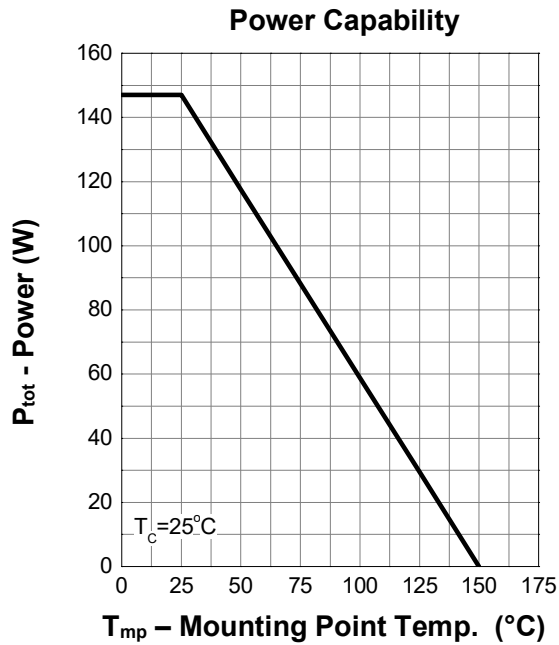
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	40	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = 250 μA	2	-	4	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} = 32 V, V _{GS} = 0 V	-	-	1	μA
		T _J = 85 °C	-	-	30	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	Drain-Source On-State Resistance	V _{GS} = 10 V, I _D = 20 A	-	1.6	1.8	mΩ
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = 20 A, V _{GS} = 0 V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} = 20 A, dI _{SD} /dt = 100 A/μs	-	68	-	ns
Q _{rr}	Reverse Recovery Charge		-	92	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 20 V Frequency = 1 MHz	-	4925	-	pF
C _{oss}	Output Capacitance		-	1655	-	
C _{rss}	Reverse Transfer Capacitance		-	122	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = 20 V, V _{GEN} = 10 V, R _G = 4.5 Ω, R _L = 1 Ω, I _{DS} = 20 A	-	16	-	ns
t _r	Turn-on Rise Time		-	48	-	
t _{d(off)}	Turn-off Delay Time		-	75	-	
t _f	Turn-off Fall Time		-	42	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} = 20 V, V _{GS} = 10 V, I _{DS} = 20 A	-	81	-	nC
Q _{gs}	Gate-Source Charge		-	18	-	
Q _{gd}	Gate-Drain Charge		-	15	-	

Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

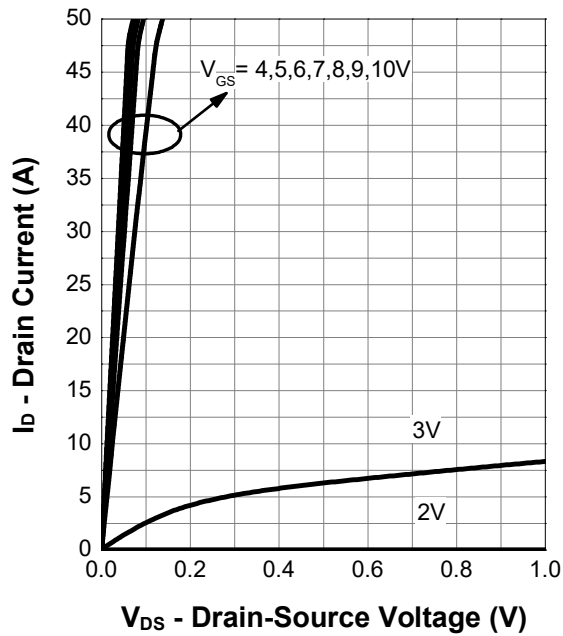
b : Guaranteed by design, not subject to production testing

7. Typical Characteristics

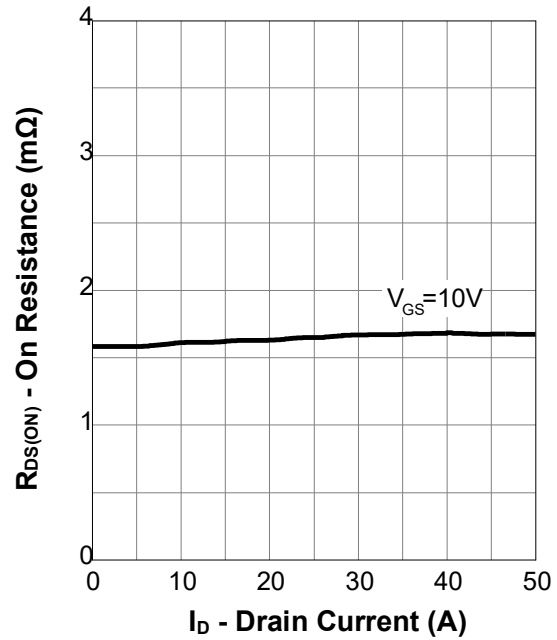


7. Typical Characteristics (Cont.)

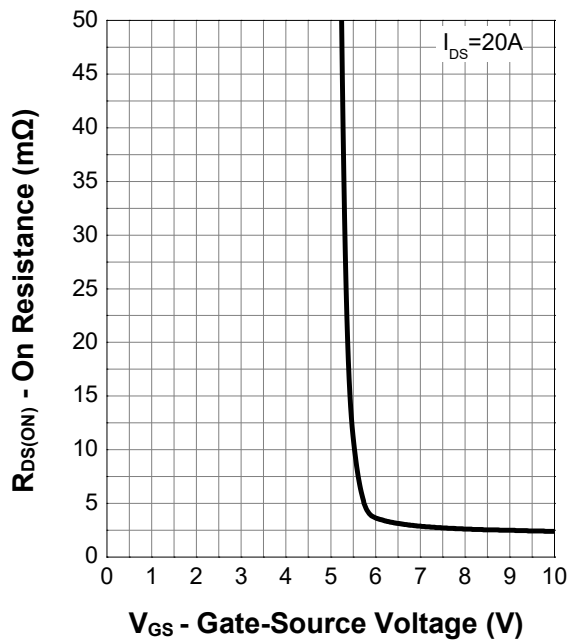
Output Characteristics



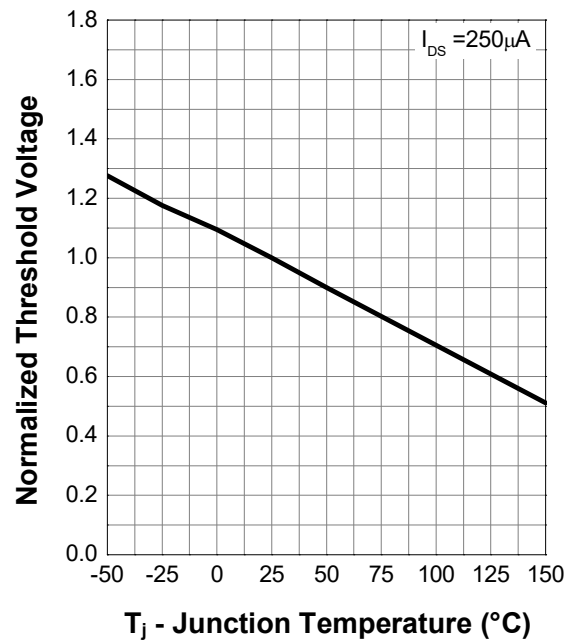
On Resistance



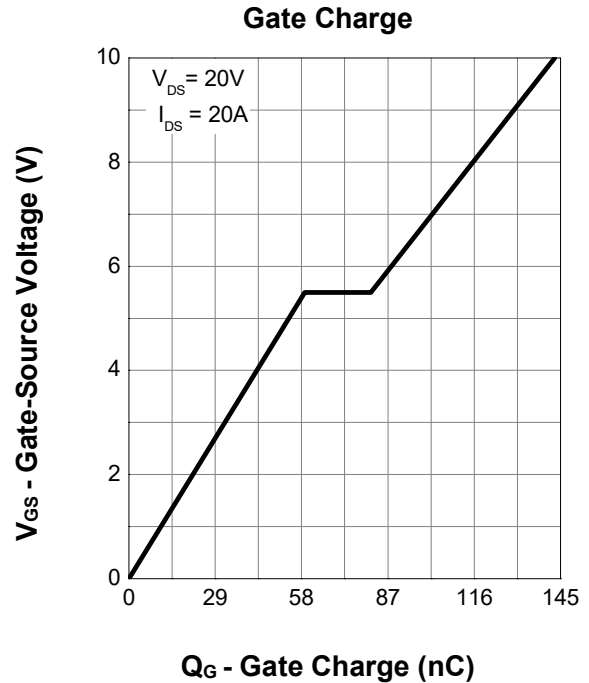
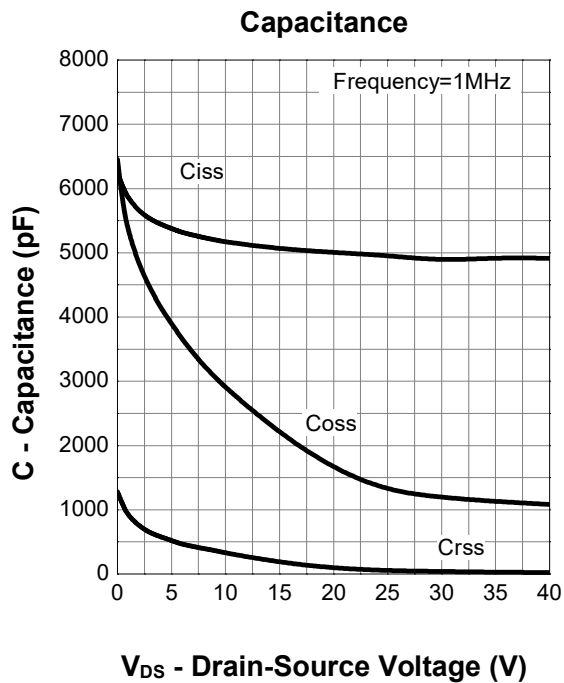
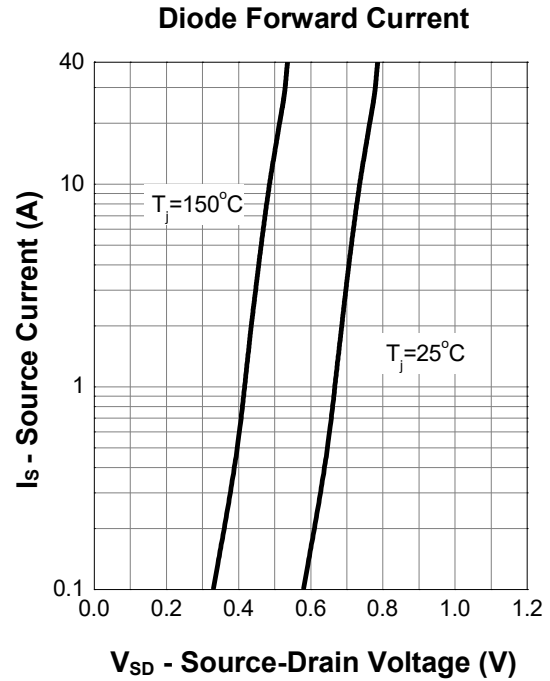
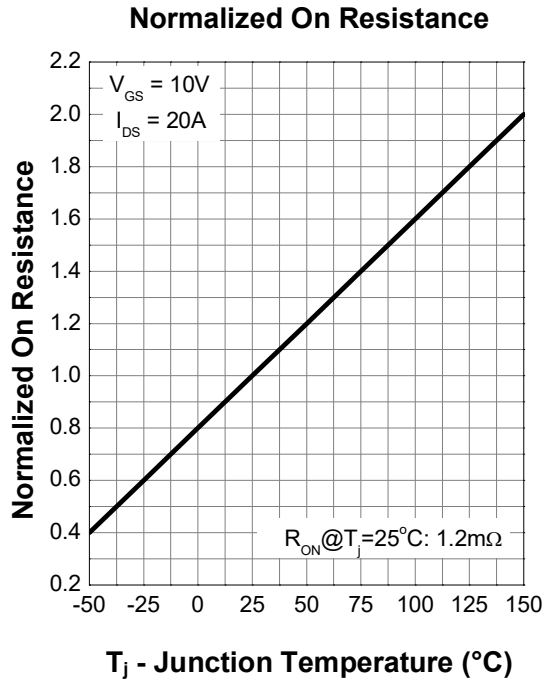
Transfer Characteristics



Normalized Threshold Voltage

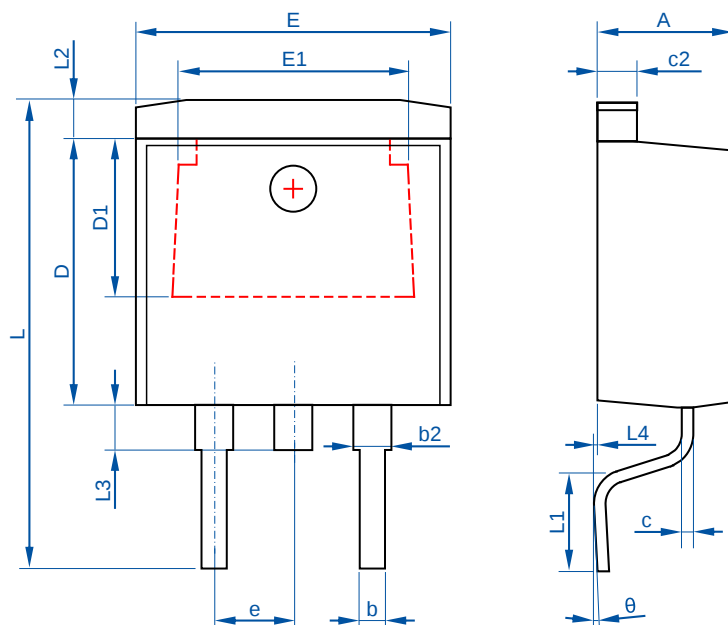


7. Typical Characteristics (Cont.)



8. Package Dimensions

TO-263 Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	4.40	4.80
b	0.76	1.00
L4	0.00	0.25
C	0.36	0.50
L3	1.50 REF	
L1	2.29	2.79
E	9.80	10.40
E1	7.40 REF	
c2	1.25	1.45
b2	1.17	1.47
D	8.60	9.00
D1	5.10 REF	
e	2.54 REF	
L	14.60	15.80
θ	0° ± 3°	
L2	1.27 REF	