

P-Channel Enhancement Mode MOSFET

1. Product Information

Features

- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low gate charge

Applications

- Power Management Switches
- DC/DC Converters

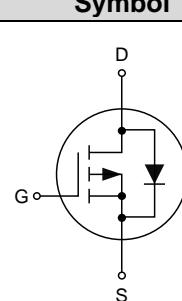
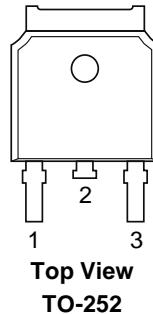
Quick reference

- $V_{DS} = -100 \text{ V}$
- $I_D = -35 \text{ A}$
- $R_{DS(ON)} \leq 50 \text{ m}\Omega @ V_{GS} = -10 \text{ V}$ (Type: 35 m Ω)
- $R_{DS(ON)} \leq 65 \text{ m}\Omega @ V_{GS} = -4.5 \text{ V}$ (Type: 45 m Ω)

Pin Description

Pin	Description
1	Gate (G)
2	Drain (D)
3	Source (S)

Simplified Outline



Package Marking and Ordering Information

Product Name	Package	Marking	Reel Size	Tape Width	Quantity
KJ35P10K	TO-252	KJ35P10K	-	-	2500

2. Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Values	Unit
V_{DS}	Drain-Source Voltage	-100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current, $V_{GS} = 10 \text{ V}$, $T_A = 25^\circ\text{C}$ ¹	-35	A
	Continuous Drain Current, $V_{GS} = 10 \text{ V}$, $T_A = 100^\circ\text{C}$ ¹	-27	A
I_{DM}	Pulsed Drain Current ²	-120	A
I_S	Continuous Source Current (Diode Conduction) ¹	-35	A
E_{AS}	Single Pulse Avalanche Energy ³	289	mJ
P_D	Power Dissipation, $T_A = 25^\circ\text{C}$ ¹	104	W
T_J , T_{STG}	Operating Junction and Storage Temperature Range	-55~150	°C
$R_{\theta JA}$	Maximum Junction-Ambient, $\leq 10 \text{ s}$ ¹	62	°C/W
$R_{\theta JC}$	Maximum Junction-Foot, Steady-State	1.2	°C/W

3. Electrical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0 \text{ V}, I_{\text{D}}=-250 \mu\text{A}$	-100	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_{\text{D}}=-250 \mu\text{A}$, Referenced to 25°C	-	60	200	$\text{mV}/^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-100 \text{ V}, V_{\text{GS}}=0 \text{ V}$	-	-	1	μA
I_{GSS}	Gate-body Leakage current	$V_{\text{DS}}=0 \text{ V}, V_{\text{GS}}=\pm 20 \text{ V}$	-	-	± 100	nA
$V_{\text{GS(th)}}$	Gate-Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250 \mu\text{A}$	-1.5	-2.0	-2.5	V
$\Delta V_{\text{GS(th)}}/\Delta T_J$	Gate Threshold Voltage Temperature Coefficient	$I_{\text{D}}=-250 \mu\text{A}$, Referenced to 25°C	-	-6	-	$\text{mV}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10 \text{ V}, I_{\text{D}}=-15 \text{ A}$	-	35	50	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5 \text{ V}, I_{\text{D}}=-10 \text{ A}$	-	45	65	
g_{FS}	Forward Transconductance	$V_{\text{GS}}=-15 \text{ V}, I_{\text{D}}=-10 \text{ A}$	5	-	-	S
C_{iss}	Input Capacitance	$V_{\text{GS}}=0 \text{ V}, V_{\text{DS}}=-25 \text{ V}, f=1.0 \text{ MHz}$	-	4440	-	pF
C_{oss}	Output Capacitance		-	233	-	
C_{rss}	Reverse Transfer Capacitance		-	180	-	
R_G	Gate Resistance	$V_{\text{GS}}=0 \text{ V}, V_{\text{DS}}=0 \text{ V}, f=1.0 \text{ MHz}$	-	12	-	Ω
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DS}}=-50 \text{ V}, I_{\text{D}}=-15 \text{ A}, V_{\text{GS}}=-10 \text{ V}, R_{\text{GEN}}=9 \Omega$	-	9.8	-	ns
t_r	Turn-on Rise Time		-	41	-	
$t_{\text{d(off)}}$	Turn-off Delay Time		-	258	-	
t_f	Turn-off Fall Time		-	90	-	
Q_g	Total Gate Charge	$V_{\text{GS}}=-10 \text{ V}, V_{\text{DD}}=-50 \text{ V}, I_{\text{D}}=-15 \text{ A}$	-	80	-	nC
Q_{gs}	Gate-Source Charge		-	10	-	
Q_{gd}	Gate-Drain Charge		-	15	-	
I_s	Maximum Continuous Drain-Source Diode Forward Current	-	-	-35	A	
V_{SD}	Diode Forward Voltage	$I_{\text{SD}}=-10 \text{ A}, V_{\text{GS}}=0 \text{ V}$	-	-	-1.4	V

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board.
2. Pulse width limited by maximum junction temperature.
2. The E_{AS} data shows Max. rating. $T_J=25^\circ\text{C}$, $V_{\text{DD}}=-50 \text{ V}$, $V_{\text{G}}=-10 \text{ V}$, $L=0.5 \text{ mH}$, $R_G=25 \Omega$.

4. Typical Characteristics

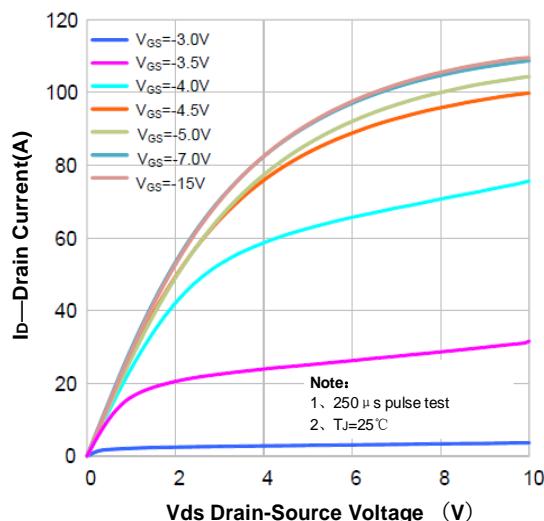


Figure 1 Output Characteristics

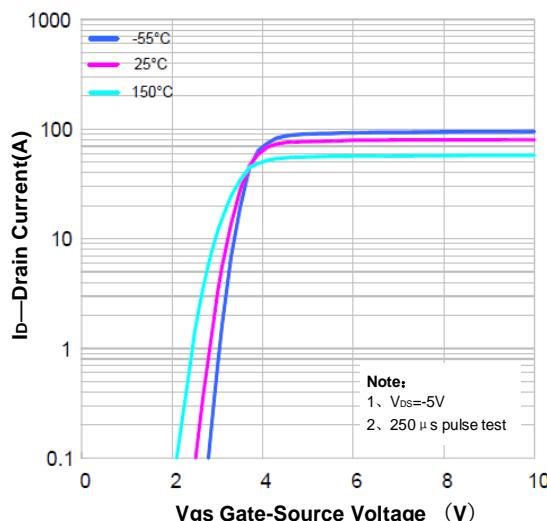


Figure 2 Transfer Characteristics

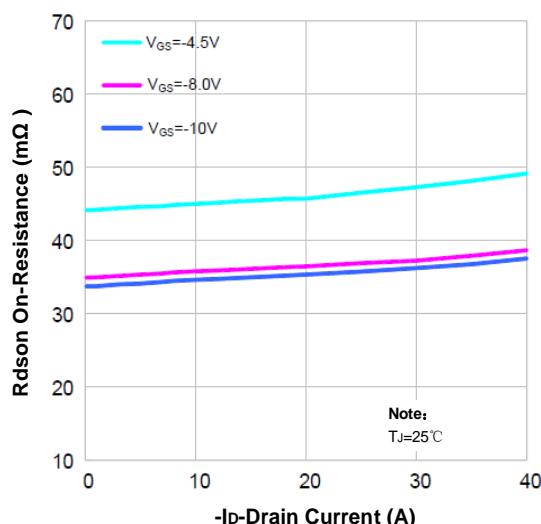


Figure 3 Rdson-Drain Current

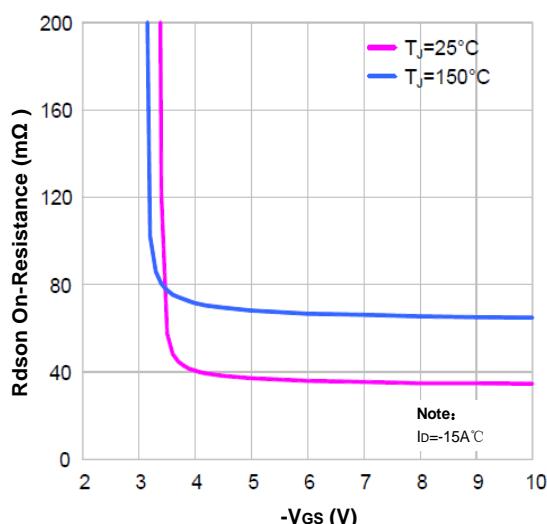


Figure 4 Rdson-Vgs Voltage

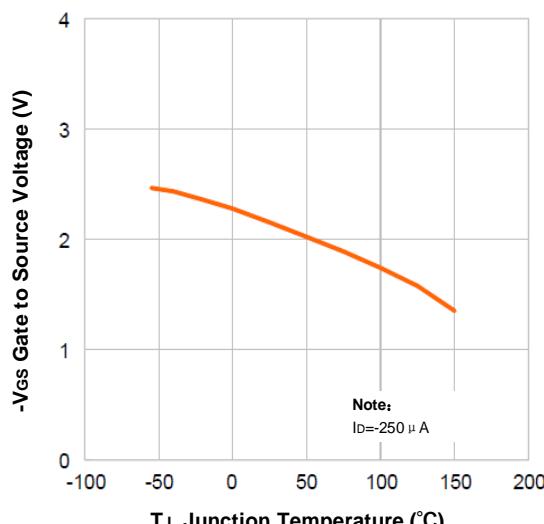


Figure 5 Normalized $V_{gs(th)}$ vs. T_J

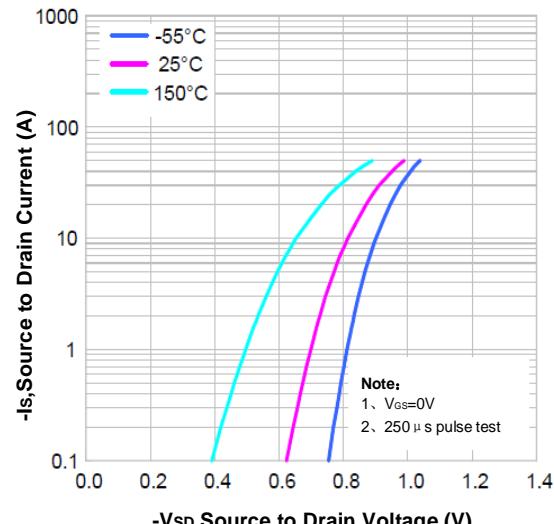
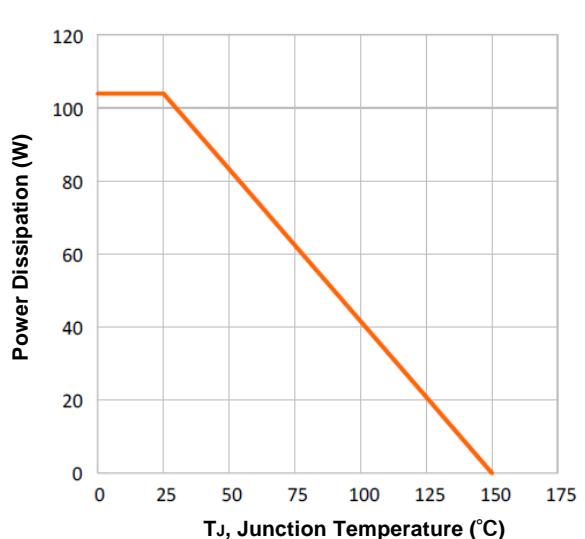
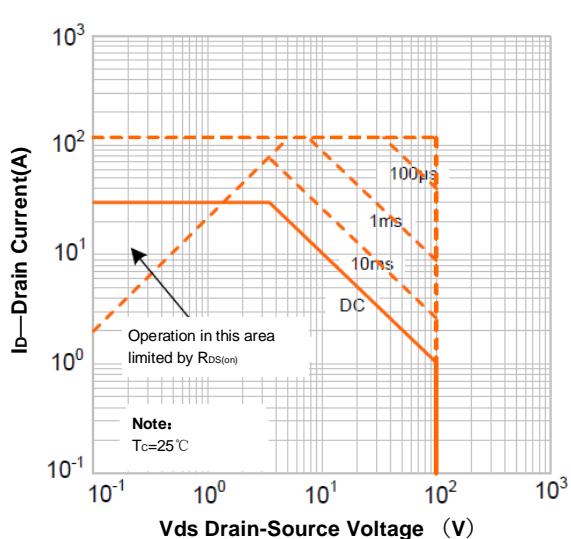
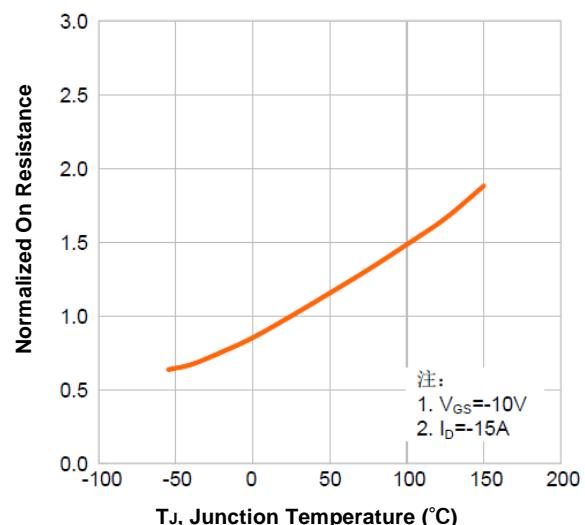
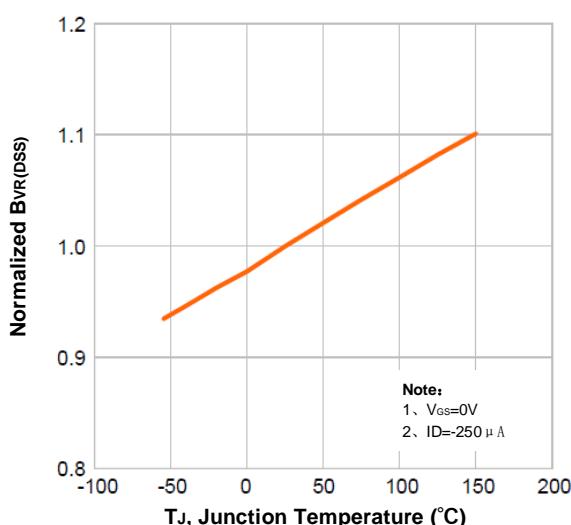
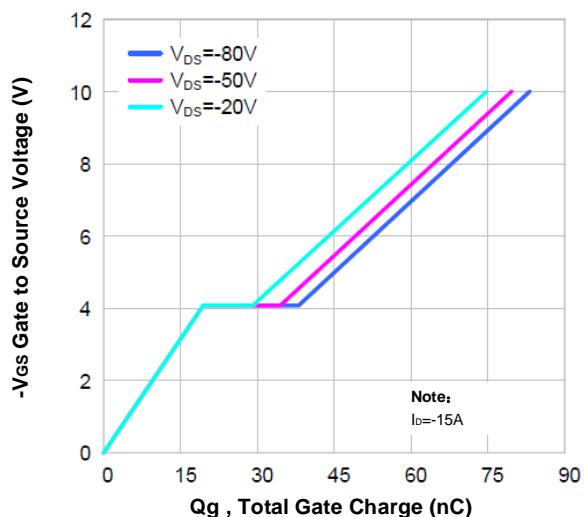
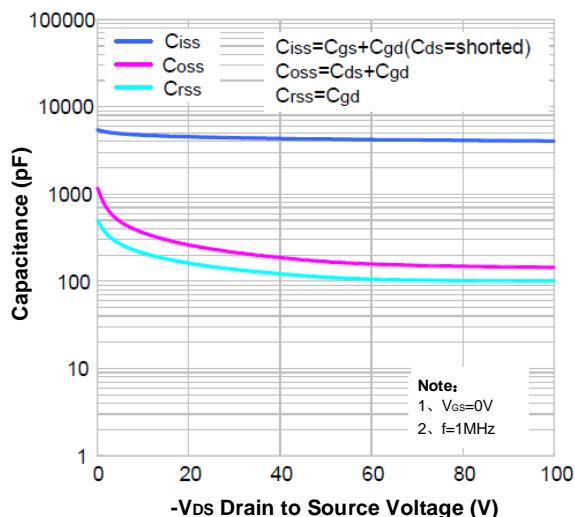


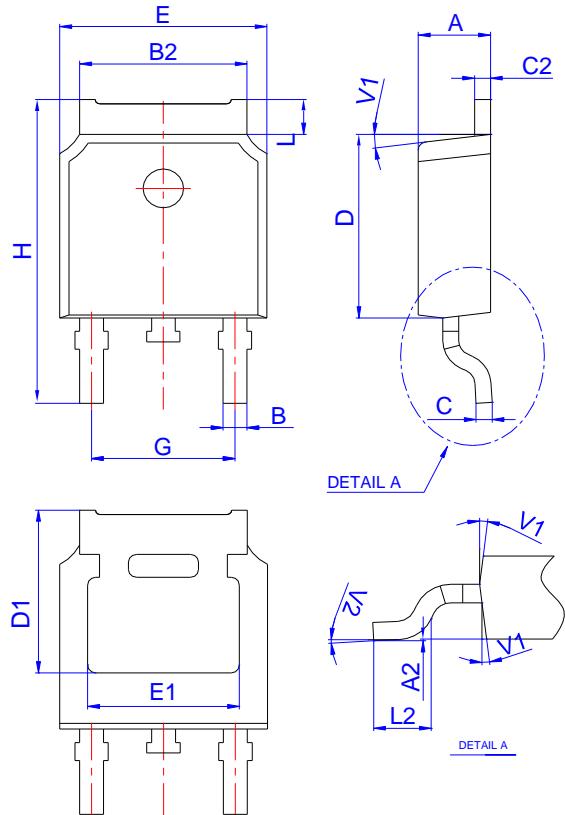
Figure 6 Source to Drain Voltage (V)

4. Typical Characteristics (Cont.)



5. Package Mechanical Data

TO-252 Package



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°