

Super-Junction Power Mosfet

1. Product Information

Features

- Low FOM $R_{DS(ON)} \times Q_G$
- Extremely Low Switching Loss
- Good Stability and Uniformity
- 100% UIS and Isolation Tested

Pin Description

Pin	Description
1	Gate(G)
2	Drain(D)
3	Source(S)

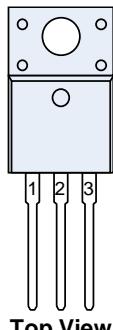
Applications

- EV Charger
- Sever/Telecom/PC Power
- AC-DC Power Management
- Motor Driver

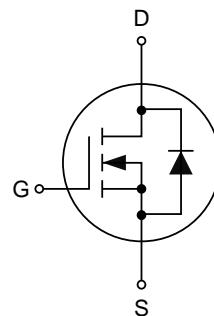
Quick reference

- $V_{DS} \geq 600 \text{ V}$
- $I_D \leq 55 \text{ A}$
- $R_{DS(ON)} \leq 68 \text{ m}\Omega @ V_{GS} = 10\text{V}$ (Type 57 mΩ)

Simplified Outline



Symbol



Package Marking and Ordering Information

Product Name	Marking	Package	Packaging	Quantity (pcs)
KJC60R068CF	KJC60R068F	TO-220F	Tube	50

2. Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Values	Unit
V_{DS}	Drain-Source Voltage	600	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Continuous Drain Current ($T_c=25^\circ\text{C}$) [1]	55	A
	Continuous Drain Current ($T_c=100^\circ\text{C}$) [1]	38	A
I_{DM}	Pulsed Drain Current [1]	220	A
E_{AS}	Single Pulsed Avalanche Energy [2]	410	mJ
dv/dt	Peak Diode Recovery dv/dt	50	V/ns
P_D	Power Dissipation	50	W
T_J, T_{stg}	Operating Junction and Storage Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance, Junction-Ambient	80	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-Case	2.3	°C/W

3. Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Type	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0 \text{ V}, I_{\text{D}}=250 \mu\text{A}$	600	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=600 \text{ V}, V_{\text{GS}}=0 \text{ V}$	-	-	10	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 30 \text{ V}, V_{\text{DS}}=0 \text{ V}$	-	-	± 100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250 \mu\text{A}$	3.0	-	5.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10 \text{ V}, I_{\text{D}}=27 \text{ A}$	-	57	68	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=100 \text{ V}, V_{\text{GS}}=0 \text{ V}, f=1 \text{ MHz}$	-	3012	-	pF
C_{oss}	Output Capacitance		-	145	-	pF
C_{rss}	Reverse Transfer Capacitance		-	2.9	-	pF
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=400 \text{ V}, I_{\text{D}}=27 \text{ A}, R_{\text{G}}=25 \Omega, V_{\text{GS}}=10 \text{ V}$	-	88	-	ns
t_r	Turn-on Rise Time		-	75	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	170	-	ns
t_f	Turn-off Fall Time		-	50	-	ns
Q_g	Total Gate Charge	$V_{\text{DS}}=480 \text{ V}, I_{\text{D}}=27 \text{ A}, V_{\text{GS}}=10 \text{ V}$	-	79	-	nC
Q_{gs}	Gate-Source Charge		-	21	-	nC
Q_{gd}	Gate-Drain Charge		-	23	-	nC
Source-Drain Diode Characteristics						
V_{SD}	Drain-Source Diode Forward Voltage	$V_{\text{GS}}=0 \text{ V}, I_{\text{F}}=27 \text{ A}$	-	-	1.4	V
I_s	Diode Continuous Forward Current		-	-	55	A
I_{SM}	Maximum Pulsed Body-Diode Forward Current		-	-	220	A
T_{rr}	Reverse Recovery Time	$V_{\text{R}}=400 \text{ V}, I_{\text{F}}=27 \text{ A}, \frac{di}{dt}=100 \text{ A}/\mu\text{s}$	-	176	-	ns
Q_{rr}	Reverse Recovery Charge		-	1.4	-	μC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. $T_J=25^\circ\text{C}$, $V_{\text{DD}}=50 \text{ V}$, $V_{\text{G}}=10 \text{ V}$, $R_{\text{G}}=25 \Omega$.

4. Test Circuits and Waveforms ($T_J=25^\circ\text{C}$)

Table 1. Gate Charge Test Circuit and Waveforms

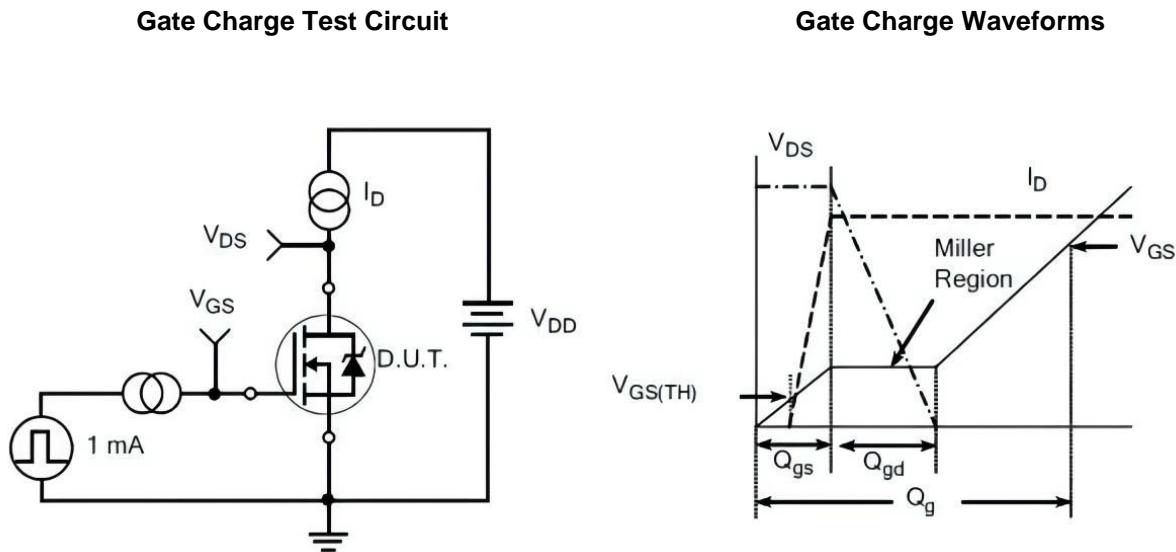


Table 2. Resistive Switching Test Circuit and Waveforms

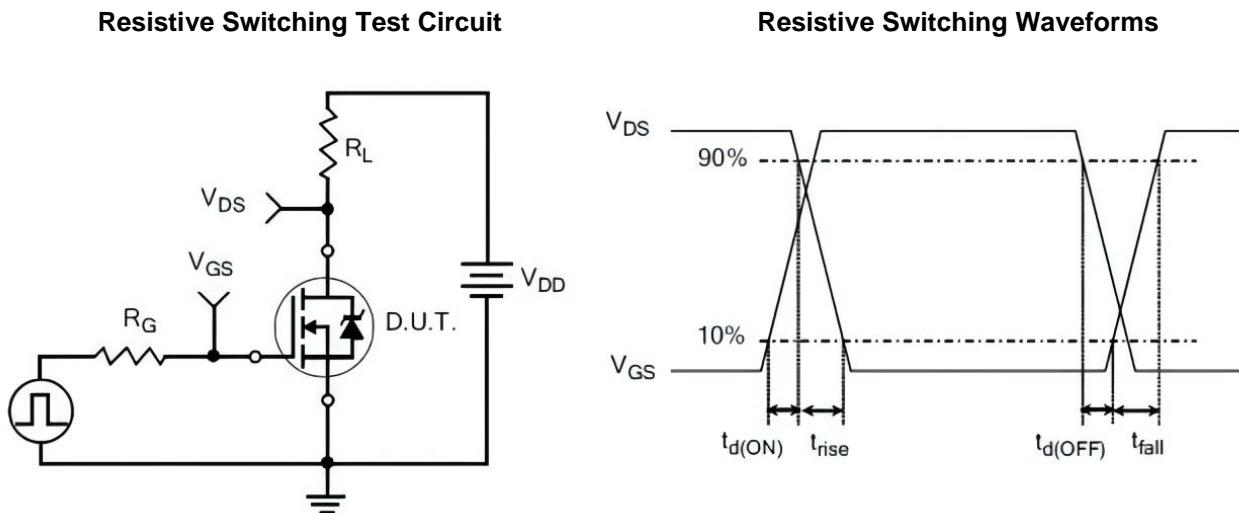
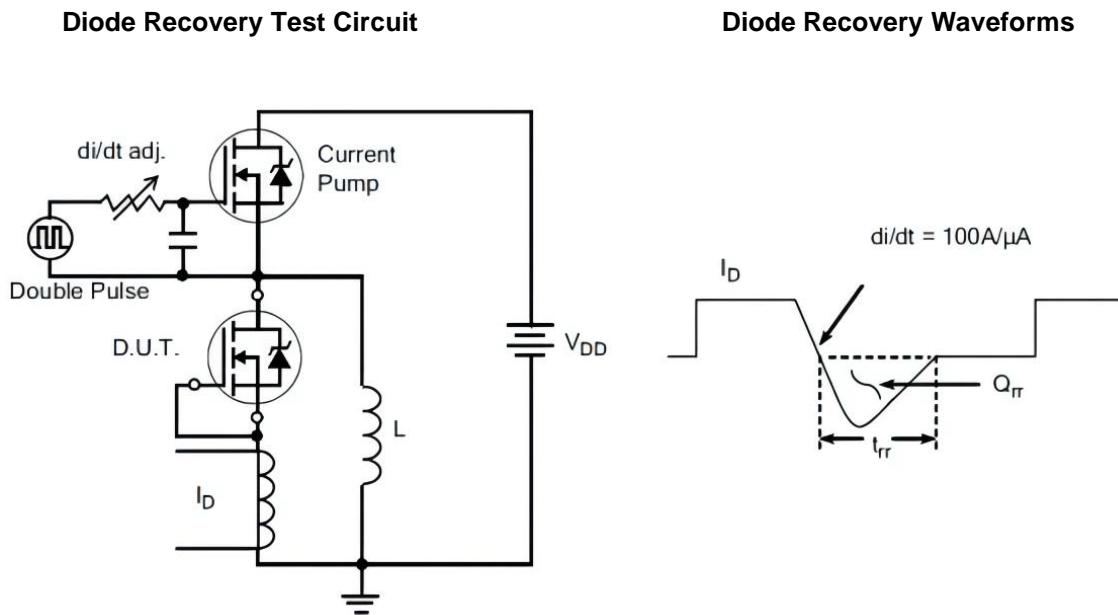
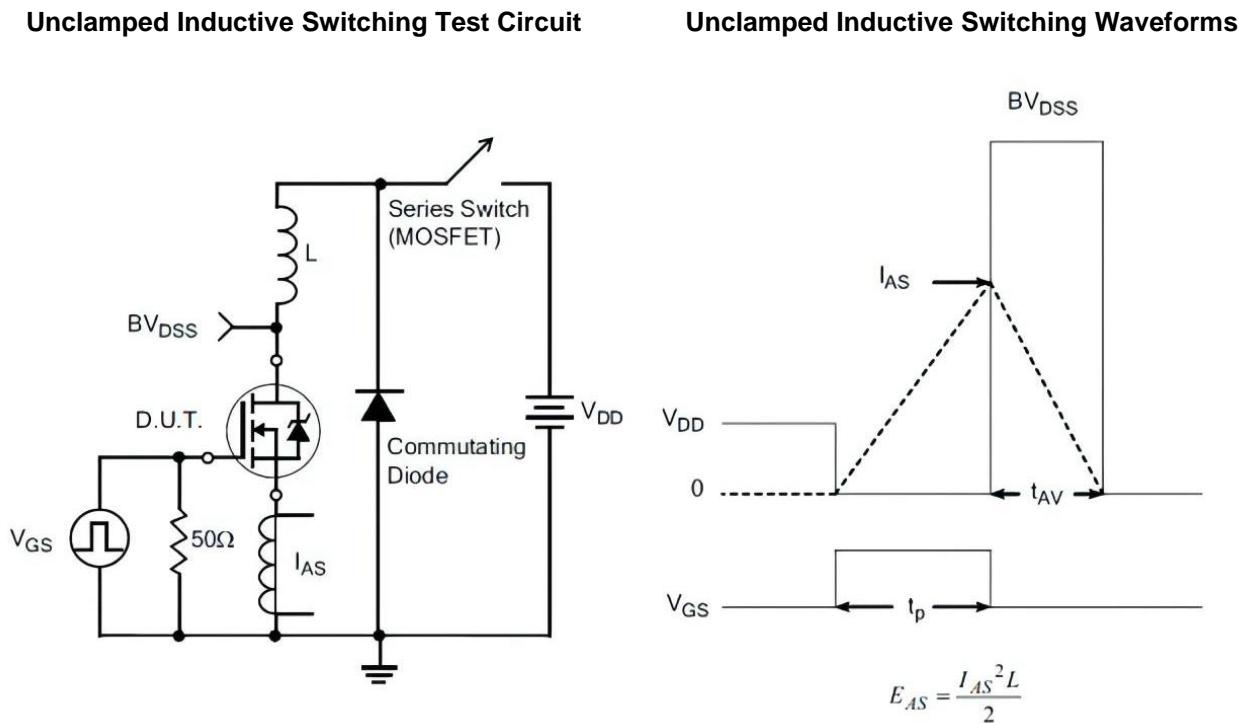


Table 3. Diode Recovery Test Circuit and Waveforms

Table 4. Unclamped inductive Switching (UIS) Test Circuit and Waveforms


5. Electrical Characteristics

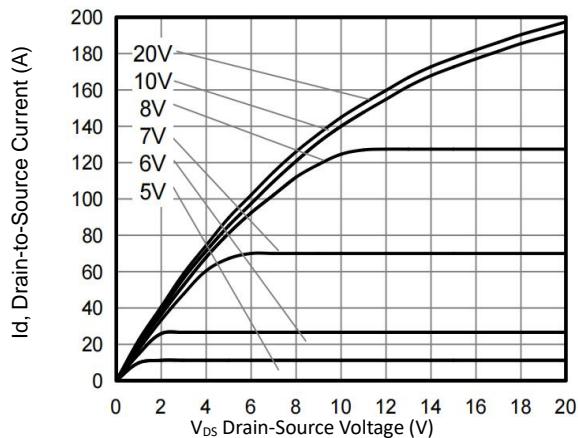


Figure 1. Typical Output Characteristics

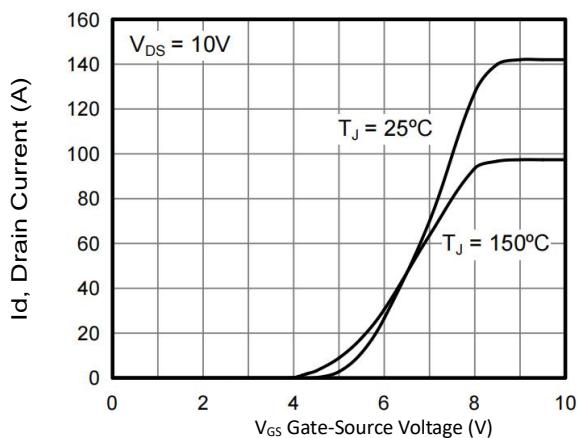


Figure 2. Typical Transfer Characteristics

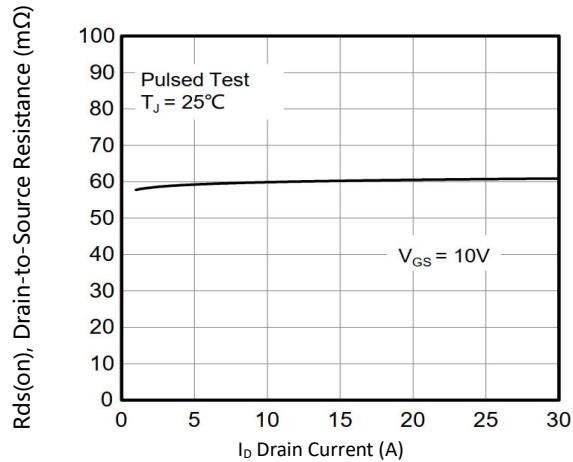


Figure 3. On-Resistance versus Drain Current

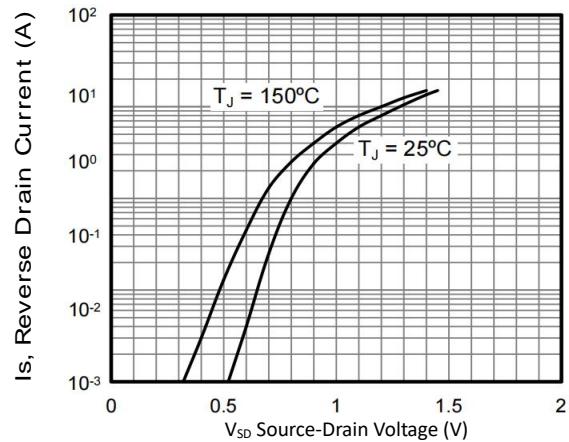


Figure 4. Diode forward voltage versus Current

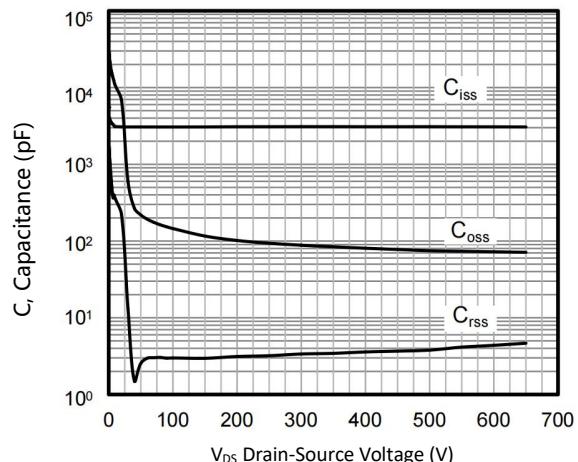


Figure 5. Typical Capacitance versus V_{DS}

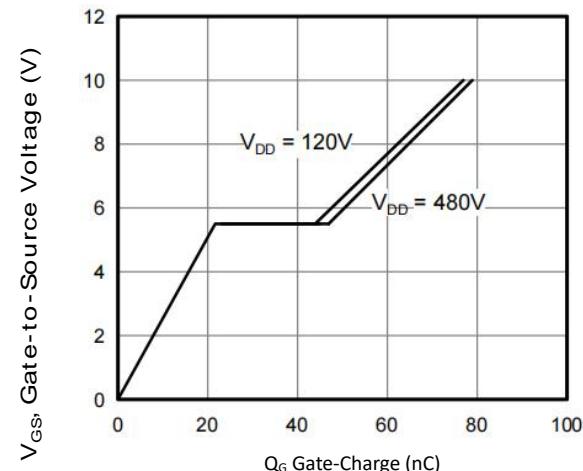


Figure 6. Typical Gate Charge versus V_{GS}

5. Electrical Characteristics (cont.)

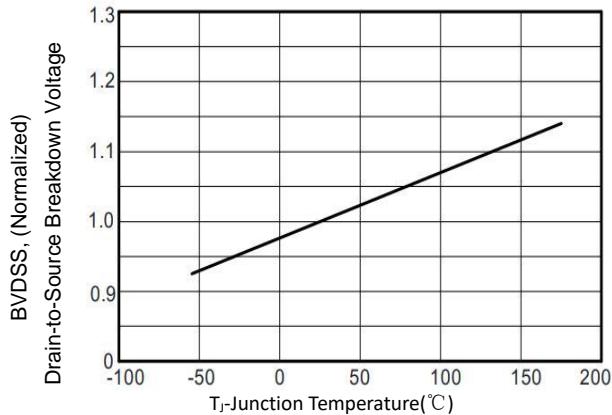


Figure 7. BV_{DSS} Variation with Temperature

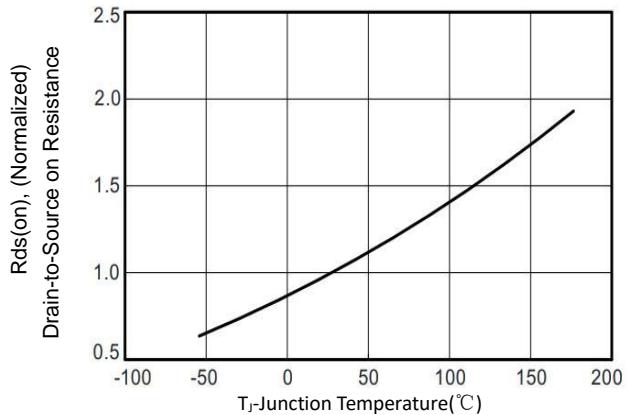


Figure 8. On-Resistance Variation with Temperature

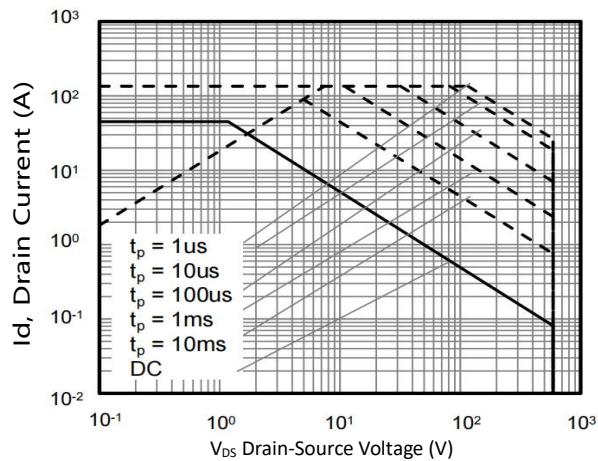


Figure 9. Maximum Safe Operating Area

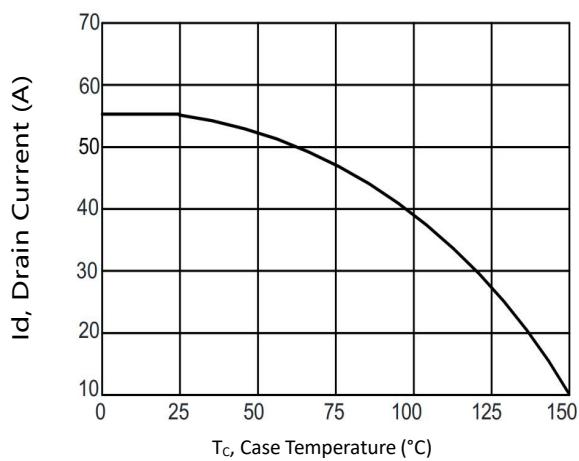
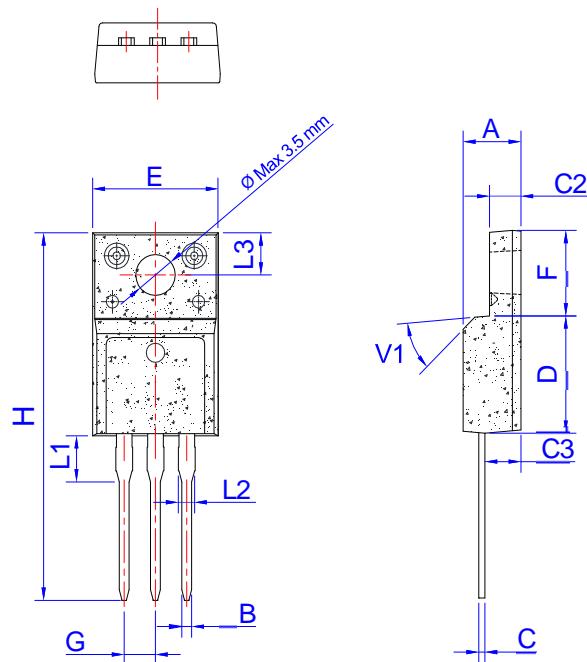


Figure 10. Maximum Continuous Drain Current versus Case Temperature

6. Package Mechanical Data

- TO-220F Package



Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX
A	4.50		4.90
B	0.74	0.80	0.83
C	0.47		0.65
C2	2.45		2.75
C3	2.60		3.00
D	8.80		9.30
E	9.80		10.4
F	6.40		6.80
G		2.54	
H	28.0		29.8
L1		3.63	
L2	1.14		1.70
L3		3.30	
V1		45°	