

Super-Junction Power MOSFET

1. Product Information

Features

- Low FOM $R_{DS(ON)} \times Q_G$
- Better EMI
- Extremely low switching loss
- Good stability and uniformity
- 100% UIS and Isolation Tested

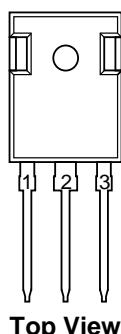
Pin Description

Pin	Description
1	Gate(G)
2	Drain(D)
3	Source(S)

Applications

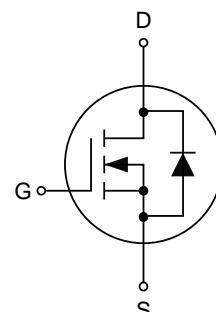
- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charge

Simplified Outline



Top View
TO-247

Symbol



Quick reference

$V_{DS} \geq 650$ V

$I_D \leq 20$ A

$R_{DS(ON)} \leq 190$ mΩ @ $V_{GS} = 10$ V (Type 160 mΩ)

Package Marking and Ordering Information

Product Name	Marking	Package	Packaging	Quantity (pcs)
KJC65R190P	KJC65R190P	TO-247	Tube	30

2. Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Values	Unit
V_{DS}	Drain-Source Voltage	650	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Continuous Drain Current ($T_C=25^\circ\text{C}$)	20	A
	Continuous Drain Current ($T_C=100^\circ\text{C}$)	12.5	A
I_{DM}	Pulsed Drain Current ^[1]	80	A
E_{AS}	Single Pulsed Avalanche Energy ^[2]	388	mJ
P_D	Power Dissipation ^[2]	219	W
dv/dt	Peak Diode Recovery dv/dt	5	V/ns
T_J, T_{stg}	Operating Junction and Storage Temperature Range	-55 to 150	°C
$R_{θJA}$	Thermal Resistance, Junction-Ambient ^[3]	45	°C/W
$R_{θJC}$	Thermal Resistance, Junction-Case ^[3]	0.57	°C/W

3. Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Type	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0 \text{ V}, I_{\text{D}}=250 \mu\text{A}$	650	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=650 \text{ V}, V_{\text{GS}}=0 \text{ V}, T_J=25^\circ\text{C}$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 30 \text{ V}, V_{\text{DS}}=0 \text{ V}$	-	-	± 100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250 \mu\text{A}$	2.5	3.5	4.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10 \text{ V}, I_{\text{D}}=20 \text{ A}$	-	160	190	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=100 \text{ V}, V_{\text{GS}}=0 \text{ V}, F=1 \text{ MHz}$	-	1655	-	pF
C_{oss}	Output Capacitance		-	69	-	pF
C_{rss}	Reverse Transfer Capacitance		-	1.2	-	pF
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=325 \text{ V}, I_{\text{D}}=20 \text{ A}, R_{\text{G}}=25 \Omega, V_{\text{GS}}=10 \text{ V}$	-	34	-	ns
t_r	Turn-on Rise Time		-	37	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	148	-	ns
t_f	Turn-off Fall Time		-	36	-	ns
Q_g	Total Gate Charge	$V_{\text{DS}}=325 \text{ V}, I_{\text{D}}=20 \text{ A}, V_{\text{GS}}=10 \text{ V}$	-	34	-	nC
Q_{gs}	Gate-Source Charge		-	7	-	nC
Q_{gd}	Gate-Drain Charge		-	12	-	nC
Source-Drain Diode Characteristics						
V_{SD}	Drain-Source Diode Forward Voltage	$V_{\text{GS}}=0 \text{ V}, I_{\text{F}}=20 \text{ A}$	-	-	1.4	V
I_s	Diode Continuous Forward Current		-	-	20	A
I_{SM}	Maximum Pulsed Body-Diode Forward Current		-	-	60	A
T_{rr}	Reverse Recovery Time	$V_{\text{R}}=50 \text{ V}, I_{\text{F}}=20 \text{ A}, \text{di/dt}=100 \text{ A}/\mu\text{s}$	-	301	-	ns
Q_{rr}	Reverse Recovery Charge		-	4.47	-	μC

Notes:

- Limited by maximum junction temperature, maximum duty cycle is 0.75.
- $T_J=25^\circ\text{C}$, $V_{\text{DD}}=50 \text{ V}$, $V_{\text{GS}}=10 \text{ V}$, $R_{\text{G}}=25 \Omega$.
- Mount on minimum PCB layout.

4. Test Circuits and Waveforms ($T_J=25^\circ\text{C}$, unless otherwise noted)

Table 1. Gate Charge Test Circuit and Waveforms

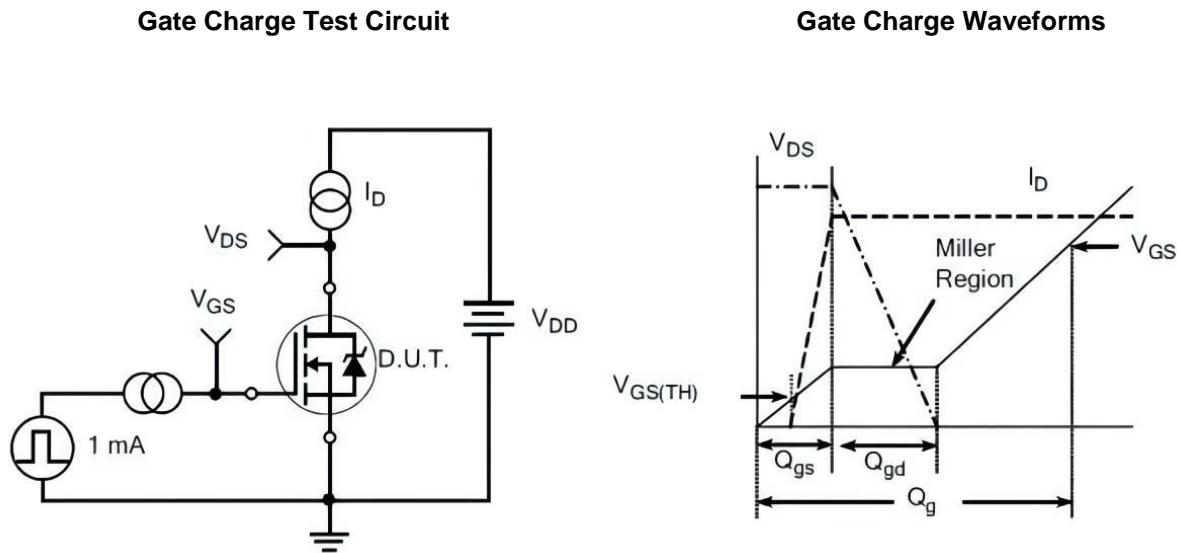


Table 2. Resistive Switching Test Circuit and Waveforms

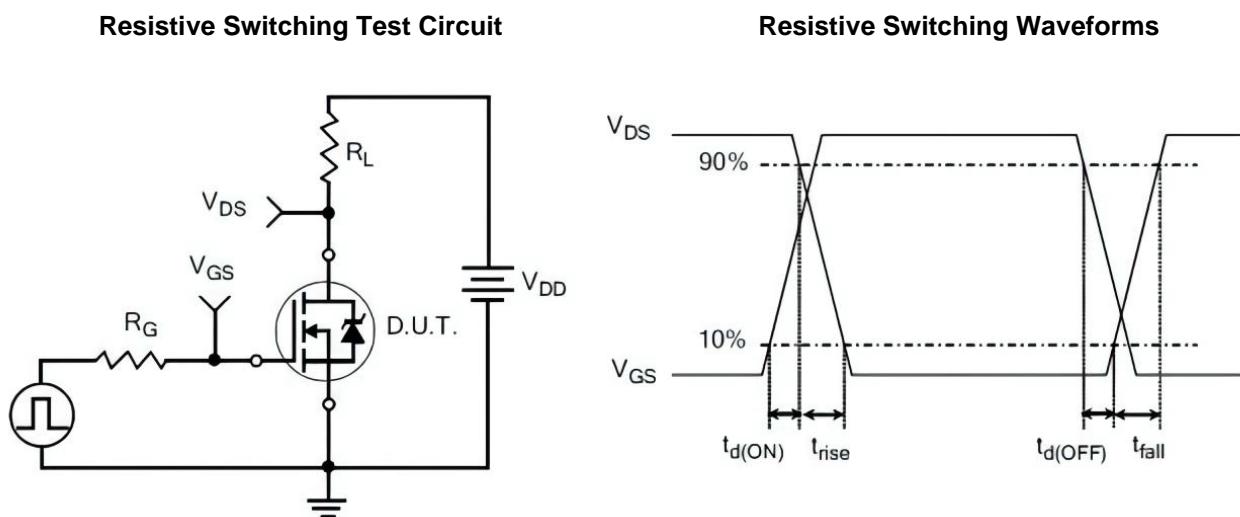


Table 3. Diode Recovery Test Circuit and Waveforms

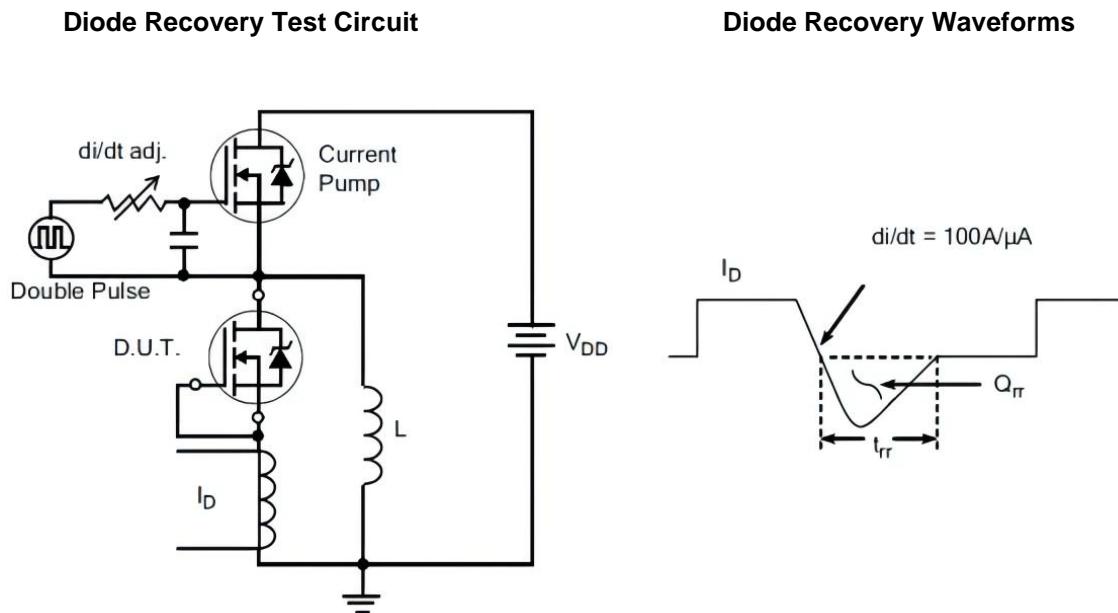
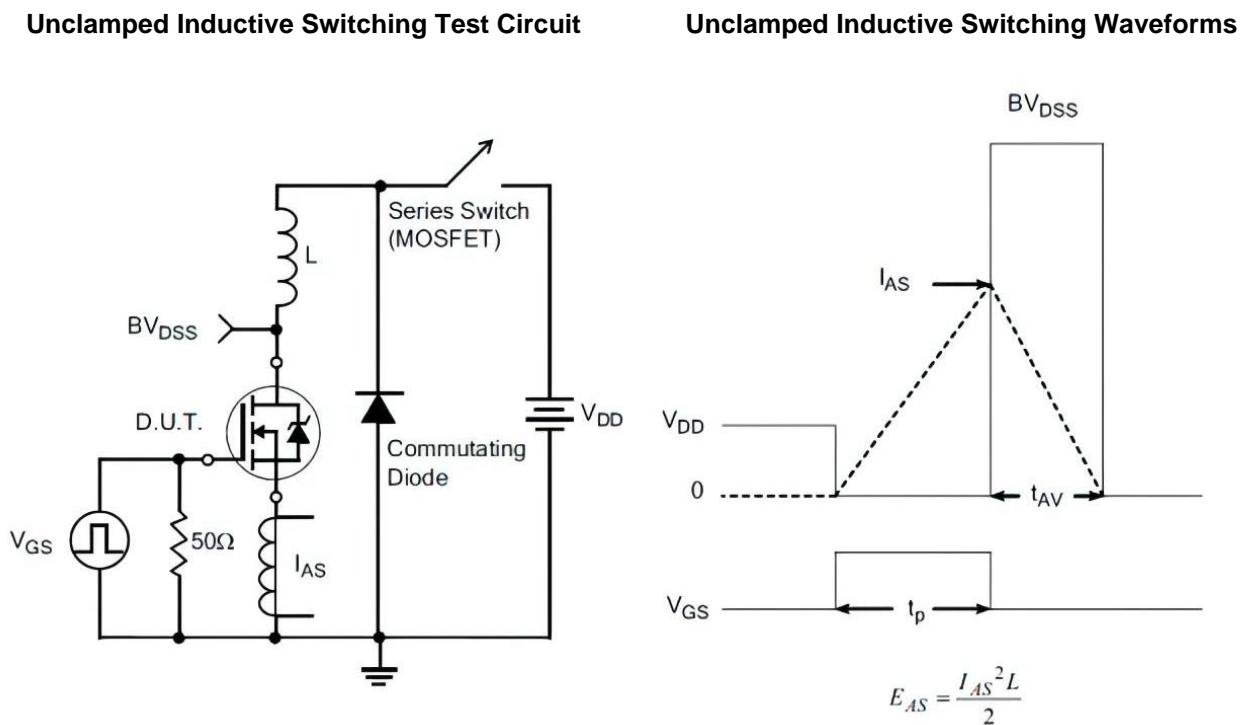


Table 4. Unclamped inductive Switching (UIS) Test Circuit and Waveforms



5. Electrical Characteristics

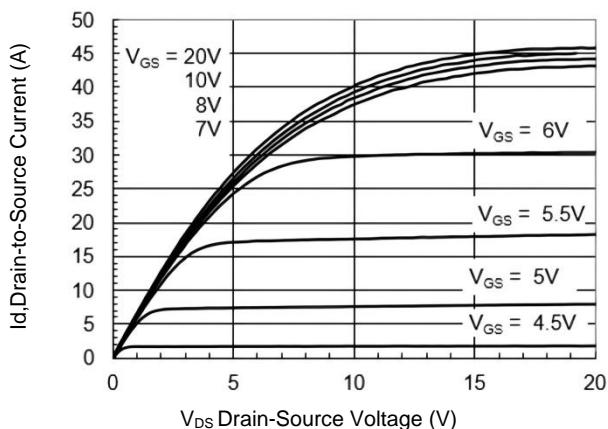


Figure 1. Typical Output Characteristics

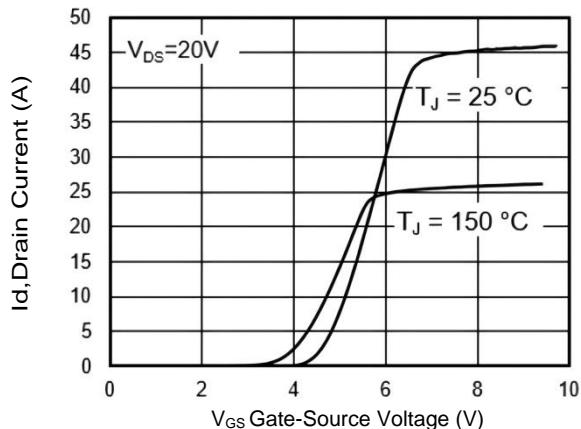


Figure 2. Typical Transfer Characteristics

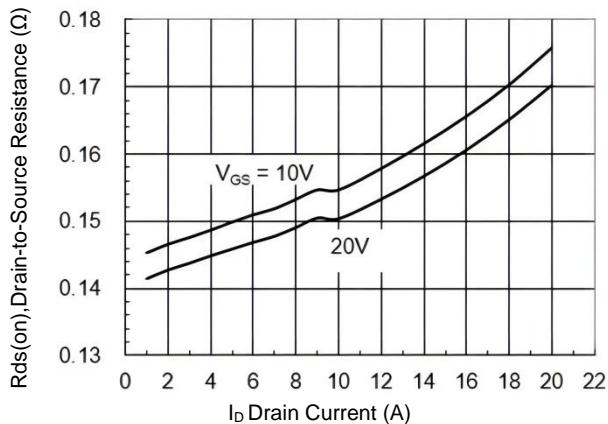


Figure 3. On-Resistance versus Drain Current

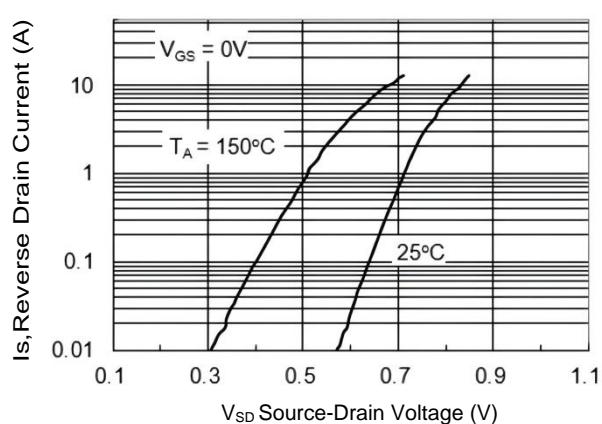


Figure 4. Diode forward voltage versus Current

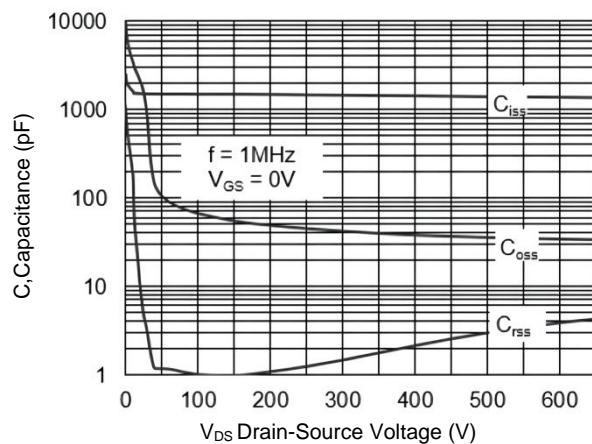


Figure 5. Typical Capacitance versus V_{DS}

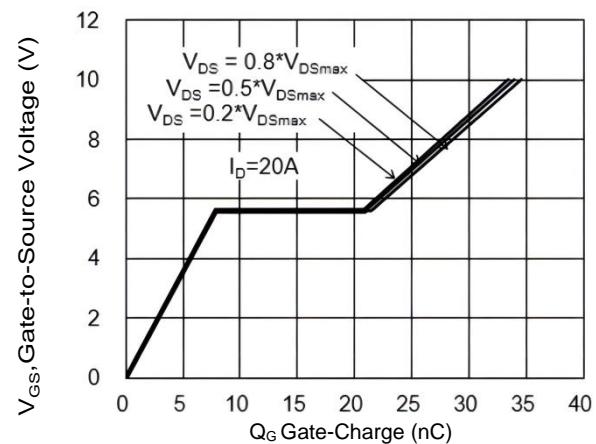


Figure 6. Typical Gate Charge versus V_{GS}

5. Electrical Characteristics (cont.)

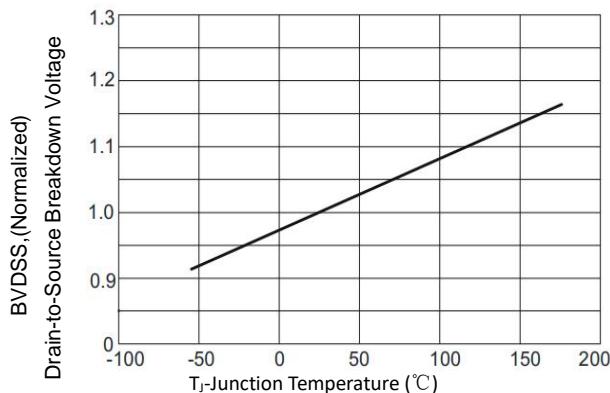


Figure 7. BV_{DSS} Variation with Temperature

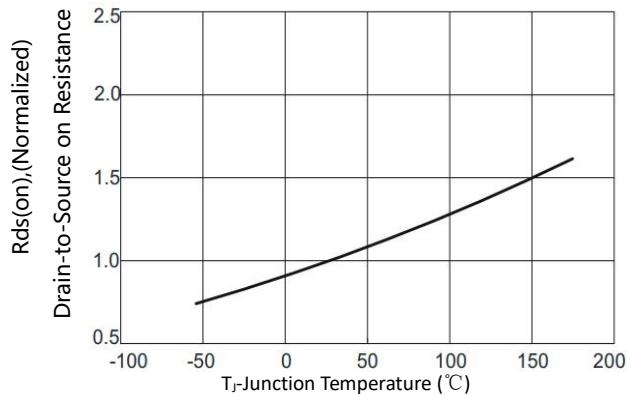


Figure 8. On-Resistance Variation with Temperature

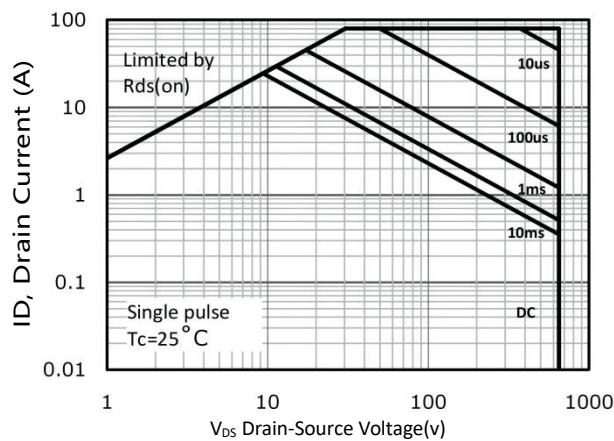


Figure 9. Maximum Safe Operating Area

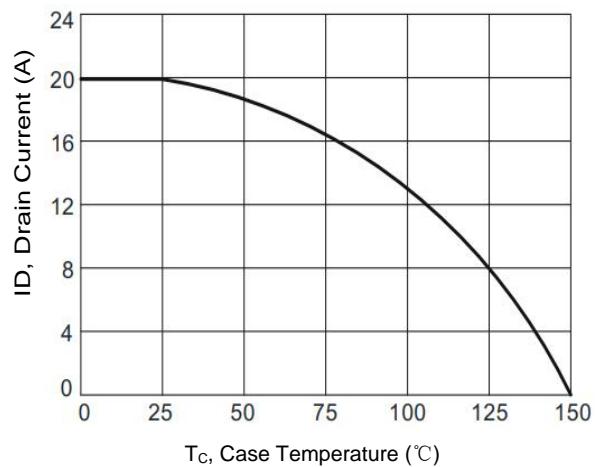
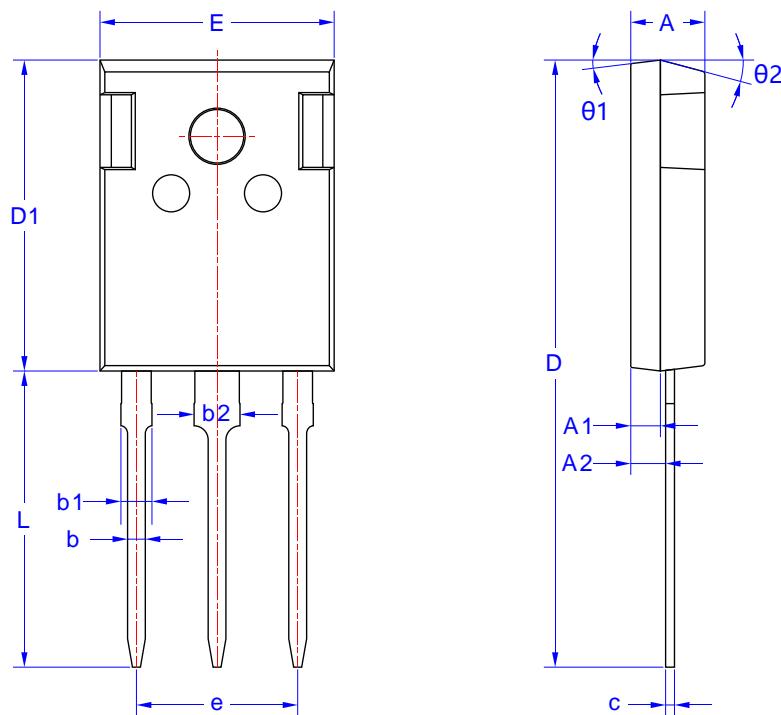


Figure 10. Maximum Continuous Drain Current versus Case Temperature

6. Package Mechanical Data

- TO-247 Package



Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A	4.90	5.00	5.10
A1	1.90	2.00	2.10
A2	2.25	2.35	2.45
b		1.20	
b1		2.10	
b2		3.10	
c		0.60	
D	40.00	41.00	42.00
D1	20.80	21.00	21.20
E	15.60	15.80	16.00
e		10.88	
L	19.80	20.00	20.20
θ1		7°	
θ2		15°	