

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Advanced trench cell design

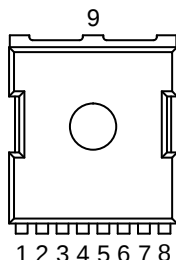
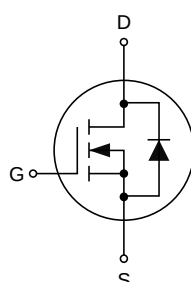
1.2 Applications

- ☒ LCD TV appliances
- ☒ High power inverter system
- ☒ LCDM appliances

1.3 Quick reference

- ☒ $BV \geq 200\text{ V}$
- ☒ $R_{DS(ON)} \leq 7.5\text{ m}\Omega @V_{GS} = 10\text{ V}$
- ☒ $P_D \leq 300\text{ W}$
- ☒ $R_{DS(ON)} \leq 8.0\text{ m}\Omega @V_{GS} = 6\text{ V}$
- ☒ $I_D \leq 114\text{ A}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate (G)	 Top View TOLL-8L	
2,3,4,5,6,7,8	Source (S)		
9	Drain (D)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C=25^{\circ}\text{C}$	200	-	V
V_{GS}	Gate-Source Voltage	$T_C=25^{\circ}\text{C}$	-	± 20	V
$I_D^{*,***}$	Drain Current (DC)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	114	A
		$T_C=100^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	81	A
$I_{DM}^{*,**}$	Drain Current (Pulsed)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	456	A
P_D	Drain Power Dissipation	$T_C=25^{\circ}\text{C}$	-	300	W
I_S	Continuous-Source Current	$T_C=25^{\circ}\text{C}$	-	114	A
E_{AS}^{*}	Single Pulsed Avalanche Energy	$V_{DD}=50\text{ V}, L=1.0\text{ mH}$	-	1800	mJ
T_J, T_{stg}	Operating Junction and Storage Temperature Range		-55	175	$^{\circ}\text{C}$
$R_{\theta JA}^{*}$	Thermal Resistance-Junction to Ambient		-	40	$^{\circ}\text{C/W}$
$R_{\theta JC}^{*}$	Thermal Resistance-Junction to Case		-	0.5	$^{\circ}\text{C/W}$

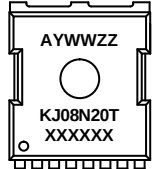
Notes:

* Surface mounted on 1 in² pad area, $t \leq 10\text{ sec.}$

** Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

*** Limited by bonding wire.

4. Marking Information

Product Name	Marking
KJ08N20T	

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity (pcs)
KJ08N20T	TOLL-8L	13"	24 mm	2000

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C)

6. Electrical Characteristics (T_A=25°C unless otherwise noted)

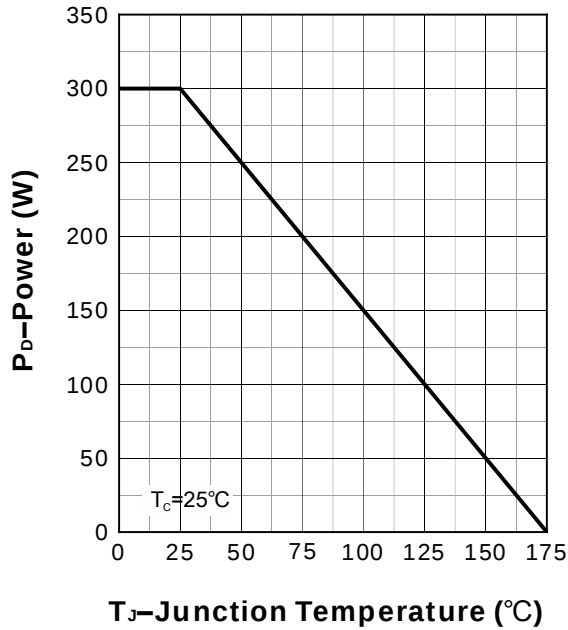
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0 V, I _{DS} =250 μA	200	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250 μA	2	-	4	V
I _{DSS}	Drain Leakage Current	V _{DS} =160 V, V _{GS} =0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{DS} =0 V, V _{GS} =±20 V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} =10 V, I _{DS} =50 A	-	6.4	7.5	mΩ
		V _{GS} =6 V, I _{DS} =30 A	-	6.8	8.0	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	V _{GS} =0 V, I _{SD} =50 A	-	-	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} =0 V, I _{SD} =50 A, dI _{SD} /dt=100 A/μs	-	148	-	ns
Q _{rr}	Reverse Recovery Charge		-	805	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{DS} =100 V, V _{GS} =0 V, Frequency=1 MHz	-	11636	-	pF
C _{oss}	Output Capacitance		-	468	-	
C _{rss}	Reverse Transfer Capacitance		-	38	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =100 V, V _{GEN} =10 V, R _G =3.9 Ω, R _L =2 Ω, I _{DS} =50 A	-	25	-	ns
t _r	Turn-on Rise Time		-	70	-	
t _{d(off)}	Turn-off Delay Time		-	135	-	
t _f	Turn-off Fall Time		-	84	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} =100 V, V _{GS} =10 V, I _{DS} =50 A	-	186	-	nC
Q _{gs}	Gate-Source Charge		-	58	-	
Q _{gd}	Gate-Drain Charge		-	31	-	

Notes:

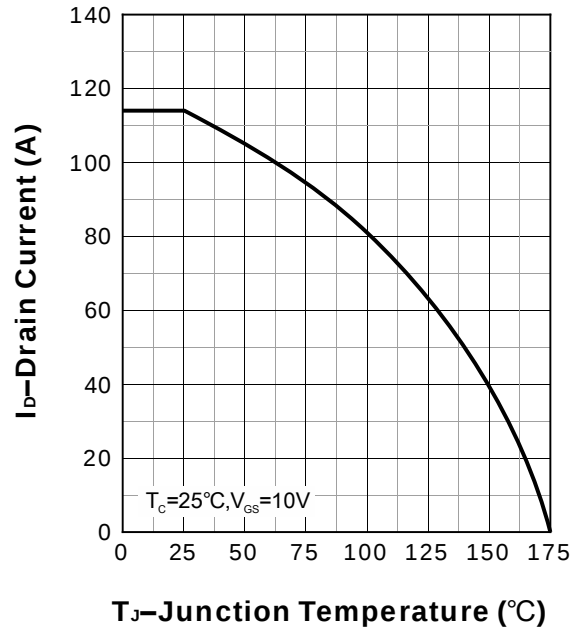
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

7. Typical Characteristics

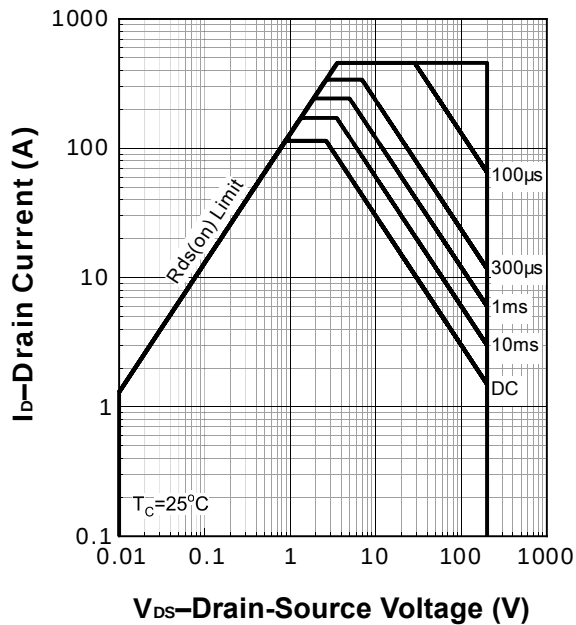
Power Capability



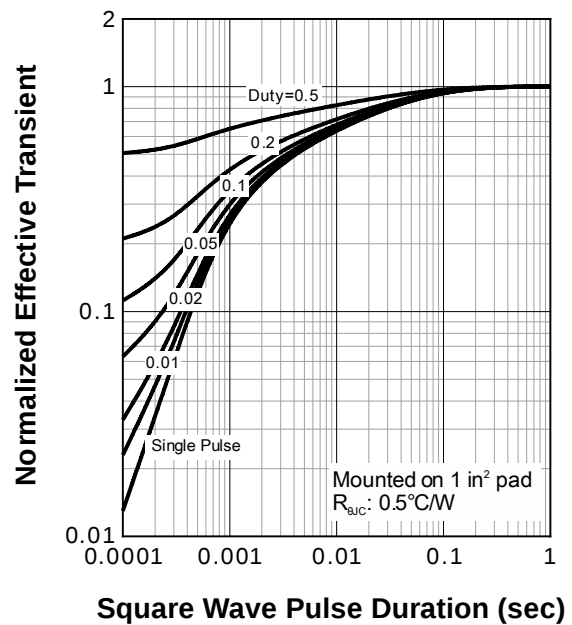
Current Capability



Safe Operating Area

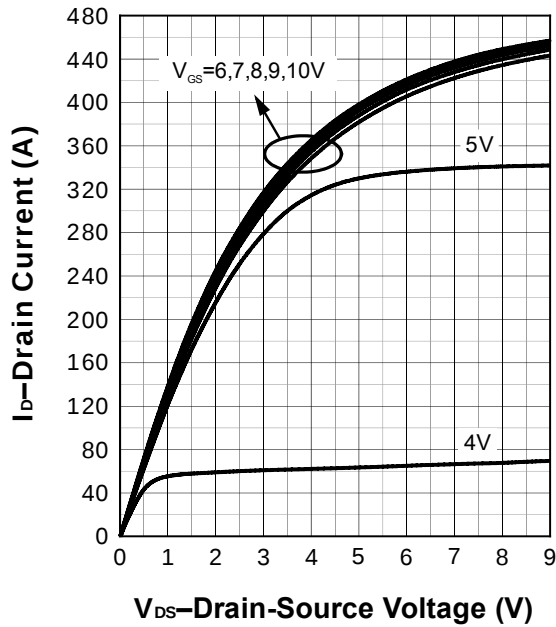


Transient Thermal Impedance

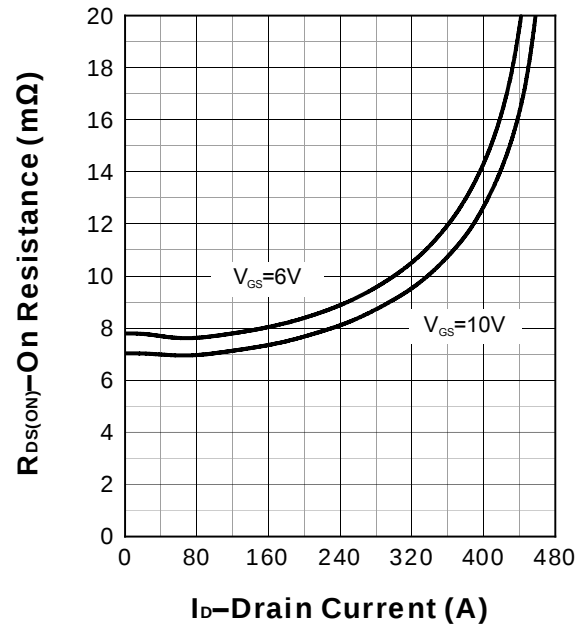


7. Typical Characteristics (cont.)

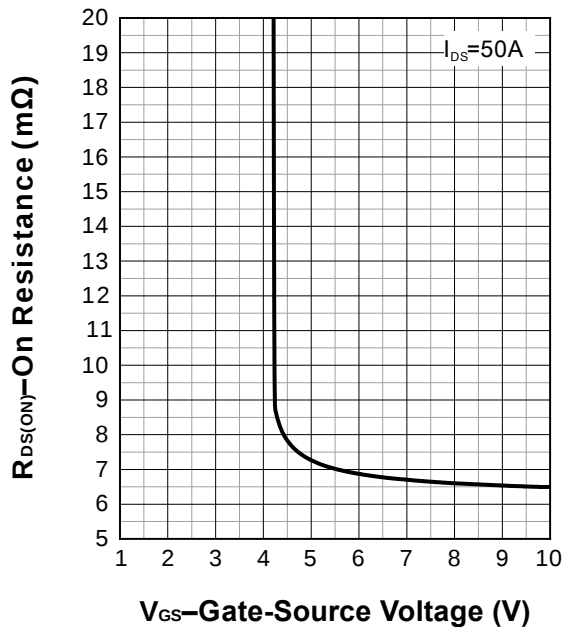
Output Characteristics



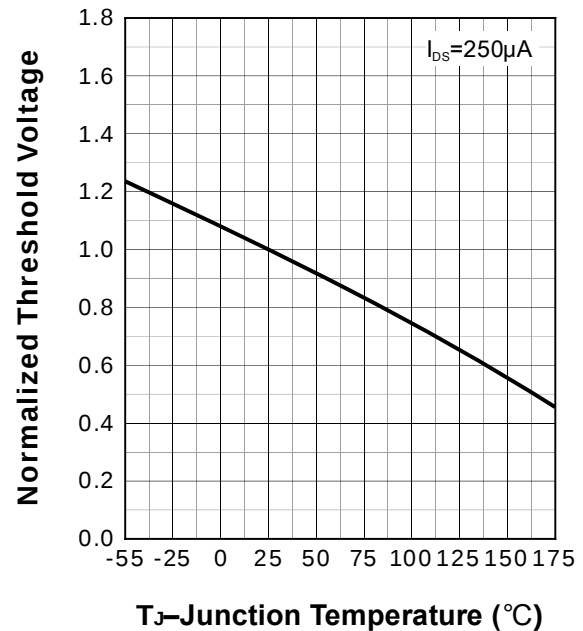
On Resistance



Transfer Characteristics

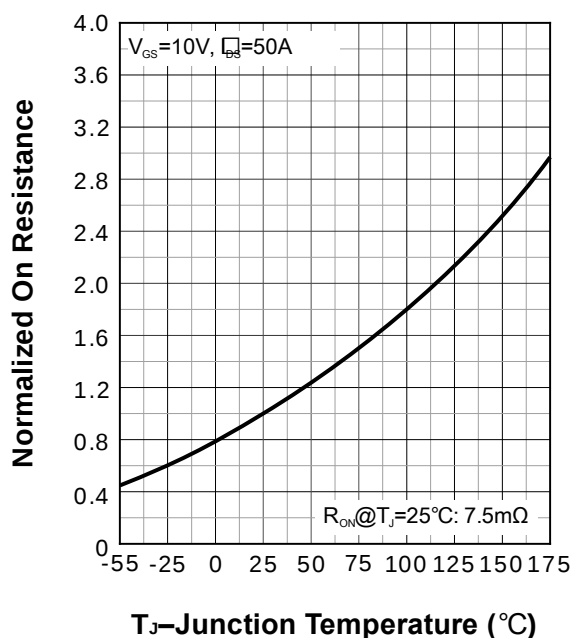


Normalized Threshold Voltage

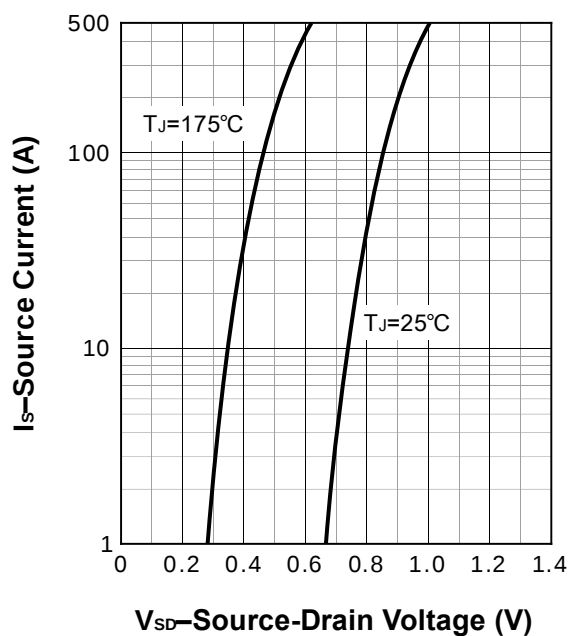


7. Typical Characteristics (cont.)

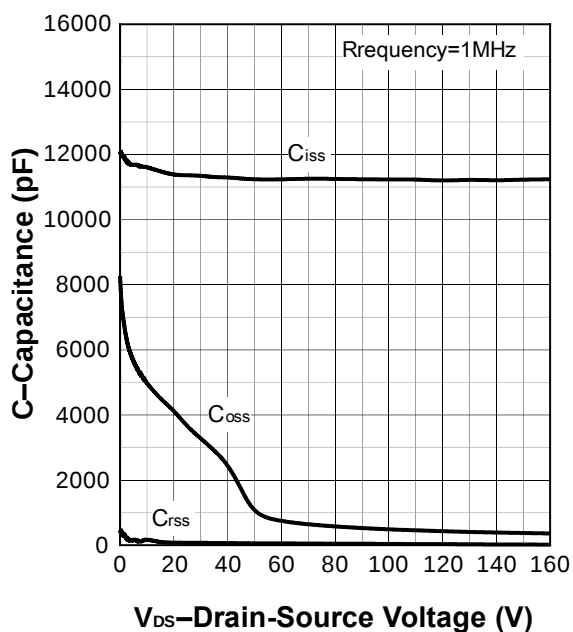
Normalized On Resistance



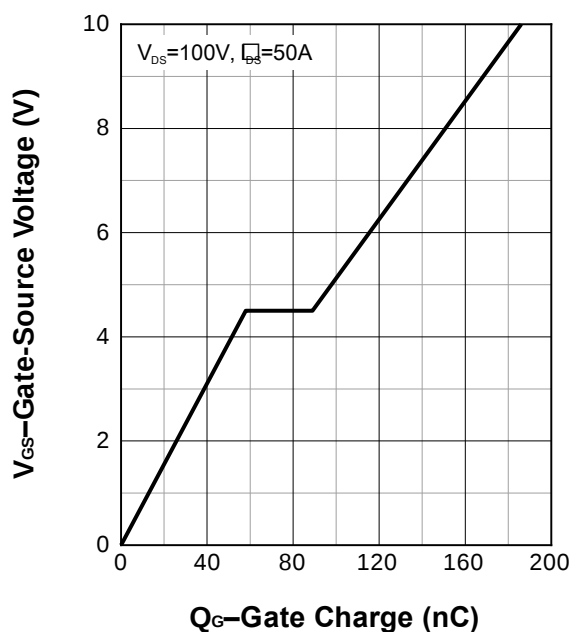
Diode Forward Current



Capacitance

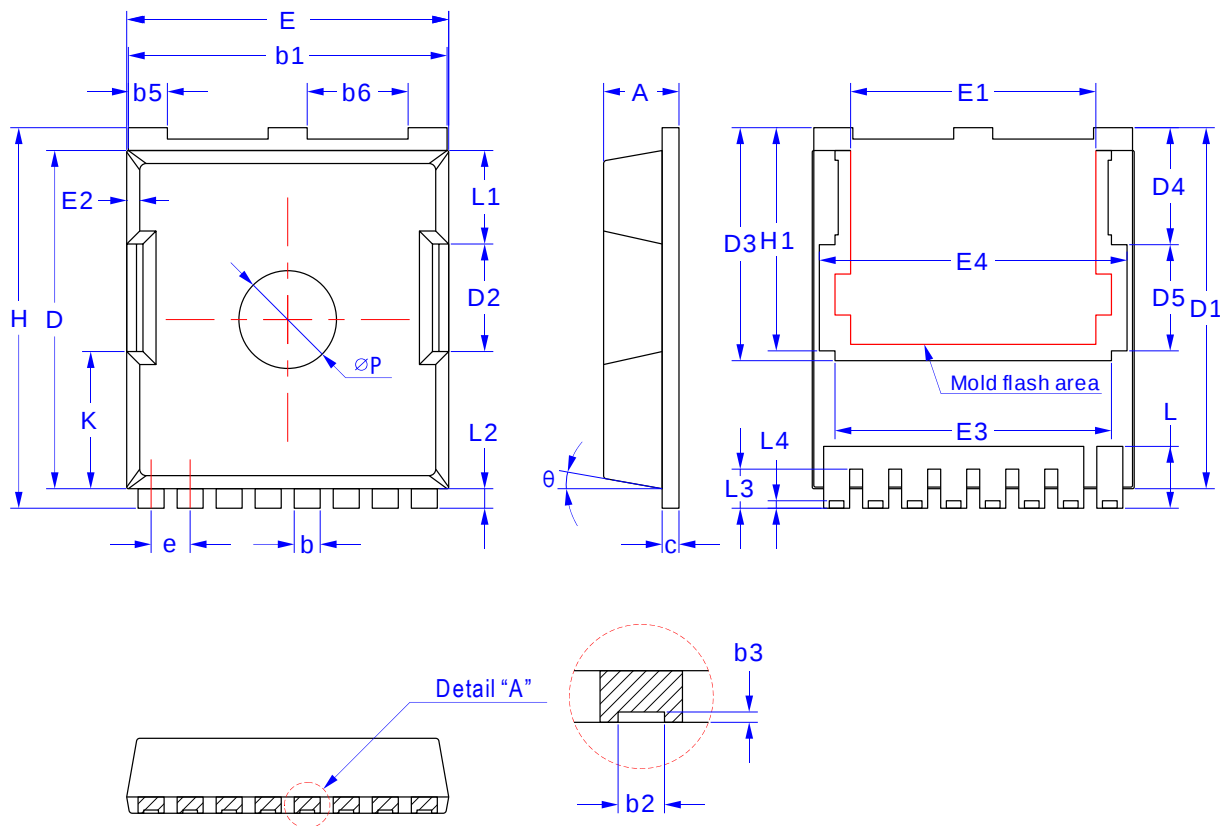


Gate Charge



8. Package Dimensions

TOLL-8L Package



Symbol	Dimensions in Millimeters		
	MIN	NOM	MAX
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.36	0.45	0.55
b3	0.05	0.10	/
b4	0.30	0.40	0.50
b5	1.10	1.20	1.30
b6	3.00	3.10	3.20
c	0.40	0.50	0.60
D	10.28	10.38	10.55
D1	10.98	11.08	11.18
D2	3.20	3.30	3.40
D3		7.15	
D4		3.59	
D5		3.26	
e	1.10	1.20	1.30

Symbol	Dimensions in Millimeters		
	MIN	NOM	MAX
E	9.80	9.90	10.00
E1	7.40	7.50	7.60
E2	0.30	0.40	0.50
E3		8.50	
E4		9.46	
H	11.50	11.68	11.85
H1	6.55	6.65	6.75
K	4.08	4.18	4.28
L	1.60	1.90	2.10
L1	0.50	0.70	0.90
L2	0.50	0.60	0.70
L3	1.00	1.20	1.30
L4	0.13	0.23	0.33
P	2.85	3.00	3.15
θ		10° REF	