

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ✓ Advanced trench cell design
- ✓ Improved dv/dt capability
- ✓ Low thermal resistance
- ✓ MSL1

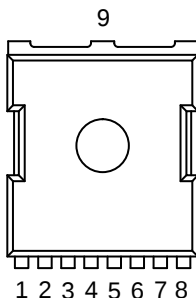
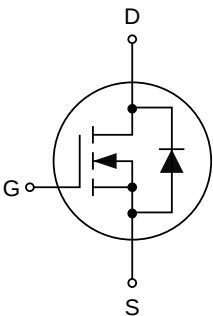
1.2 Applications

- ✓ Motor drivers
- ✓ BMS
- ✓ High power inverter system

1.3 Quick reference

- ✓ $BV \geq 40\text{ V}$
- ✓ $P_D \leq 333\text{ W}$
- ✓ $I_D \leq 460\text{ A}$
- ✓ $R_{DS(ON)} \leq 0.75\text{ m}\Omega @ V_{GS} = 10\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate	 Top View TOLL-8L	
2,3,4,5,6,7,8	Source		
9	Drain		

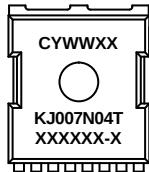
3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C=25^{\circ}\text{C}$	40	-	V
V_{GS}	Gate-Source Voltage	$T_C=25^{\circ}\text{C}$	-	± 20	V
$I_D^{*, **}$	Drain Current (DC)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	460	A
		$T_C=100^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	325	A
$I_{DM}^{*, **, ***}$	Drain Current (Pulsed)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	1380	A
P_D	Drain Power Dissipation	$T_C=25^{\circ}\text{C}$	-	333	W
E_{AS}^{*}	Single Pulsed Avalanche Energy	$V_{DD}=32\text{ V}, L=0.3\text{ mH}$	-	1215	mJ
T_J, T_{stg}	Operating Junction and Storage Temperature Range		-55	175	$^{\circ}\text{C}$
$R_{\theta JA}^{*}$	Thermal Resistance-Junction to Ambient		-	62.5	$^{\circ}\text{C/W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case		-	0.45	

Notes:

- * Surface mounted on 1 in² pad area, $t \leq 10\text{ sec.}$
- ** Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- *** Limited by bonding wire.

4. Marking Information

Product Name	Marking
KJ007N04T	

5. Ordering Code

Product Name	Package	Reel size	Tape width	Quantity (pcs)
KJ007N04T	TOLL-8L	13"	24 mm	2000

Note: KUAIJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

6. Electrical Characteristics (T_A=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0 V, I _{DS} =250 μA	40	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250 μA	1.2	1.7	2.2	V
I _{DSS}	Drain Leakage Current	V _{DS} =40 V, V _{GS} =0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{GS} =0 V, V _{GS} =±20 V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} =10 V, I _{DS} =75 A	-	0.65	0.75	mΩ
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} =75 A, V _{GS} =0 V	-	-	1.2	V
t _{rr}	Reverse Recovery Time	I _{DS} =10 A, V _{GS} =0 V dI _{SD} /dt=100 A/μs	-	141	-	ns
Q _{rr}	Reverse Recovery Charge		-	333	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} =0 V, V _{DS} =15 V f=1 MHz	-	8600	-	pF
C _{oss}	Output Capacitance		-	6360	-	
C _{rss}	Reverse Transfer Capacitance		-	180	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =17.5 V, V _{GEN} =10 V, R _G =5 Ω, R _L =0.235 Ω, I _{DS} =75 A	-	19	-	ns
t _r	Turn-on Rise Time		-	26	-	
t _{d(off)}	Turn-off Delay Time		-	85	-	
t _f	Turn-off Fall Time		-	45	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} =32 V, V _{GS} =10 V, I _{DS} =75 A	-	130	-	nC
Q _{gs}	Gate-Source Charge		-	43	-	
Q _{gd}	Gate-Drain Charge		-	29	-	

Notes:

- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

7. Typical Characteristics

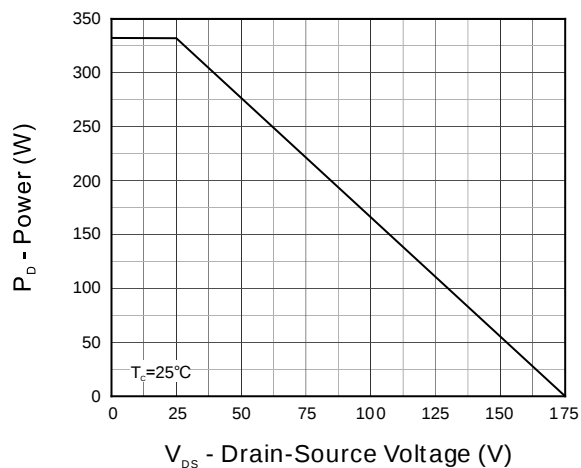


Figure 1. Output Characteristics

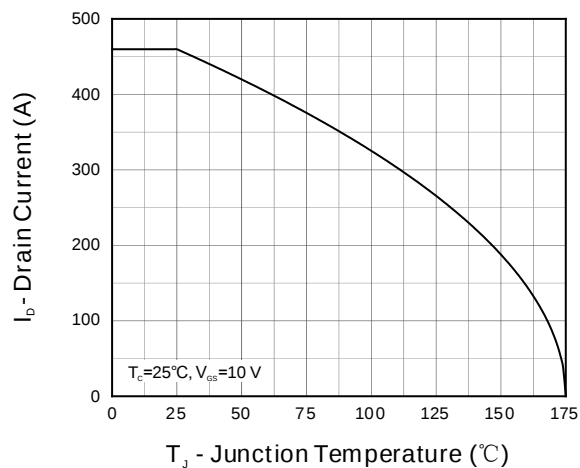


Figure 2. Current Capability

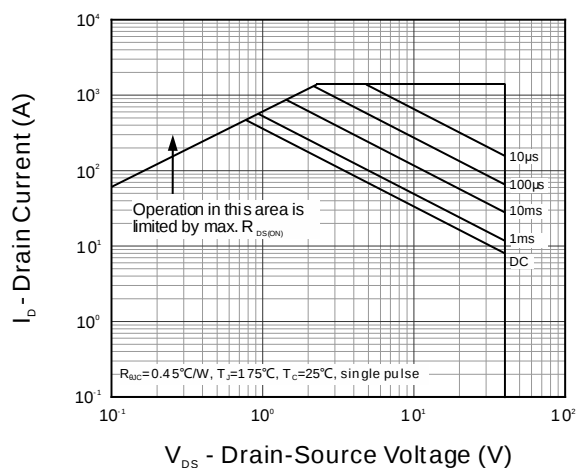


Figure 3. Safe Operation Area

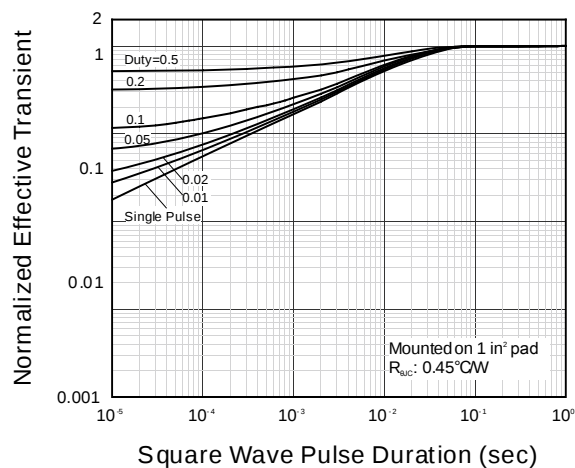


Figure 4. Transient Thermal Impedance

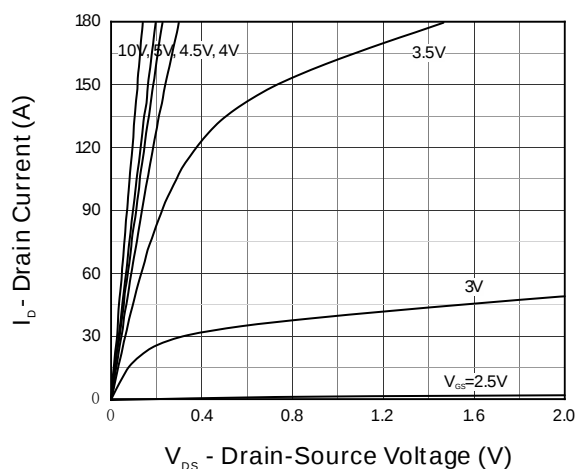


Figure 5. Output Characteristics

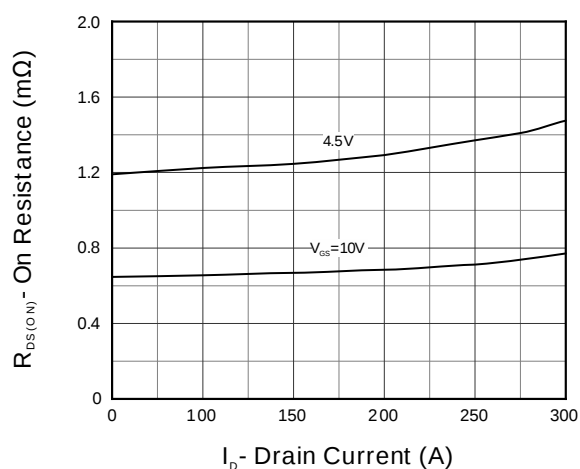


Figure 6. On Resistance

7. Typical Characteristics (cont.)

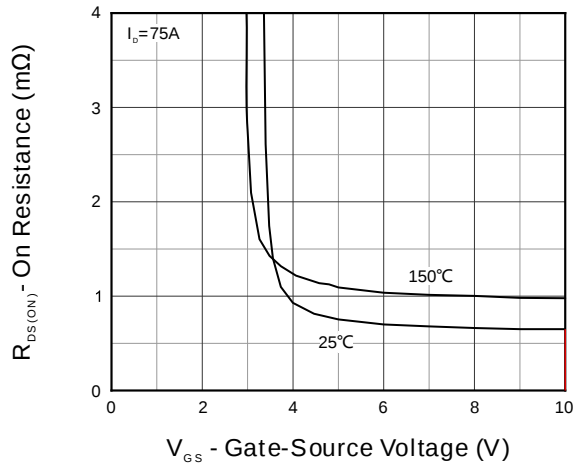


Fig 7. Transfer Characteristics

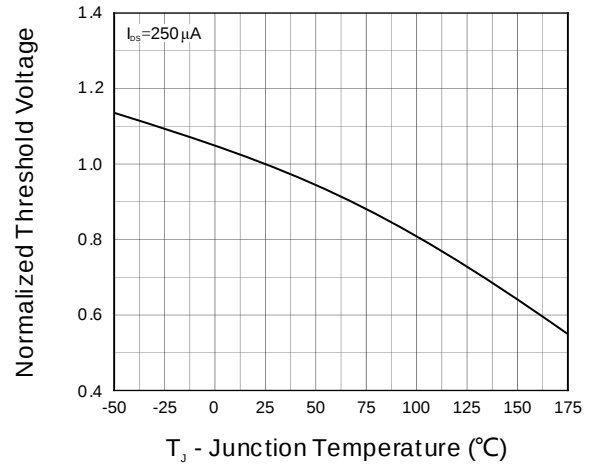


Fig 8. Normalized Threshold Voltage

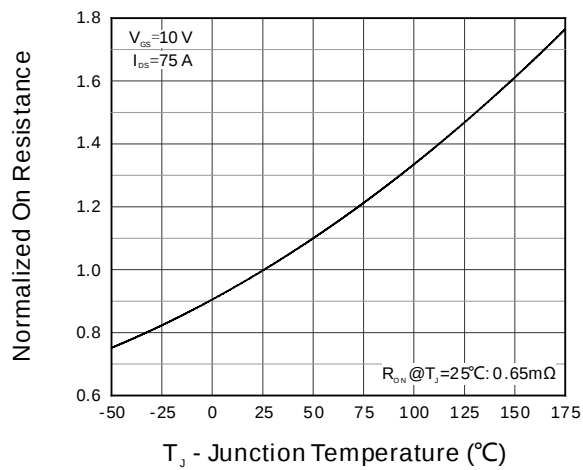


Fig 9. Normalized On Resistance

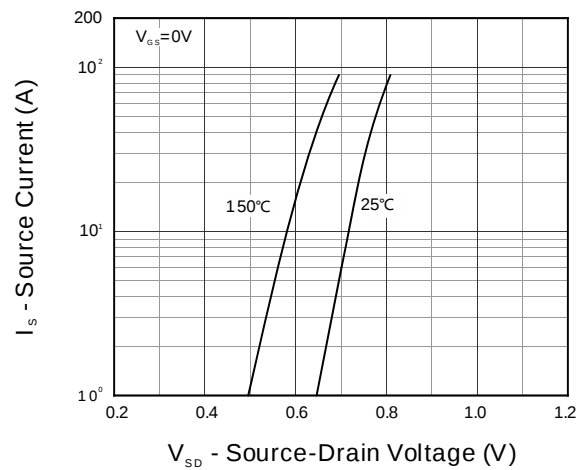


Fig 10. Diode Forward Current

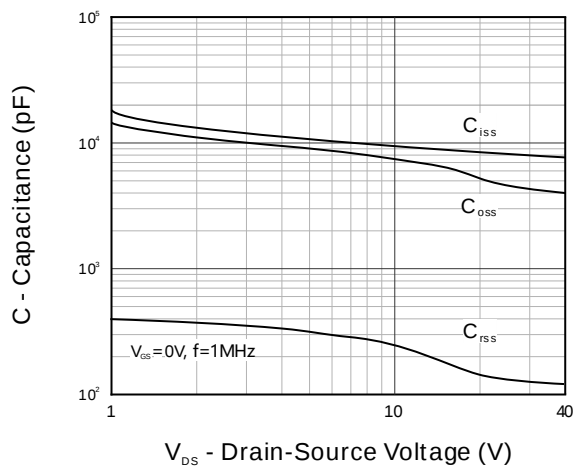


Fig 11. Capacitance

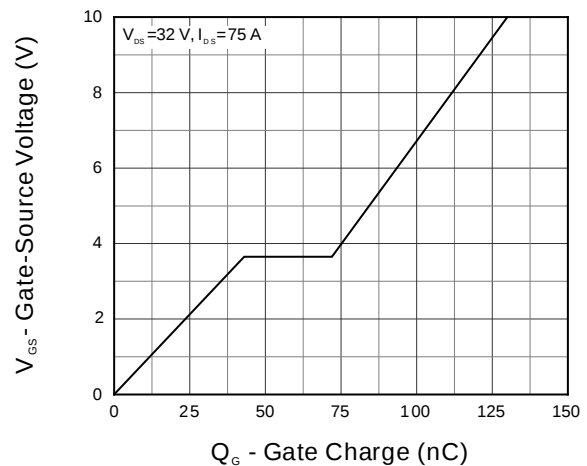
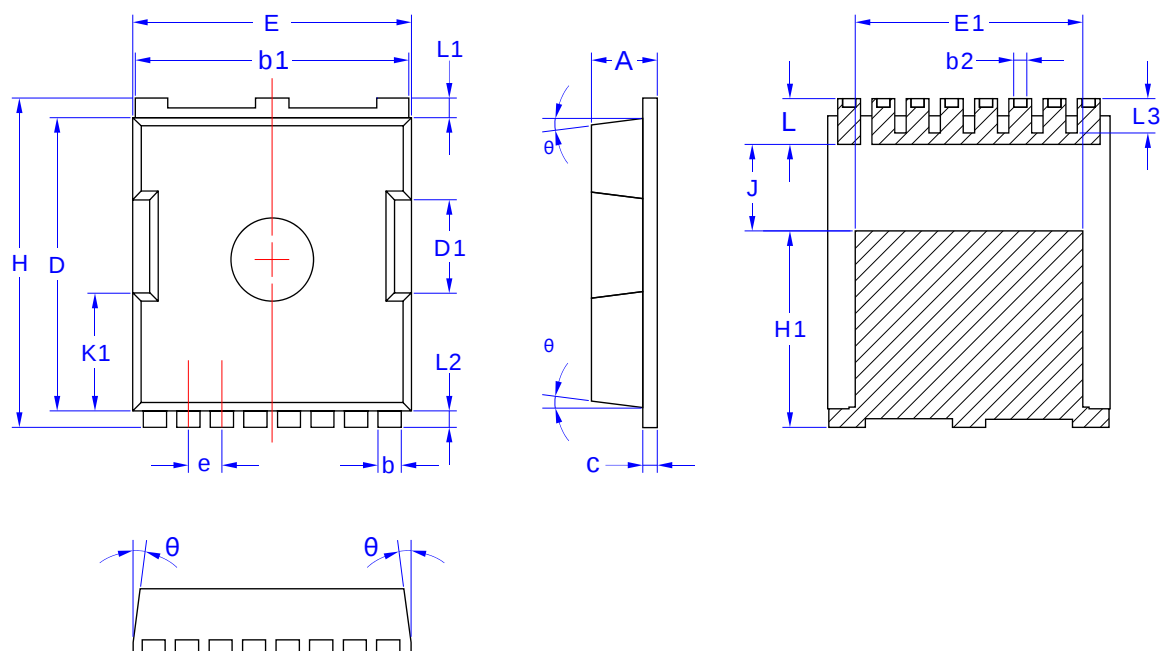


Fig 12. Gate Charge

8. Package Dimensions

TOLL-8L Package



Symbol	Dimensions in Millimeters	
	MIN	MAX
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D1	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20 BSC	

Symbol	Dimensions in Millimeters	
	MIN	MAX
H	11.48	11.88
H1	6.75	7.15
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L3	1.00	1.30
θ	4°	10°