

## Dual N-Channel Enhancement Mode MOSFET

### 1. Product Information

#### 1.1 Features

- ☒ Advanced trench cell design
- ☒ Low thermal resistance

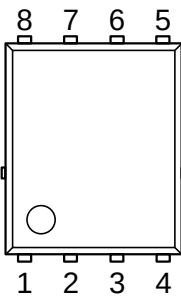
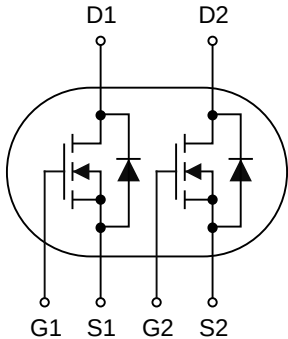
#### 1.2 Applications

- ☒ Motor drivers
- ☒ DC/DC converter

#### 1.3 Quick reference

- ☒  $BV \geq 40\text{ V}$
- ☒  $P_D \leq 35\text{ W}$
- ☒  $I_D \leq 61\text{ A}$
- ☒  $R_{DS(ON)} \leq 6\text{ m}\Omega @V_{GS} = 10\text{ V}$
- ☒  $R_{DS(ON)} \leq 10\text{ m}\Omega @V_{GS} = 4.5\text{ V}$

### 2. Pin Description

| Pin | Description | Simplified Outline                                                                                                 | Symbol                                                                                |
|-----|-------------|--------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|
| 1   | Source (S1) |  <p>Top View<br/>PDFN5x6-8L</p> |  |
| 2   | Gate (G1)   |                                                                                                                    |                                                                                       |
| 3   | Source (S2) |                                                                                                                    |                                                                                       |
| 4   | Gate (G2)   |                                                                                                                    |                                                                                       |
| 5,6 | Drain (D2)  |                                                                                                                    |                                                                                       |
| 7,8 | Drain (D1)  |                                                                                                                    |                                                                                       |

## 3. Limiting Values

| Symbol               | Parameter                                        | Conditions                                    | Min | Max      | Unit                 |
|----------------------|--------------------------------------------------|-----------------------------------------------|-----|----------|----------------------|
| $V_{DS}$             | Drain-Source Voltage                             | $T_C=25^{\circ}\text{C}$                      | 40  | -        | V                    |
| $V_{GS}$             | Gate-Source Voltage                              | $T_C=25^{\circ}\text{C}$                      | -   | $\pm 20$ | V                    |
| $I_D^{***}$          | Drain Current (DC)                               | $T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$  | -   | 61       | A                    |
|                      |                                                  | $T_C=100^{\circ}\text{C}, V_{GS}=10\text{ V}$ | -   | 39       | A                    |
| $I_{DM}^{*,***}$     | Drain Current (Pulsed)                           | $T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$  | -   | 244      | A                    |
| $P_D$                | Drain Power Dissipation                          | $T_C=25^{\circ}\text{C}$                      | -   | 35       | W                    |
| $I_S$                | Continuous-Source Current                        | $T_C=25^{\circ}\text{C}$                      | -   | 61       | A                    |
| $E_{AS}$             | Single Pulsed Avalanche Energy                   | $V_{DD}=32\text{V}, L=0.3\text{ mH}$          | -   | 25       | mJ                   |
| $T_J, T_{stg}$       | Operating Junction and Storage Temperature Range |                                               | -55 | 150      | $^{\circ}\text{C}$   |
| $R_{\theta JA}$      | Thermal Resistance-Junction to Ambient           |                                               | -   | 62.5     | $^{\circ}\text{C/W}$ |
| $R_{\theta JC}^{**}$ | Thermal Resistance-Junction to Case              |                                               | -   | 3.5      |                      |

Notes:

- \* Pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- \*\* Mounted on large heat sink.
- \*\*\* Limited by bonding wire.

## 4. Marking Information

| Product Name | Marking            |
|--------------|--------------------|
| KJ0504DG     | KJ0504DG<br>XXXXXX |

## 5. Ordering Code

| Product Name | Package     | Reel size | Tape width | Quantity (pcs) |
|--------------|-------------|-----------|------------|----------------|
| KJ0504DG     | PDFN 5x6-8L | 13"       | 12 mm      | 5000           |

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

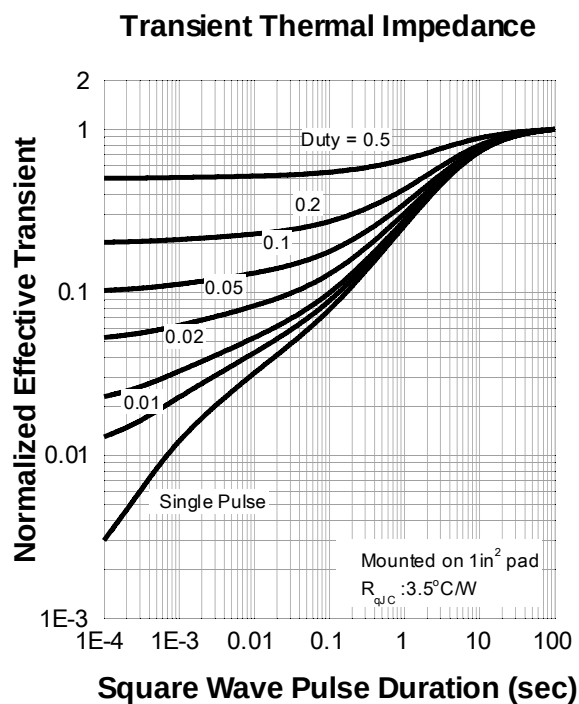
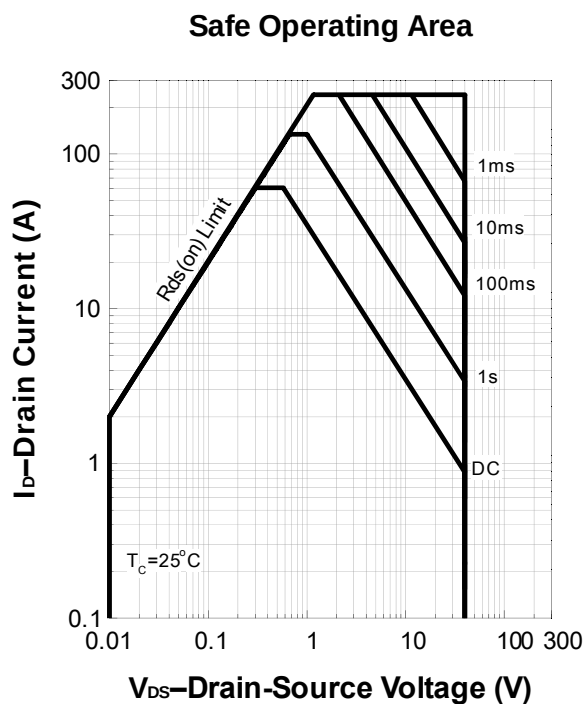
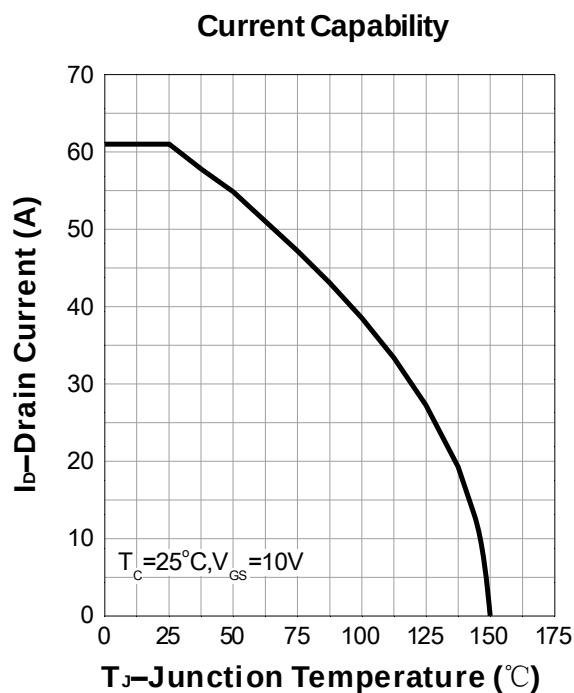
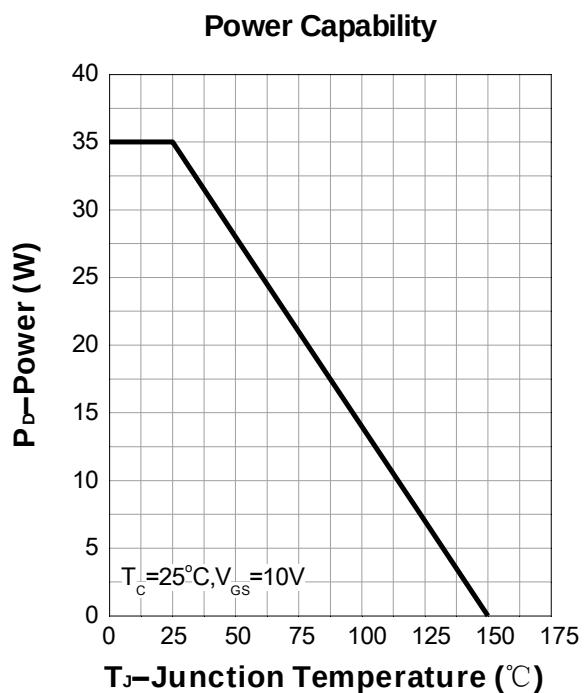
## 6. Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

| Symbol                                   | Parameter                      | Conditions                                                                                                             | Min | Typ | Max  | Unit |
|------------------------------------------|--------------------------------|------------------------------------------------------------------------------------------------------------------------|-----|-----|------|------|
| Static Characteristics                   |                                |                                                                                                                        |     |     |      |      |
| BV <sub>DSS</sub>                        | Drain-Source Breakdown Voltage | V <sub>GS</sub> =0 V, I <sub>DS</sub> =250 μA                                                                          | 40  | -   | -    | V    |
| V <sub>GS(th)</sub>                      | Gate Threshold Voltage         | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250 μA                                                             | 1   | -   | 2.2  | V    |
| I <sub>DSS</sub>                         | Drain Leakage Current          | V <sub>DS</sub> =40 V, V <sub>GS</sub> =0 V                                                                            | -   | -   | 1    | μA   |
| I <sub>GSS</sub>                         | Gate Leakage Current           | V <sub>DS</sub> =0 V, V <sub>GS</sub> =±20 V                                                                           | -   | -   | ±100 | nA   |
| R <sub>DS(ON)</sub> <sup>a</sup>         | On-State Resistance            | V <sub>GS</sub> =10 V, I <sub>DS</sub> =15 A                                                                           | -   | 4.5 | 6    | mΩ   |
|                                          |                                | V <sub>GS</sub> =4.5 V, I <sub>DS</sub> =8 A                                                                           | -   | 7.5 | 10   |      |
| Diode Characteristics                    |                                |                                                                                                                        |     |     |      |      |
| V <sub>SD</sub> <sup>a</sup>             | Diode Forward Voltage          | I <sub>SD</sub> =15 A, V <sub>GS</sub> =0 V                                                                            | -   | 0.8 | 1.2  | V    |
| t <sub>rr</sub>                          | Reverse Recovery Time          | I <sub>DS</sub> =15 A, V <sub>GS</sub> =0 V,<br>dI <sub>SD</sub> /dt=100 A/μs                                          | -   | 21  | -    | ns   |
| Q <sub>rr</sub>                          | Reverse Recovery Charge        |                                                                                                                        | -   | 7   | -    | nC   |
| Dynamic Characteristics <sup>b</sup>     |                                |                                                                                                                        |     |     |      |      |
| C <sub>iss</sub>                         | Input Capacitance              | V <sub>GS</sub> =0 V, V <sub>DS</sub> =20 V,<br>f=1 MHz                                                                | -   | 930 | -    | pF   |
| C <sub>oss</sub>                         | Output Capacitance             |                                                                                                                        | -   | 500 | -    |      |
| C <sub>rss</sub>                         | Reverse Transfer Capacitance   |                                                                                                                        | -   | 30  | -    |      |
| t <sub>d(on)</sub>                       | Turn-on Delay Time             | V <sub>DS</sub> =20 V, V <sub>GEN</sub> =10 V,<br>R <sub>G</sub> =3.9 Ω, R <sub>L</sub> =1 Ω,<br>I <sub>DS</sub> =15 A | -   | 6   | -    | ns   |
| t <sub>r</sub>                           | Turn-on Rise Time              |                                                                                                                        | -   | 26  | -    |      |
| t <sub>d(off)</sub>                      | Turn-off Delay Time            |                                                                                                                        | -   | 27  | -    |      |
| t <sub>f</sub>                           | Turn-off Fall Time             |                                                                                                                        | -   | 24  | -    |      |
| Gate Charge Characteristics <sup>b</sup> |                                |                                                                                                                        |     |     |      |      |
| Q <sub>g</sub>                           | Total Gate Charge              | V <sub>DS</sub> =20 V, V <sub>GS</sub> =10 V,<br>I <sub>DS</sub> =15 A                                                 | -   | 16  | -    | nC   |
| Q <sub>gs</sub>                          | Gate-Source Charge             |                                                                                                                        | -   | 4   | -    |      |
| Q <sub>gd</sub>                          | Gate-Drain Charge              |                                                                                                                        | -   | 8   | -    |      |

Notes:

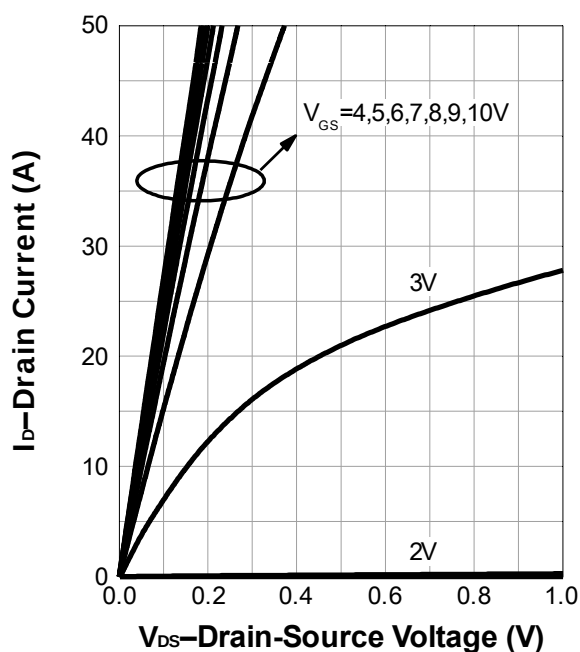
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

## 7. Typical Characteristics

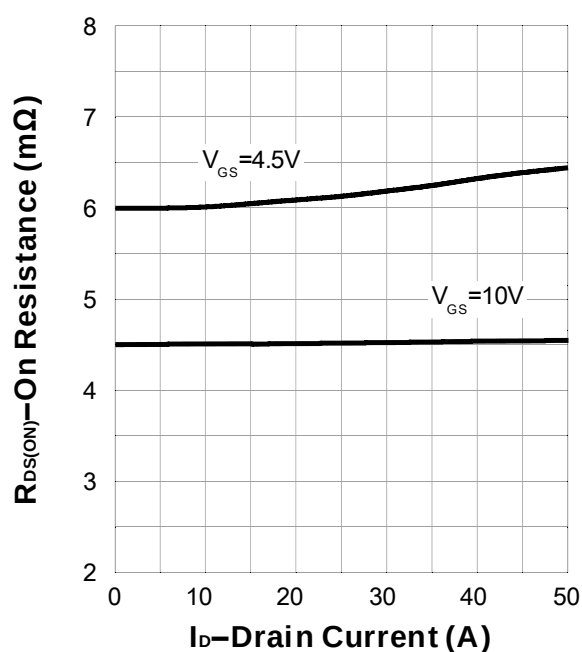


## 7. Typical Characteristics (cont.)

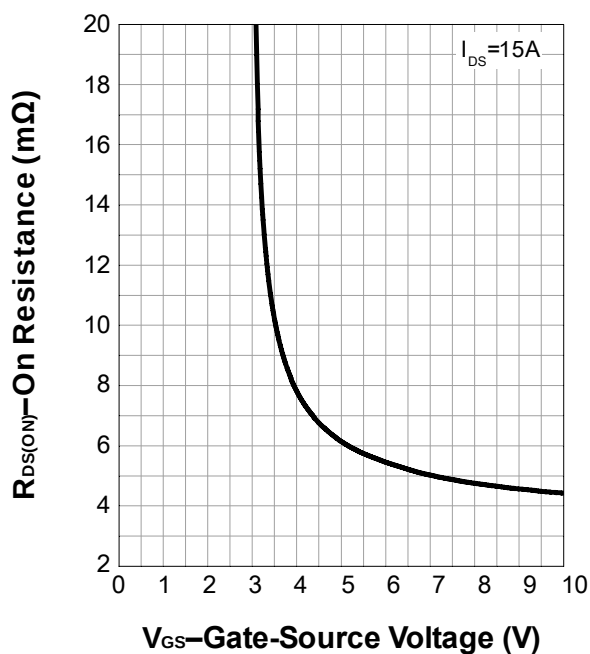
Output Characteristics



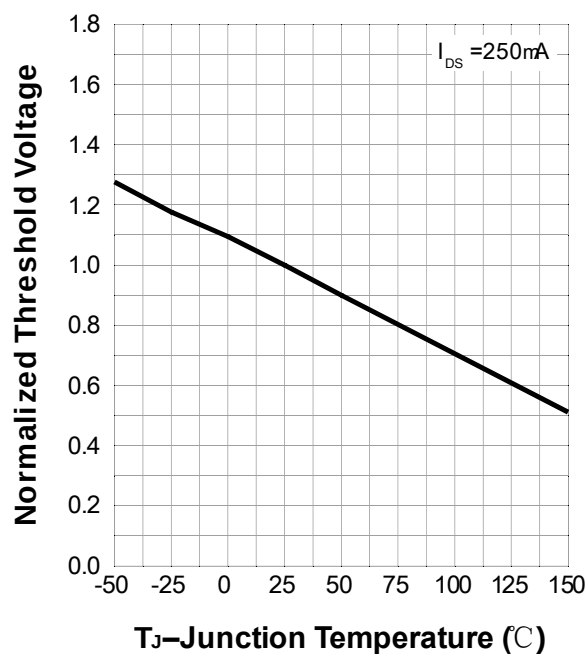
On Resistance



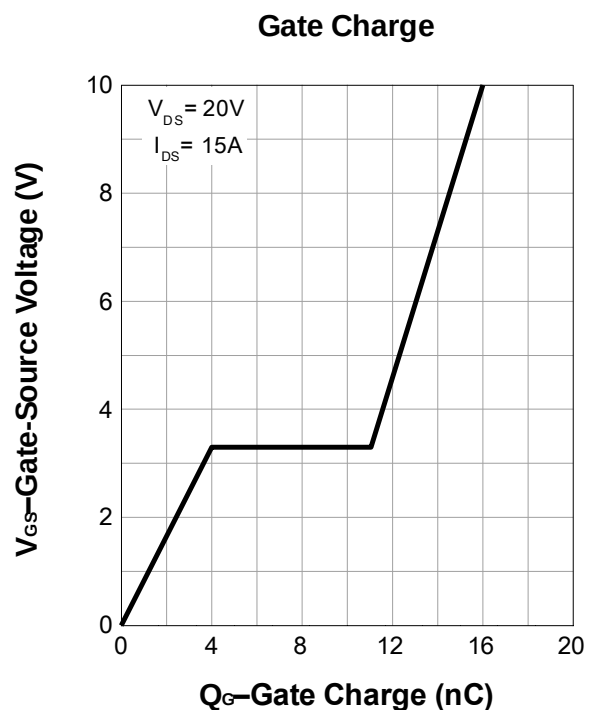
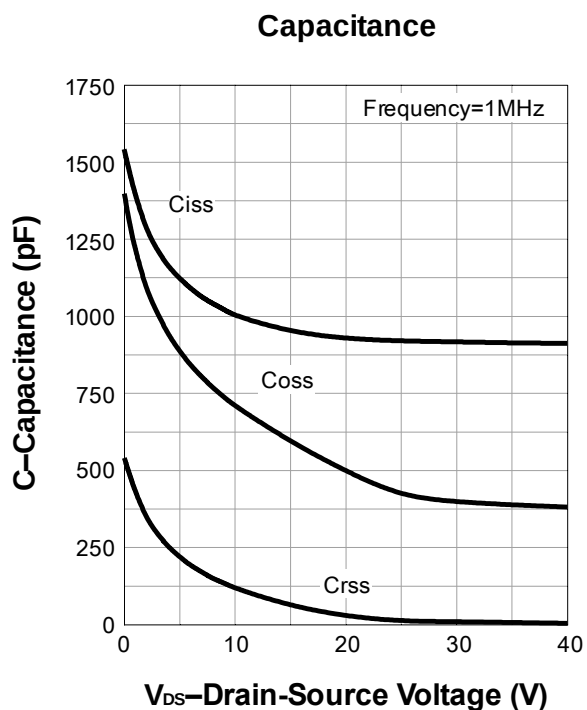
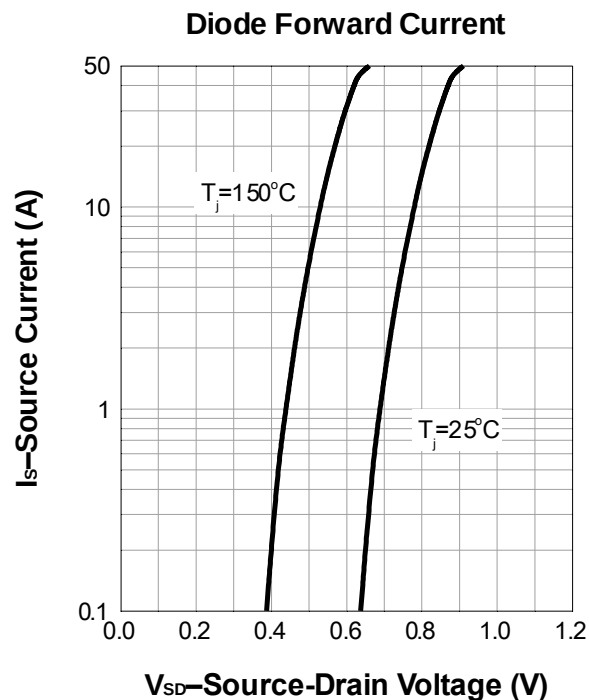
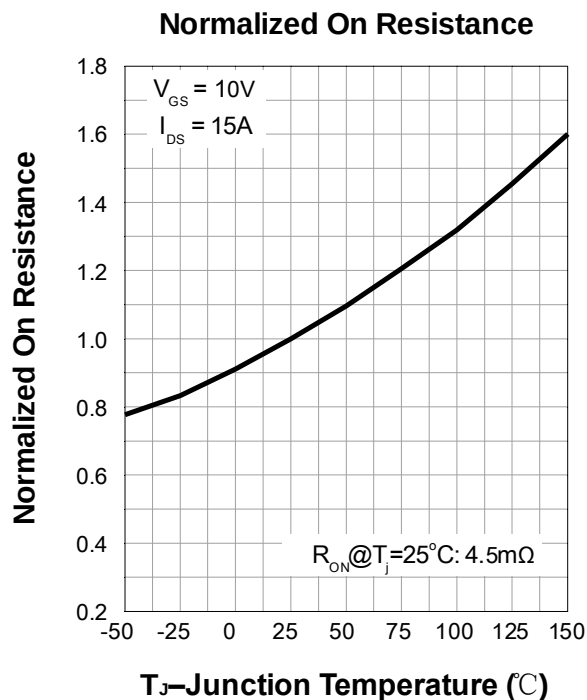
Transfer Characteristics



Normalized Threshold Voltage

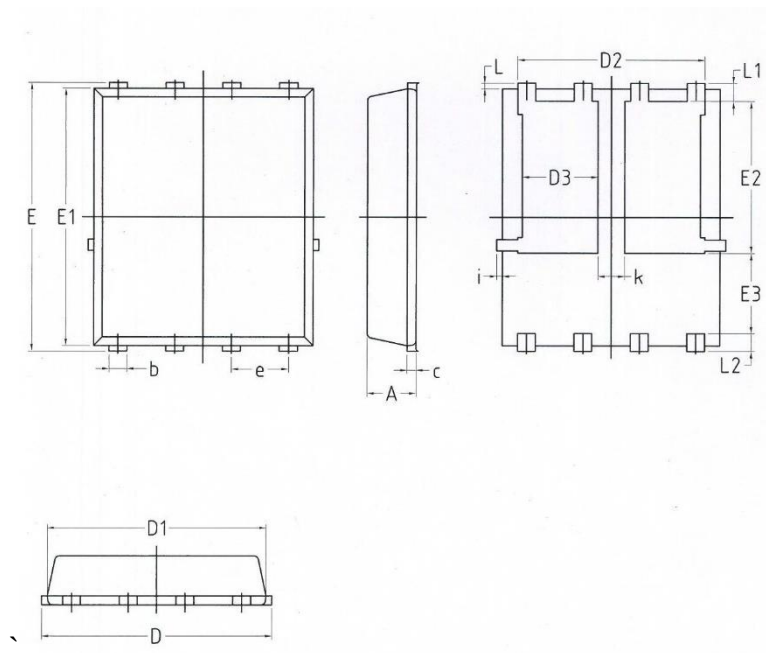


## 7. Typical Characteristics (cont.)



## 8. Package Dimensions

PDFN 5x6-8L dual Package



| Symbol | Dimensions In Millimeters |      |
|--------|---------------------------|------|
|        | MIN                       | MAX  |
| A      | 1.03                      | 1.17 |
| B      | 0.34                      | 0.48 |
| c      | 0.203 BSC                 |      |
| D      | 4.80                      | 5.40 |
| D1     | 4.80                      | 5.00 |
| D2     | 4.11                      | 4.31 |
| D3     | 1.60                      | 1.80 |
| E      | 5.95                      | 6.15 |
| E1     | 5.65                      | 5.85 |
| E2     | 3.30                      | 3.50 |
| E3     | 1.70                      | -    |
| e      | 1.27 BSC                  |      |
| L      | 0.05                      | 0.25 |
| L1     | 0.38                      | 0.5  |
| L2     | 0.38                      | 0.5  |
| i      | -                         | 0.18 |
| k      | 0.5                       | 0.7  |