

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☑ Advanced Trench Technology
- ☑ Low Thermal Resistance

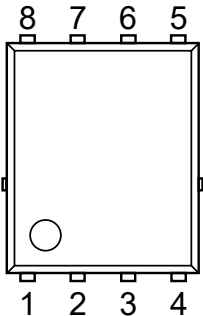
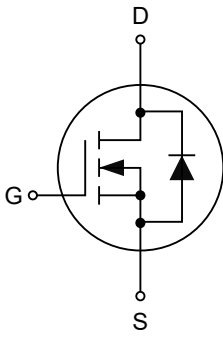
1.2 Applications

- ☑ Motor drivers
- ☑ DC-DC Converter

1.3 Quick reference

- ☑ $BV \geq 40\text{ V}$
- ☑ $P_{tot} \leq 20.8\text{ W}$
- ☑ $I_D \leq 40\text{ A}$
- ☑ $R_{DS(ON)} \leq 7\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- ☑ $R_{DS(ON)} \leq 11\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source	 Top View PDFN5x6-8L	
4	Gate		
5,6,7,8	Drain		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C =25°C	40	-	V
V _{GS}	Gate-Source Voltage	T _C =25°C	-	±20	V
I _D ^{*,***}	Drain Current (DC)	T _C =25°C, V _{GS} =10 V	-	40	A
		T _C =100°C, V _{GS} =10 V	-	26	A
I _{DM} [*]	Pulsed Source Current	T _C =25°C, V _{GS} =10 V	-	120	A
P _{tot}	Total Power Dissipation	T _C =25°C	-	20.8	W
T _J , T _{stg}	Operating Junction and Storage Temperature Range		-55	150	°C
E _{AS} [*]	Single Pulsed Avalanche Energy	V _{DD} =40 V, L=0.3 mH	-	51	mJ
R _{θJA} ^{**}	Thermal Resistance, Junction to Ambient		-	62.5	°C/W
R _{θJC} ^{**}	Thermal Resistance, Junction to Case		-	6	

Notes:

- * Pulse width ≤ 300 μs, duty cycle ≤ 2%.
- ** Surface mounted on 1 in² pad area, t ≤ 10 sec.
- *** Limited by maximum junction temperature.

4. Marking Information

Product Name	Marking
KJ0604G	KJ0604G XXXXXX

5. Ordering Code

Product Name	Package	Reel size	Tape width	Quantity (pcs)
KJ0604G	PDFN 5×6-8L	13"	12 mm	5000

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

6. Electrical Characteristics (T_J=25°C unless otherwise noted)

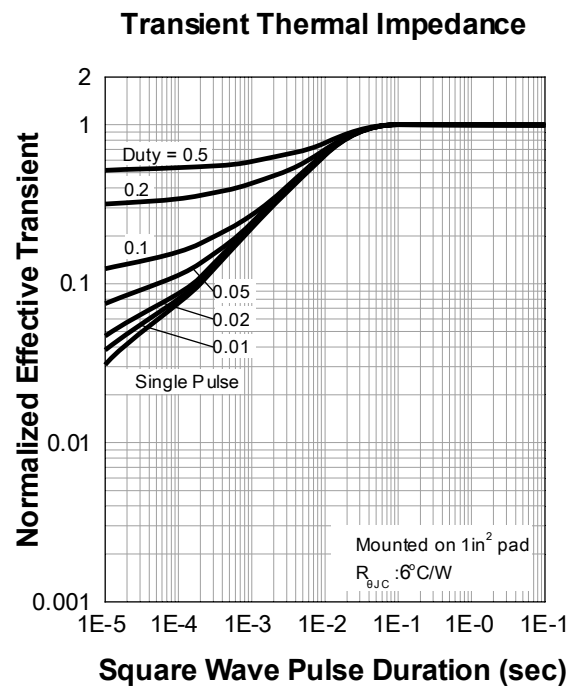
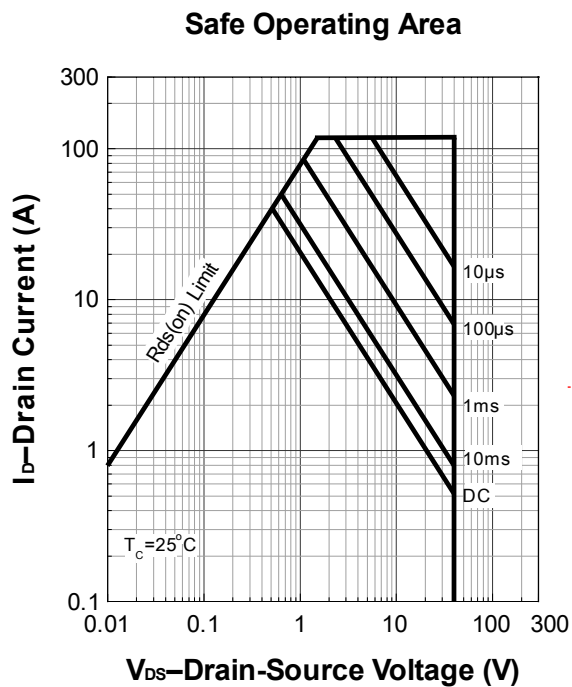
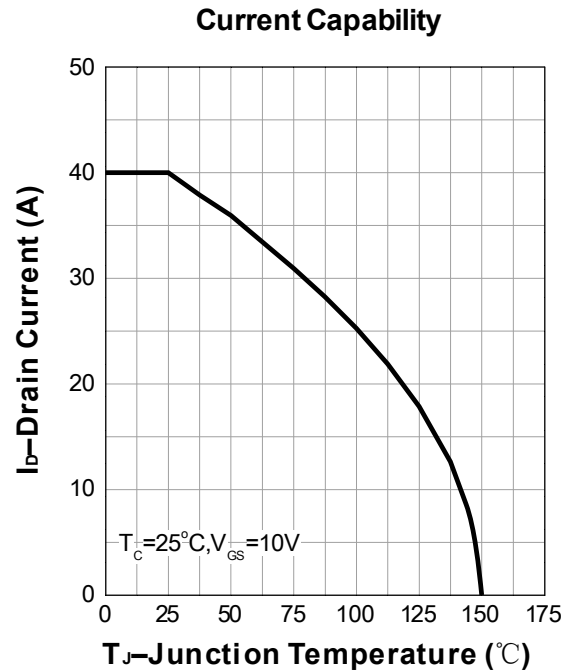
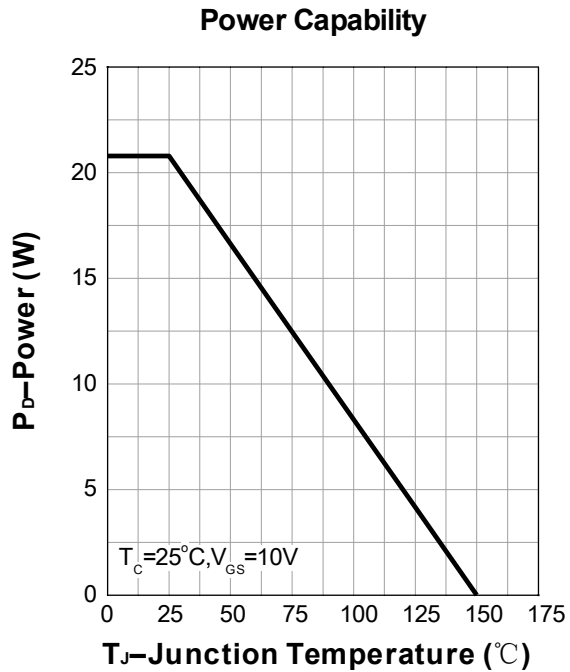
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0 V, I _{DS} =250 μA	40	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250 μA	1.0	-	2.2	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40 V, V _{GS} =0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{DS} =0 V, V _{GS} =±20 V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} =10 V, I _{DS} =15 A	-	6	7	mΩ
		V _{GS} =4.5 V, I _{DS} =12 A	-	8.5	11	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} =15 A, V _{GS} =0 V	-	0.8	1.2	V
t _{rr}	Reverse Recovery Time	I _{DS} =15 A, V _{GS} =0V, dI _{SD} /dt=100 A/μs	-	21	-	ns
Q _{rr}	Reverse Recovery Charge		-	7	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} =0 V, V _{DS} =20 V, Frequency=1.0 MHz	-	630	-	pF
C _{oss}	Output Capacitance		-	330	-	
C _{rss}	Reverse Transfer Capacitance		-	23	-	
R _g	Gate resistance		-	4	-	Ω
t _{d(on)}	Turn-on Delay Time	V _{DS} =20 V, V _{GEN} =10 V, R _G =3 Ω, I _{DS} =15 A	-	12	-	ns
t _r	Turn-on Rise Time		-	32	-	
t _{d(off)}	Turn-off Delay Time		-	20	-	
t _f	Turn-off Fall Time		-	8	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} =20 V, V _{GS} =10 V, I _{DS} =15 A	-	17	-	nC
Q _{gs}	Gate-Source Charge		-	5	-	
Q _{gd}	Gate-Drain Charge		-	4	-	

Notes:

a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.

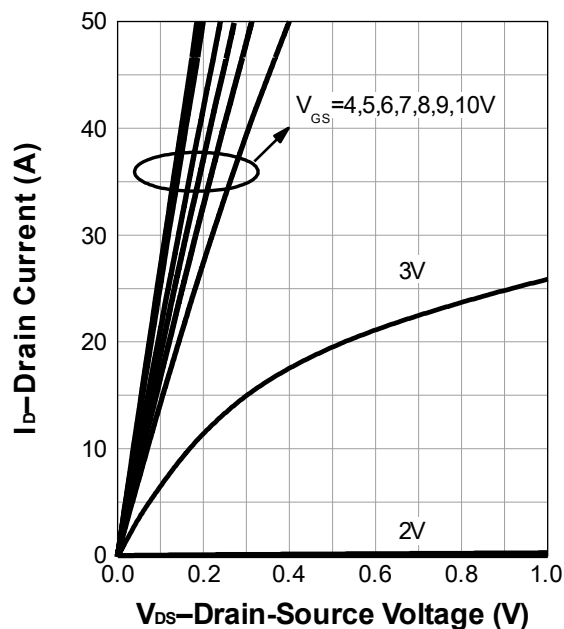
b. Guaranteed by design, not subject to production testing.

7. Typical Characteristics

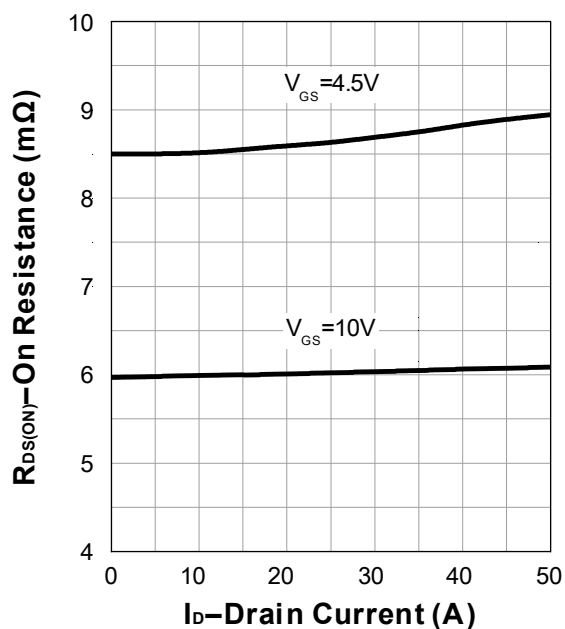


7. Typical Characteristics (cont.)

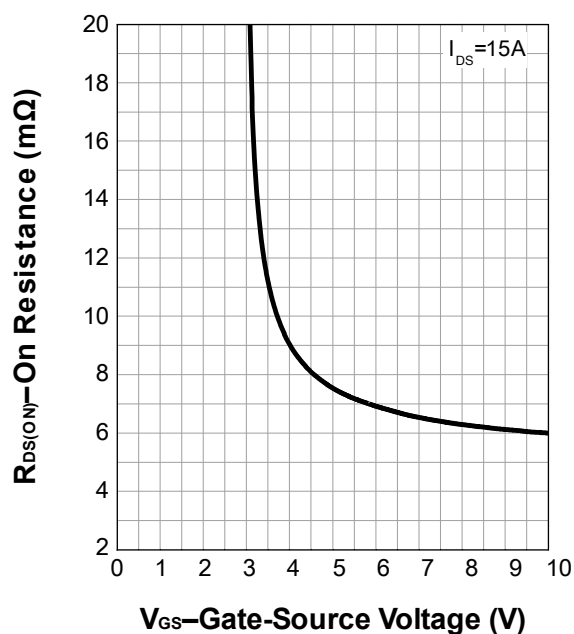
Output Characteristics



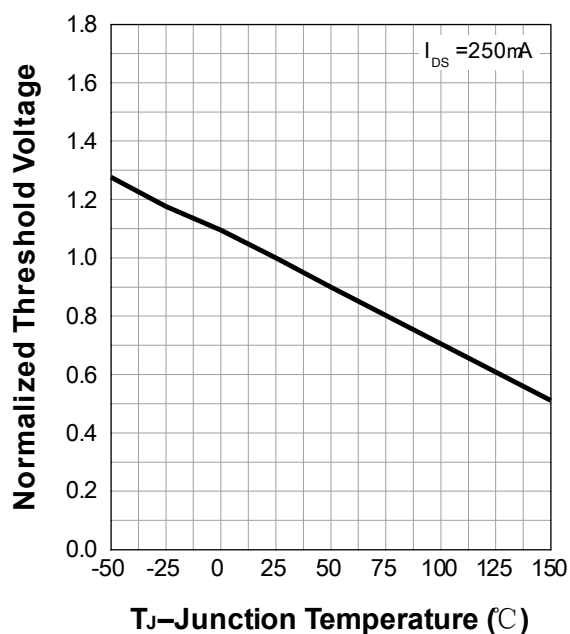
On Resistance



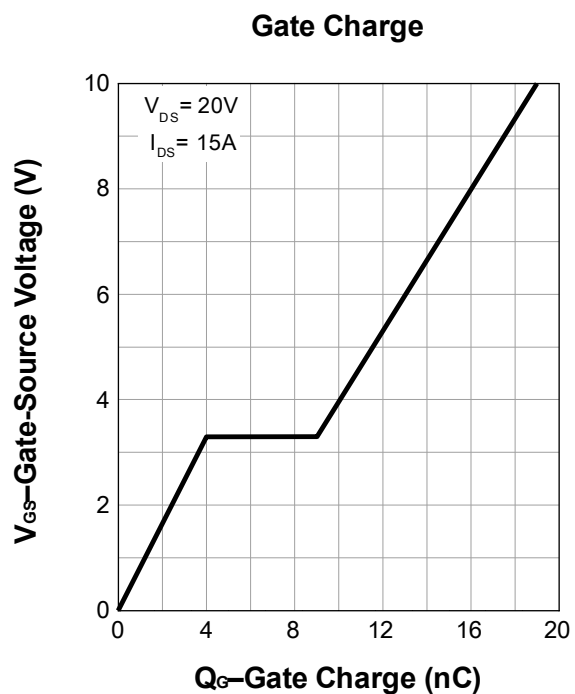
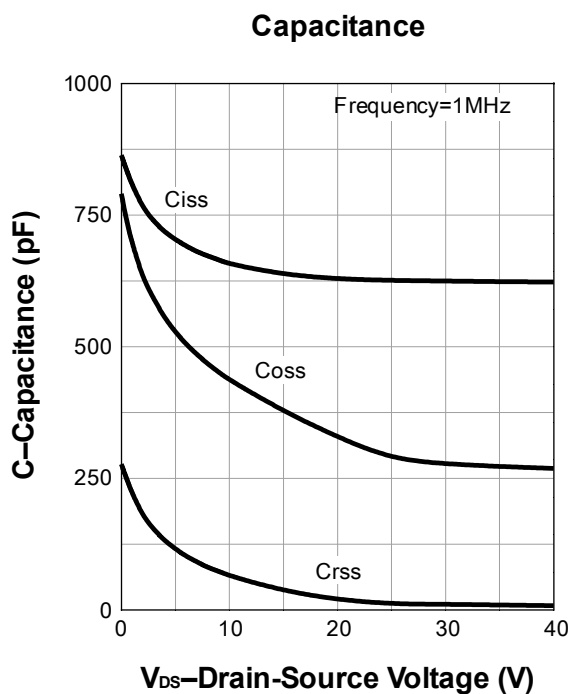
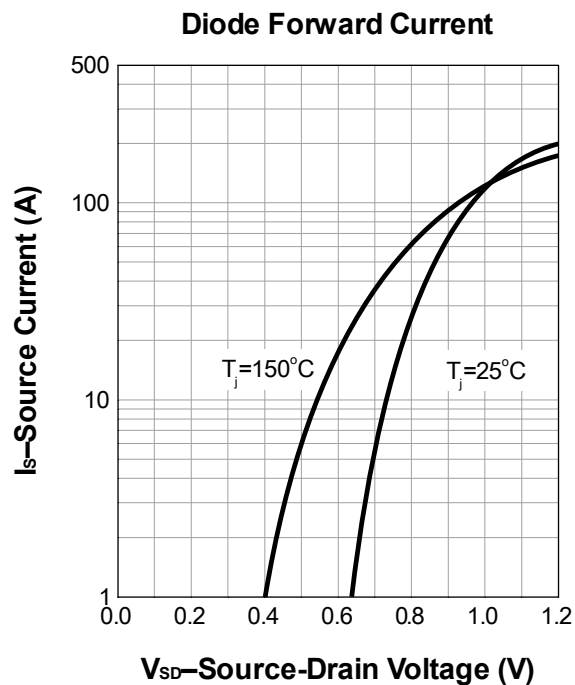
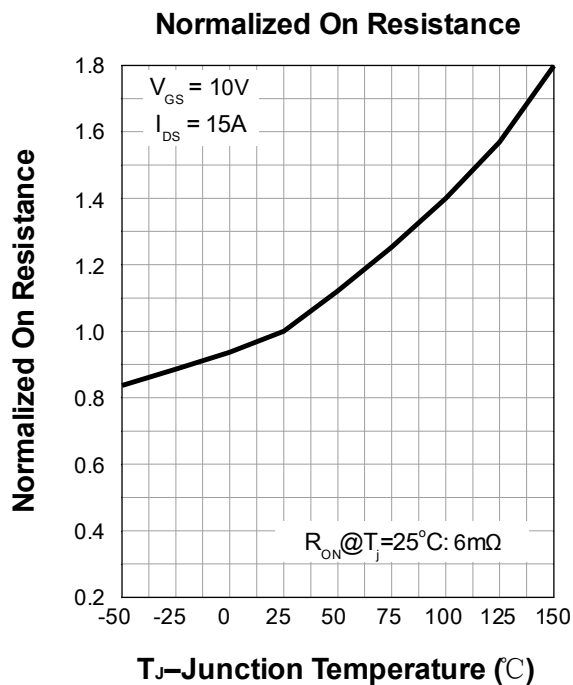
Transfer Characteristics



Normalized Threshold Voltage

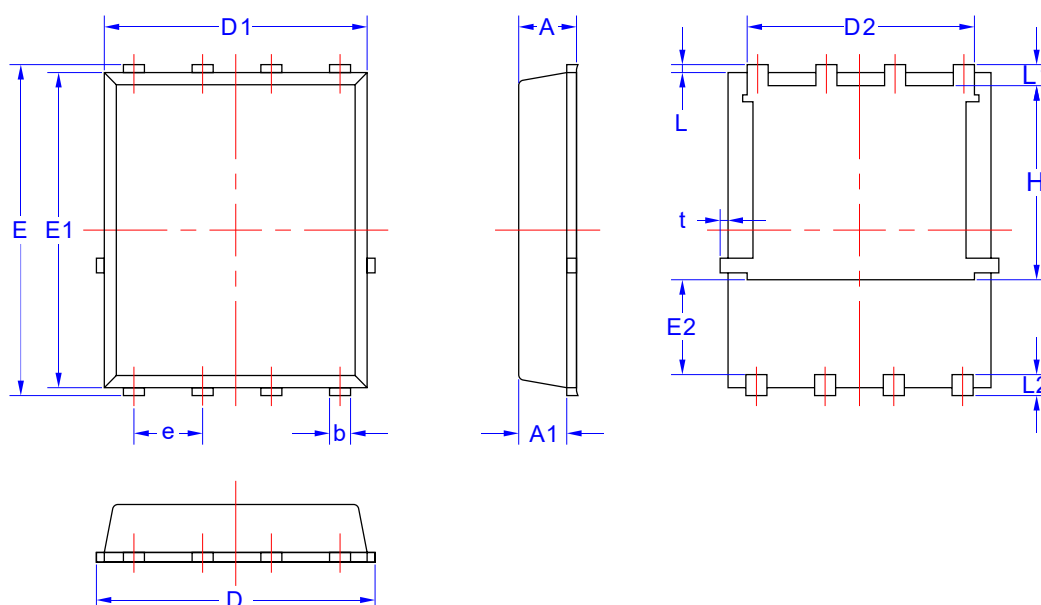


7. Typical Characteristics (cont.)



8. Package Dimensions

PDFN 5×6-8L Package



Symbol	Dimensions in Millimeters	
	MIN.	MAX
A	1.03	1.17
A1	0.824	0.97
b	0.34	0.48
D	4.80	5.40
D1	4.80	5.00
D2	4.11	4.31
E	5.95	6.15
E1	5.65	5.85
E2	1.40	-
e	1.27 BSC	
L	0.05	0.25
L1	0.38	0.50
L2	0.38	0.71
H	3.30	3.50
t	-	0.18