

Dual N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Low thermal resistance

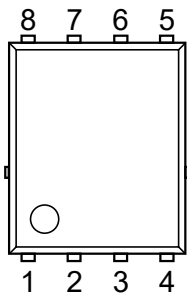
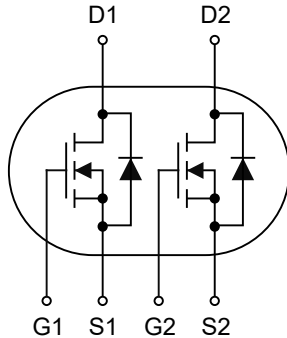
1.2 Applications

- ☒ Motor drivers
- ☒ DC/DC converter

1.3 Quick reference

- ☒ $BV \geq 66\text{ V}$
- ☒ $P_D \leq 30\text{ W}$
- ☒ $I_D \leq 33\text{ A}$
- ☒ $R_{DS(ON)} \leq 16\text{ m}\Omega @ V_{GS} = 10\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Source (S1)	 Top View PDFN5x6-8L	
2	Gate (G1)		
3	Source (S2)		
4	Gate (G2)		
5,6	Drain (D2)		
7,8	Drain (D1)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C=25^{\circ}\text{C}$	66	-	V
V_{GS}	Gate-Source Voltage	$T_C=25^{\circ}\text{C}$	-	± 20	V
I_D	Drain Current (DC)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	33	A
		$T_C=100^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	21	A
$I_{DM}^{*,***}$	Drain Current (Pulsed)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	132	A
P_D	Drain Power Dissipation	$T_C=25^{\circ}\text{C}$	-	30	W
I_S	Continuous-Source Current	$T_C=25^{\circ}\text{C}$	-	33	A
T_J, T_{stg}	Operating Junction and Storage Temperature Range		-55	150	$^{\circ}\text{C}$
$R_{\theta JA}^{**}$	Thermal Resistance-Junction to Ambient		-	50	$^{\circ}\text{C/W}$
$R_{\theta JC}^{**}$	Thermal Resistance-Junction to Case		-	3.6	

Notes :

- * Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- ** Mounted on Large Heat Sink.
- *** Limited by bonding wire.

4. Marking Information

Product Name	Marking
KJ6016DG	KJ6016DG XXXXXX

5. Ordering Code

Product Name	Package	Reel size	Tape width	Quantity (pcs)
KJ6016DG	PDFN 5×6-8L	13"	12 mm	5000

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

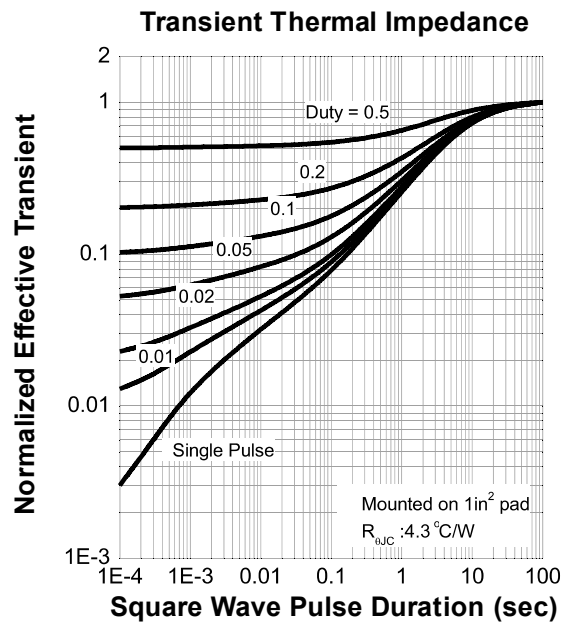
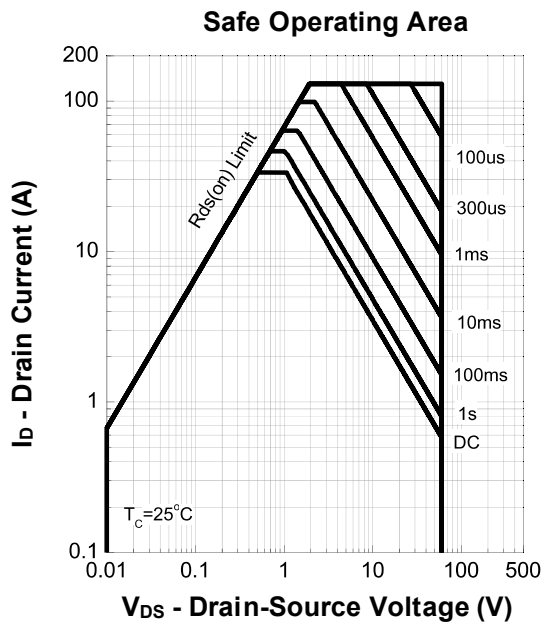
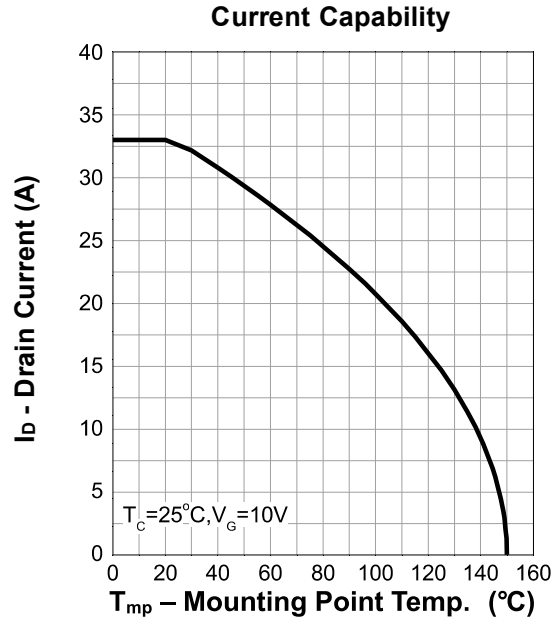
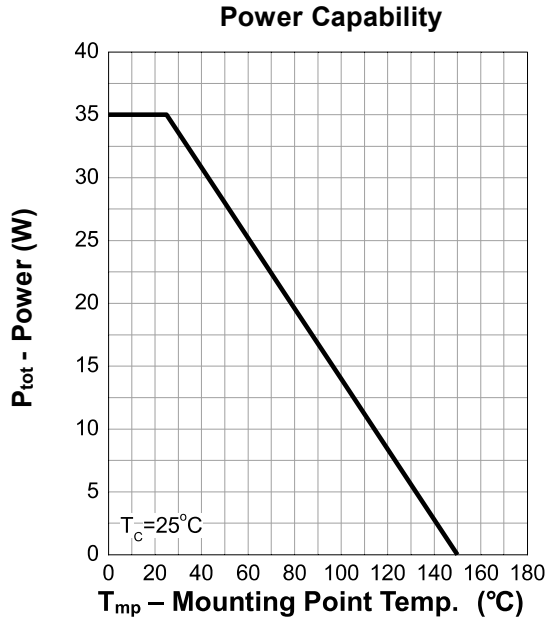
6. Electrical Characteristics (T_A=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0 V, I _{DS} =250 μA	66	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250 μA	1.3	1.9	2.5	V
I _{DSS}	Drain Leakage Current	V _{DS} =48 V, V _{GS} =0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{DS} =0 V, V _{GS} =±20 V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} =10 V, I _{DS} =12 A	-	12	16	mΩ
g _{fs}	Forward transconductance	V _{DS} =5 V, I _{DS} =12 A	-	36	-	S
R _g	Gate resistance	Frequency=1 MHz	-	3	-	Ω
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} =10 A, V _{GS} =0 V	-	1	1.3	V
t _{rr}	Reverse Recovery Time	I _{DS} =12 A, V _{GS} =0 V dI _{SD} /dt=100 A/μs	-	31	-	ns
Q _{rr}	Reverse Recovery Charge		-	11	-	μC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} =0 V, V _{DS} =30 V Frequency=1 MHz	-	596	-	pF
C _{oss}	Output Capacitance		-	250	-	
C _{rss}	Reverse Transfer Capacitance		-	22	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =30 V, V _{GEN} =10 V, R _G =4.5 Ω, R _L =2.5 Ω, I _{DS} =12 A	-	7	-	ns
t _r	Turn-on Rise Time		-	24	-	
t _{d(off)}	Turn-off Delay Time		-	14	-	
t _f	Turn-off Fall Time		-	16	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} =30 V, V _{GS} =10 V, I _{DS} =12 A	-	15	-	nC
Q _{gs}	Gate-Source Charge		-	3.4	-	
Q _{gd}	Gate-Drain Charge		-	6	-	

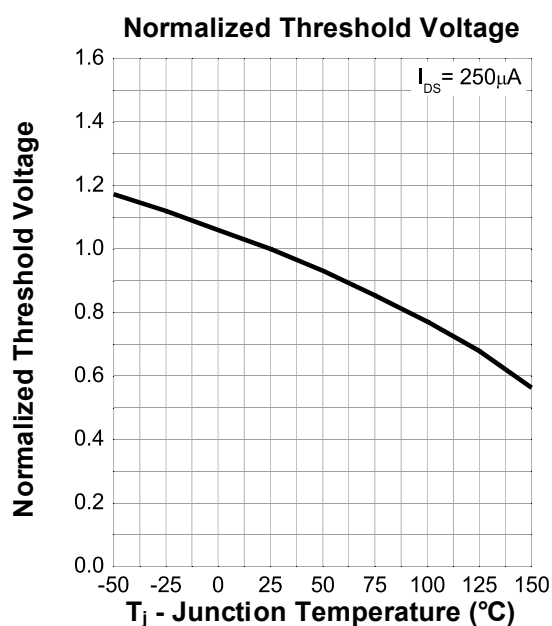
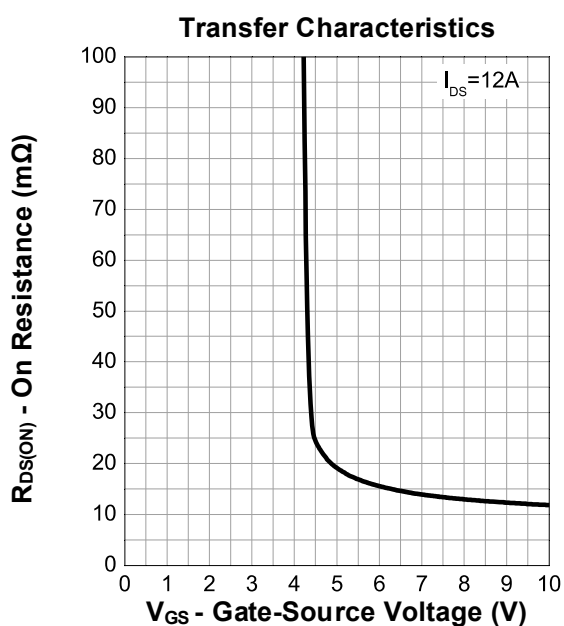
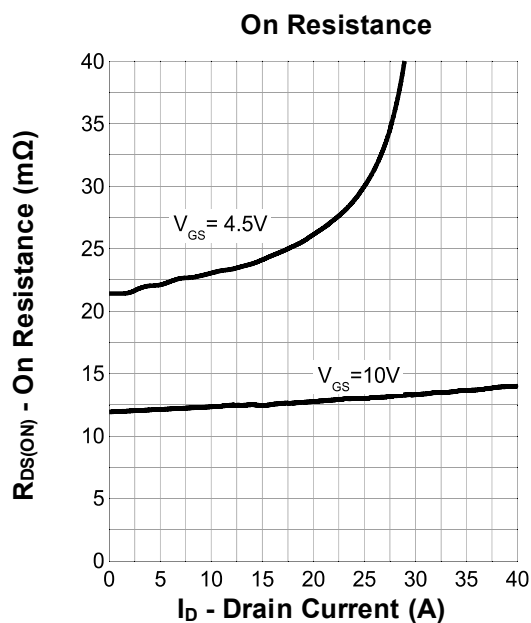
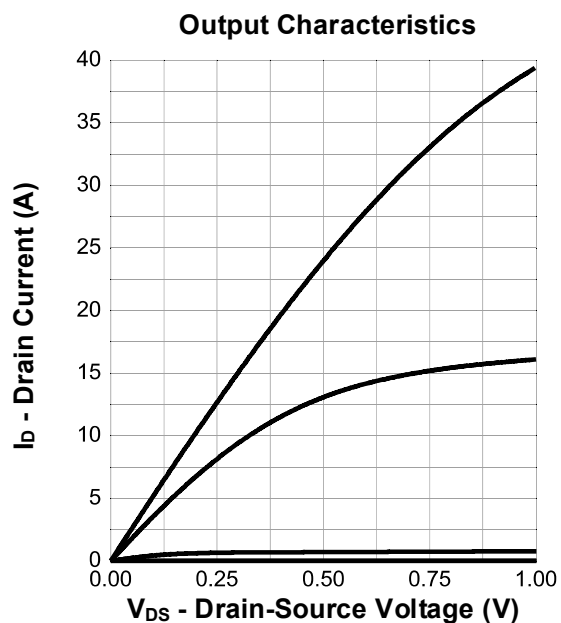
Notes:

- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

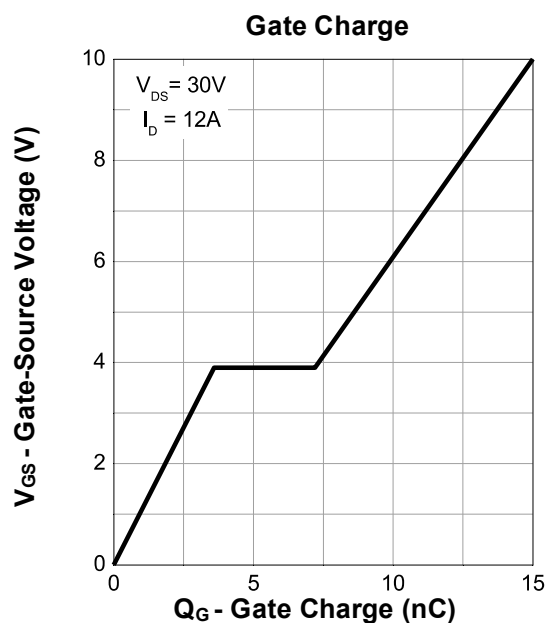
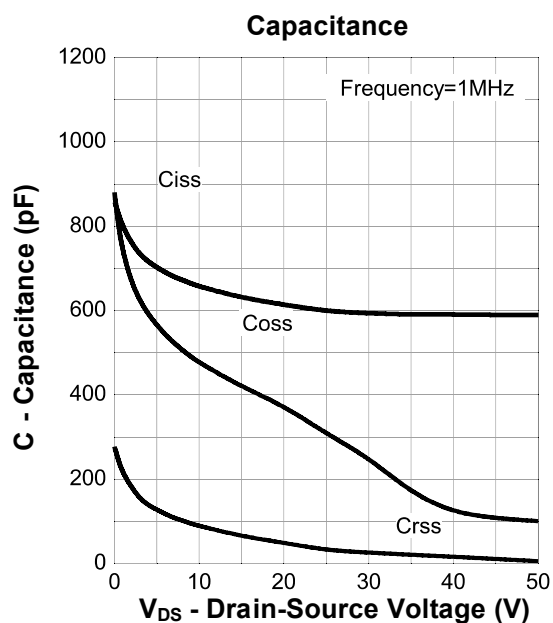
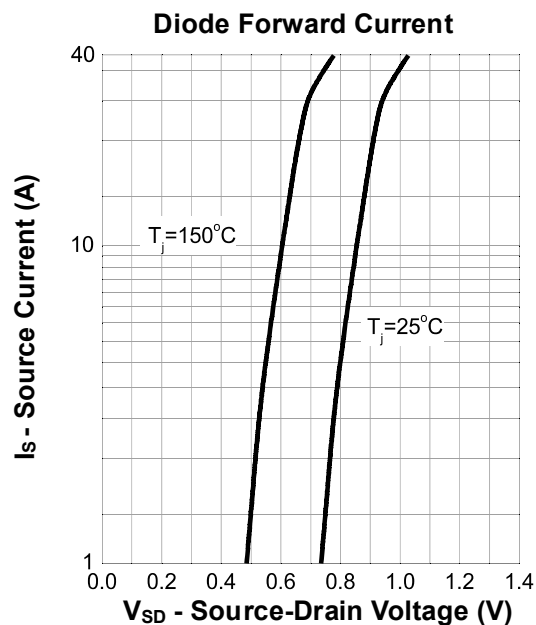
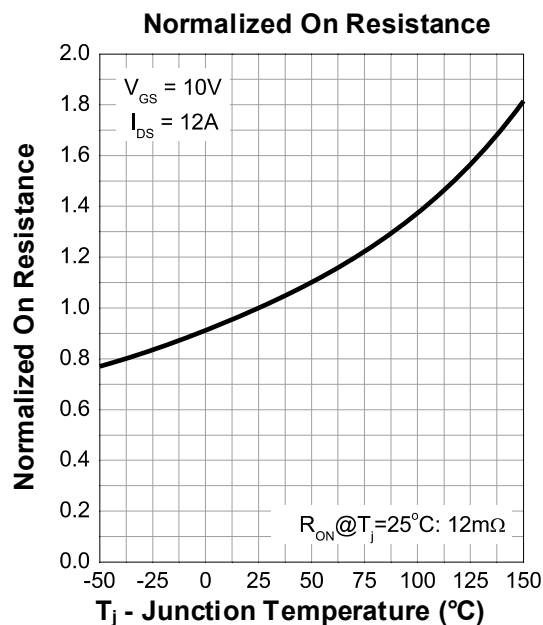
7. Typical Characteristics



7. Typical Characteristics (cont.)

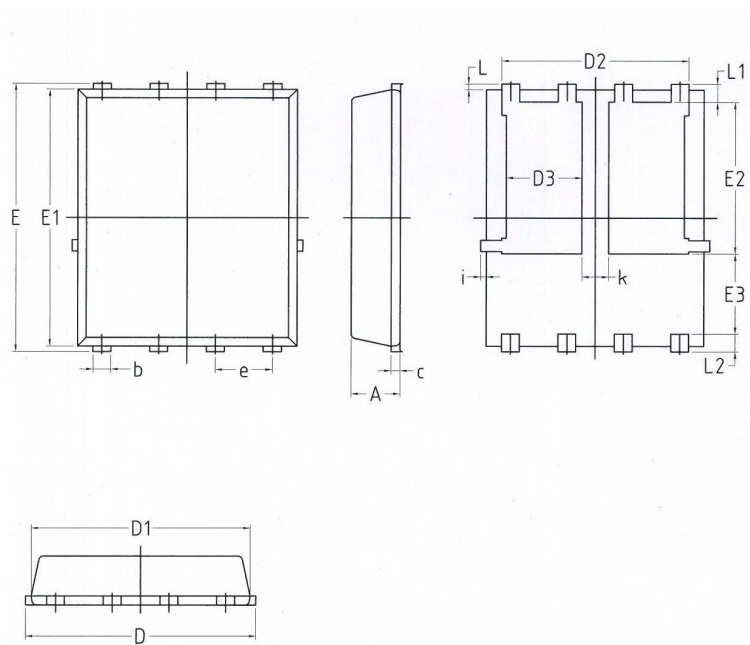


7. Typical Characteristics (cont.)



8. Package Dimensions

PDFN 5x6-8L Package



Symbol	Dimensions In Millimeters	
	MIN	MAX
A	1.03	1.17
B	0.34	0.48
c	0.203 BSC	
D	4.80	5.40
D1	4.80	5.00
D2	4.11	4.31
D3	1.60	1.80
E	5.95	6.15
E1	5.65	5.85
E2	3.30	3.50
E3	1.70	-
e	1.27 BSC	
L	0.05	0.25
L1	0.38	0.5
L2	0.38	0.5
i	-	0.18
k	0.5	0.7