

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- | | |
|---|---|
| <input checked="" type="checkbox"/> Surface-mounted package | <input checked="" type="checkbox"/> Advanced trench cell design |
| <input checked="" type="checkbox"/> T_J max 175°C | <input checked="" type="checkbox"/> MSL1 |

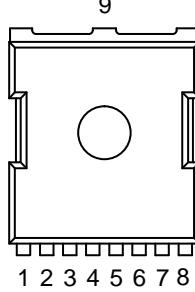
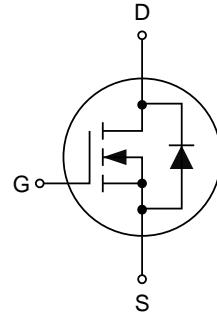
1.2 Applications

- | | |
|---|---|
| <input checked="" type="checkbox"/> Power management switches | <input checked="" type="checkbox"/> DC/DC converter |
| <input checked="" type="checkbox"/> LED backlighting | |

1.3 Quick reference

- | | |
|--|--|
| <input checked="" type="checkbox"/> $BV \geq 80$ V | <input checked="" type="checkbox"/> $R_{DS(ON)} \leq 1.2$ mΩ @ $V_{GS} = 10$ V |
| <input checked="" type="checkbox"/> $P_D \leq 465$ W | |
| <input checked="" type="checkbox"/> $I_D \leq 400$ A | |

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate		
2,3,4,5,6,7,8	Source		
9	Drain	 Top View TOLL-8L	

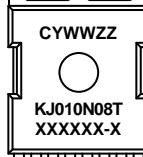
3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C =25°C	80	-	V
V _{GS}	Gate-Source Voltage	T _C =25°C	-	±20	V
I _D * , ***	Drain Current	T _C =25°C, V _{GS} =10 V	-	400	A
		T _C =100°C, V _{GS} =10 V	-	283	A
I _{DM} * , **	Drain Current (Pulsed)	T _C =25°C, V _{GS} =10 V	-	1600	A
P _D	Drain Power Dissipation	T _C =25°C	-	465	W
E _{AS}	Single Pulsed Avalanche Energy	V _{DD} =40 V, L=0.5 mH	-	2800	mJ
T _J , T _{stg}	Operating Junction and Storage Temperature Range		-55	175	°C
R _{θJA} **	Thermal Resistance-Junction to Ambient		-	40	°C/W
R _{θJC} **	Thermal Resistance-Junction to Case		-	0.32	

Notes:

- * Pulse width ≤ 300 μs, duty cycle ≤ 2%.
- ** Surface mounted on 1 in² pad area, t ≤ 10 sec.
- *** Limited by bonding wire.

4. Marking Information

Product Name	Marking
KJ010N08T	

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity (pcs)
KJ010N08T	TOLL-8L	13"	24 mm	2000

Note: KUAIJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

6. Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0 \text{ V}, I_{\text{DS}}=250 \mu\text{A}$	80	-	-	V
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{DS}}=250 \mu\text{A}$	2	-	4	V
I_{DSS}	Drain Gate Current	$V_{\text{DS}}=80 \text{ V}, V_{\text{GS}}=0 \text{ V}$	-	-	1	μA
I_{GSS}	Gate Leakage Current	$V_{\text{DS}}=0 \text{ V}, V_{\text{GS}}=\pm 20 \text{ V}$	-	-	± 100	nA
$R_{\text{DS(ON)}}^{\text{a}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10 \text{ V}, I_{\text{DS}}=20 \text{ A}$	-	1.0	1.2	$\text{m}\Omega$
g_{fs}	Forward Transconductance	$V_{\text{GS}}=10 \text{ V}, I_{\text{DS}}=20 \text{ A}$	-	62	-	S
Diode Characteristics						
V_{SD}^{a}	Diode Forward Voltage	$V_{\text{GS}}=0 \text{ V}, I_{\text{SD}}=20 \text{ A}$	-	-	1.2	V
t_{rr}	Reverse Recovery Time	$V_{\text{GS}}=0 \text{ V}, I_{\text{SD}}=20 \text{ A},$ $dI_{\text{SD}}/dt=100 \text{ A}/\mu\text{s}$	-	128	-	ns
Q_{rr}	Reverse Recovery Charge		-	141	-	nC
Dynamic Characteristics ^b						
C_{iss}	Input Capacitance	$V_{\text{DS}}=40 \text{ V}, V_{\text{GS}}=0 \text{ V},$ Frequency=1 MHz	-	13090	-	pF
C_{oss}	Output Capacitance		-	2620	-	
C_{rss}	Reverse Transfer Capacitance		-	120	-	
R_g	Gate Resistance		-	3.1	-	
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DS}}=40 \text{ V}, V_{\text{GEN}}=10 \text{ V},$ $R_g=3 \Omega, I_{\text{DS}}=20 \text{ A}$	-	45	-	ns
t_r	Turn-on Rise Time		-	88	-	
$t_{\text{d(off)}}$	Turn-off Delay Time		-	164	-	
t_f	Turn-off Fall Time		-	95	-	
Gate Charge Characteristics ^b						
Q_g	Total Gate Charge	$V_{\text{DS}}=40 \text{ V}, V_{\text{GS}}=10 \text{ V},$ $I_{\text{DS}}=20 \text{ A}$	-	245	-	nC
Q_{gs}	Gate-Source Charge		-	64	-	
Q_{gd}	Gate-Drain Charge		-	59	-	

Notes:

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

7. Typical Characteristics

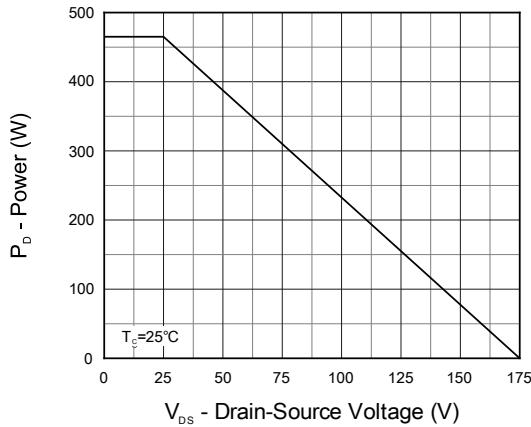


Figure 1. Power Capability

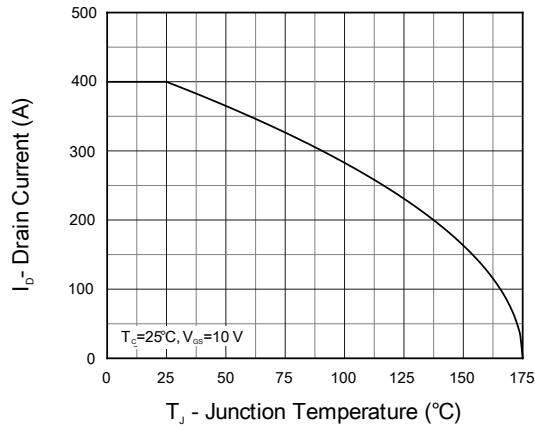


Figure 2. Current Capability

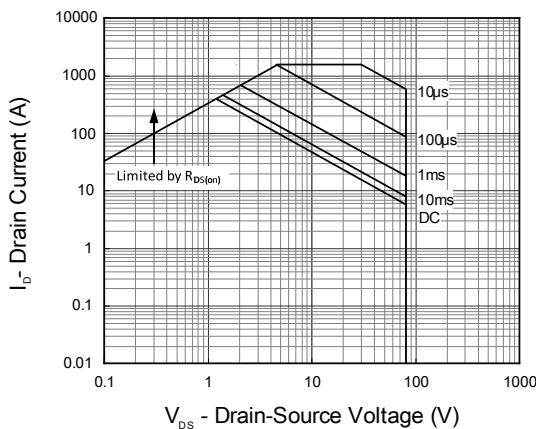


Figure 3. Safe Operation Area

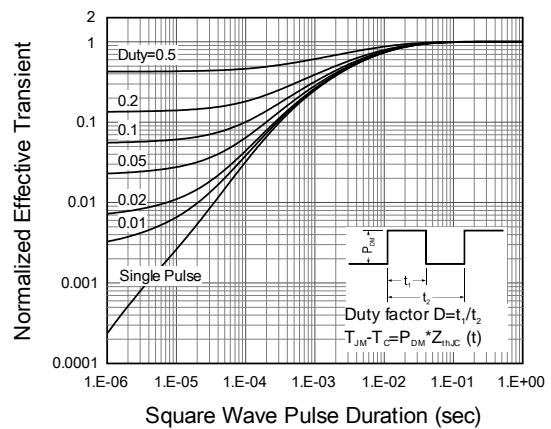


Figure 4. Transient Thermal Impedance

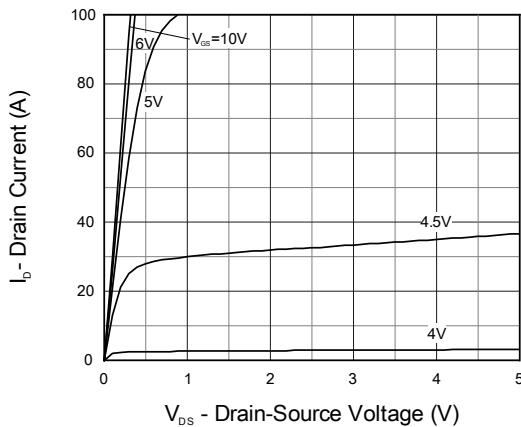


Figure 5. Output Characteristics

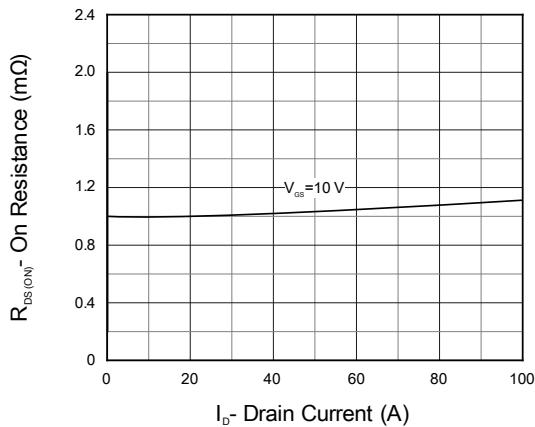


Figure 6. On Resistance

7. Typical Characteristics (cont.)

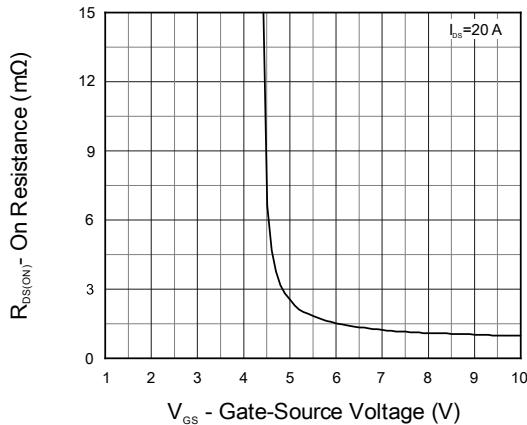


Figure 7. Transfer Characteristics

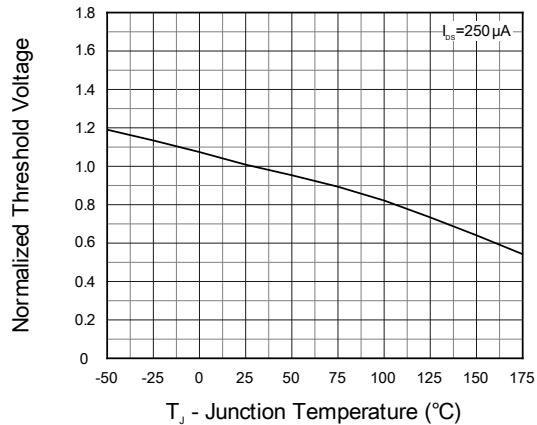


Figure 8. Normalized Threshold Voltage

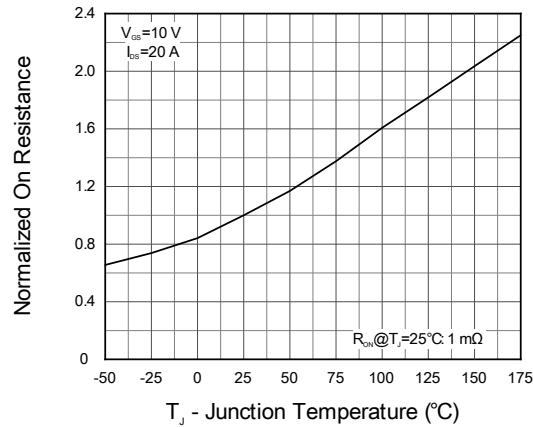


Figure 9. Normalized On Resistance

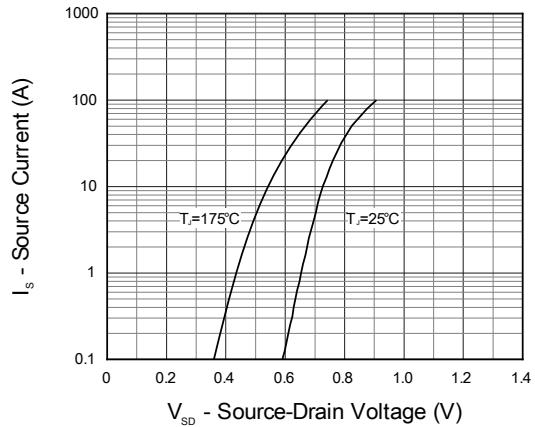


Figure 10. Diode Forward Current

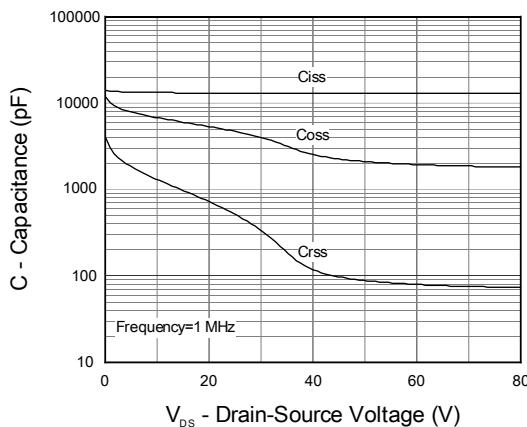


Figure 11. Capacitance

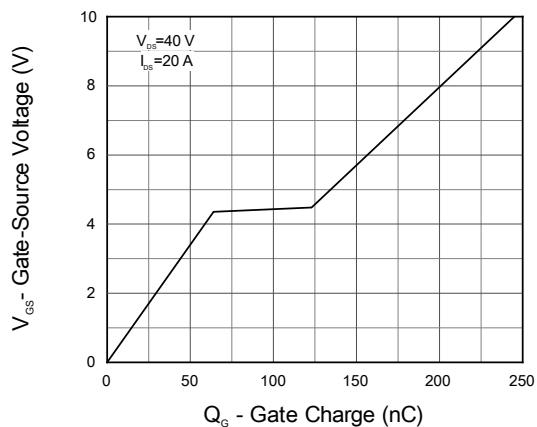


Figure 12. Gate Charge

7. Test Circuit

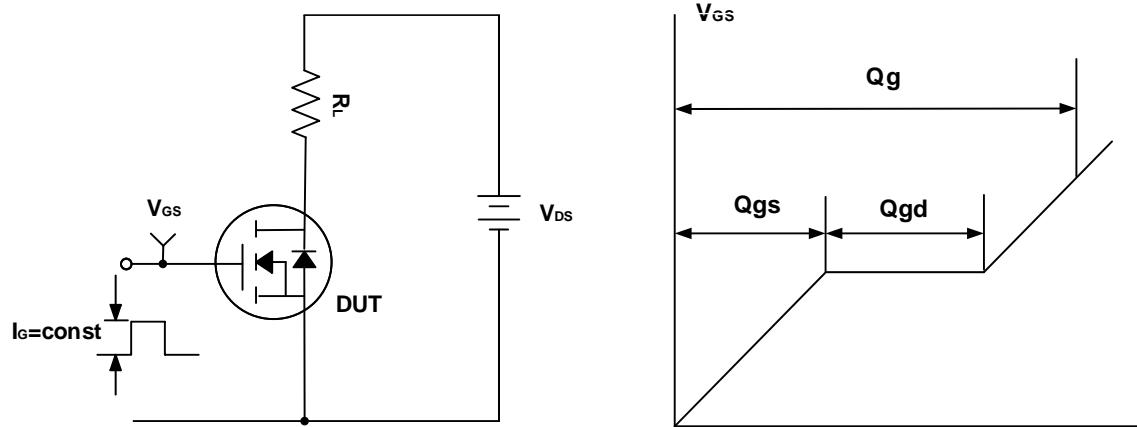


Figure A. Gate Charge Test Circuit & Waveforms

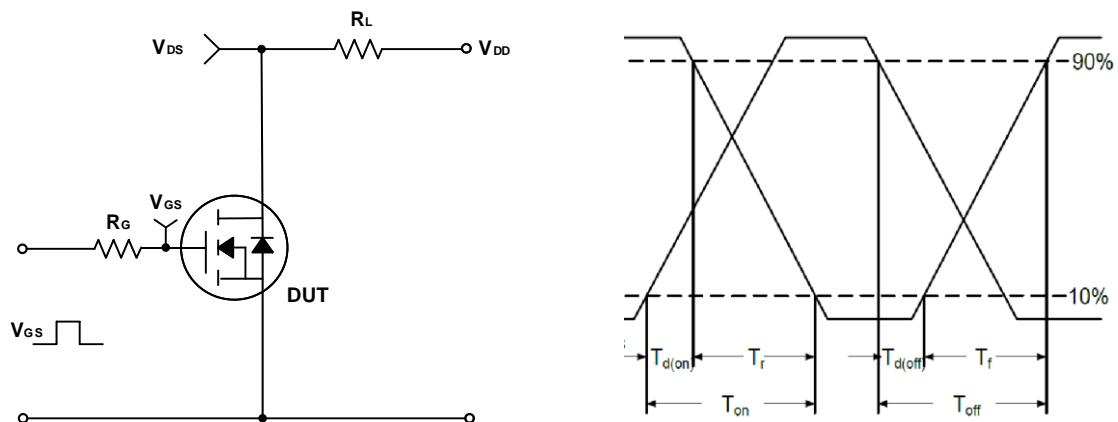


Figure B. Switching Test Circuit & Waveforms

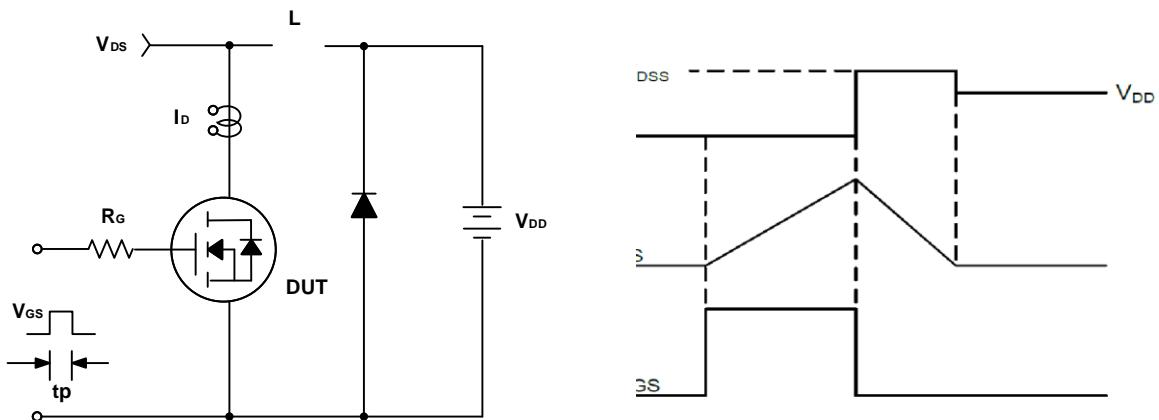
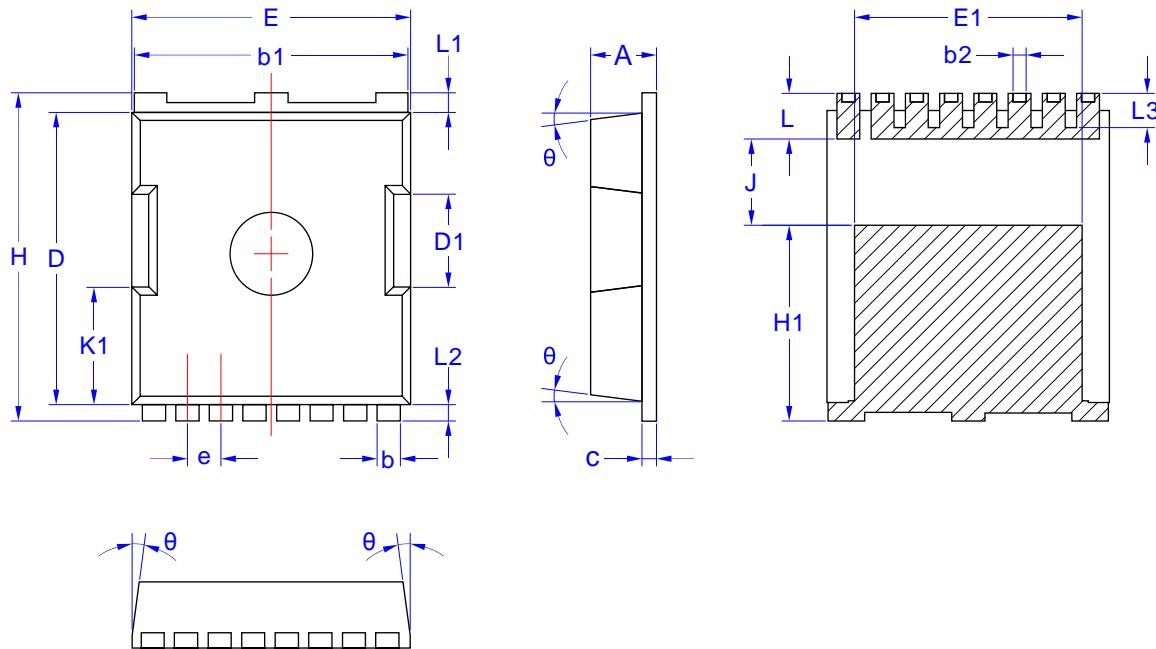


Figure C. Unclamped Inductive Switching Circuit & Waveforms

8. Package Dimensions

TOLL-8L Package



Symbol	Dimensions in Millimeters	
	MIN	MAX
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D1	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20 BSC	

Symbol	Dimensions in Millimeters	
	MIN	MAX
H	11.48	11.88
H1	6.75	7.15
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L3	1.00	1.30
θ	4°	10°