

## N-Channel Enhancement Mode MOSFET

### 1. Product Information

#### 1.1 Features

- ☒ Advanced trench cell design
- ☒ Low Thermal Resistance

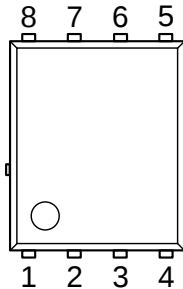
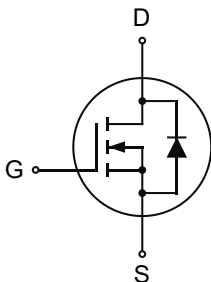
#### 1.2 Applications

- ☒ Motor drivers
- ☒ DC/DC Converter

#### 1.3 Quick reference

- ☒  $BV \geq 120\text{ V}$
- ☒  $R_{DS(ON)} \leq 6.5\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- ☒  $P_{tot} \leq 136\text{ W}$
- ☒  $R_{DS(ON)} \leq 8.9\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$
- ☒  $I_D \leq 96\text{ A}$

### 2. Pin Description

| Pin     | Description | Simplified Outline                                                                                                   | Symbol                                                                                |
|---------|-------------|----------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|
| 1,2,3   | Source      |  <p>Top View<br/>PDFN 5x6-8L</p> |  |
| 4       | Gate        |                                                                                                                      |                                                                                       |
| 5,6,7,8 | Drain       |                                                                                                                      |                                                                                       |

## 3. Limiting Values

| Symbol            | Parameter                                  | Conditions                                    | Min | Max      | Unit                 |
|-------------------|--------------------------------------------|-----------------------------------------------|-----|----------|----------------------|
| $V_{DS}$          | Drain-Source Voltage                       | $T_C=25^{\circ}\text{C}$                      | 120 | -        | V                    |
| $V_{GS}$          | Gate-Source Voltage                        | $T_C=25^{\circ}\text{C}$                      | -   | $\pm 20$ | V                    |
| $I_D^*$           | Drain Current                              | $T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$  | -   | 96       | A                    |
|                   |                                            | $T_C=100^{\circ}\text{C}, V_{GS}=10\text{ V}$ | -   | 68       | A                    |
| $I_{DM}^{*, **}$  | Drain Current (Pulsed)                     | $T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$  | -   | 384      | A                    |
| $P_{tot}^*$       | Total Power Dissipation                    | $T_C=25^{\circ}\text{C}$                      | -   | 136      | W                    |
| $T_J, T_{stg}$    | Operating Junction and Storage Temperature |                                               | -55 | 175      | $^{\circ}\text{C}$   |
| $I_S$             | Diode Forward Current                      | $T_C=25^{\circ}\text{C}$                      | -   | 96       | A                    |
| $E_{AS}^*$        | Single Pulsed Avalanche Energy             | $V_{DD}=50\text{ V}, L=0.3\text{ mH}$         | -   | 843      | mJ                   |
| $R_{\theta JA}^*$ | Thermal Resistance-Junction to Ambient     |                                               | -   | 45       | $^{\circ}\text{C/W}$ |
| $R_{\theta JC}^*$ | Thermal Resistance-Junction to Case        |                                               | -   | 1.1      |                      |

Notes:

- \* Surface mounted on 1 in<sup>2</sup> pad area,  $t \leq 10\text{ sec.}$
- \*\* Pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- \*\*\* Limited by bonding wire.

## 4. Marking Information

| Product Name | Marking                         |
|--------------|---------------------------------|
| KJ0712G      | <b>KJ0712G</b><br><b>XXXXXX</b> |

## 5. Ordering Code

| Product Name | Package     | Reel size | Tape width | Quantity (pcs) |
|--------------|-------------|-----------|------------|----------------|
| KJ0712G      | PDFN 5x6-8L | 13"       | 12 mm      | 5000           |

Note: KUAJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

## 6. Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

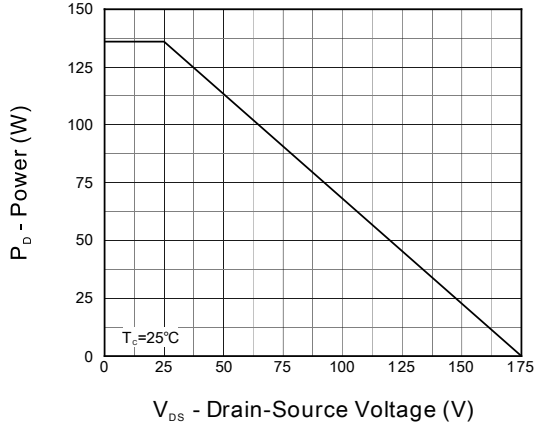
| Symbol                                   | Parameter                      | Conditions                                                                                | Min | Typ  | Max  | Unit |
|------------------------------------------|--------------------------------|-------------------------------------------------------------------------------------------|-----|------|------|------|
| Static Characteristics                   |                                |                                                                                           |     |      |      |      |
| BV <sub>DSS</sub>                        | Drain-Source Breakdown Voltage | V <sub>GS</sub> =0 V, I <sub>DS</sub> =250 μA                                             | 120 | -    | -    | V    |
| I <sub>DSS</sub>                         | Drain Leakage Current          | V <sub>DS</sub> =120 V, V <sub>GS</sub> =0 V                                              | -   | -    | 1    | μA   |
| I <sub>GSS</sub>                         | Gate Leakage Current           | V <sub>DS</sub> =0 V, V <sub>GS</sub> =±20 V                                              | -   | -    | ±100 | nA   |
| V <sub>GS(th)</sub>                      | Gate Threshold Voltage         | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250 μA                                | 1.2 | -    | 2.5  | V    |
| R <sub>DS(ON)</sub> <sup>a</sup>         | On-State Resistance            | V <sub>GS</sub> =10 V, I <sub>DS</sub> =20 A                                              | -   | 5.5  | 6.5  | mΩ   |
|                                          |                                | V <sub>GS</sub> =4.5 V, I <sub>DS</sub> =15 A                                             | -   | 7.5  | 8.9  |      |
| Diode Characteristics                    |                                |                                                                                           |     |      |      |      |
| V <sub>SD</sub> <sup>a</sup>             | Diode Forward Voltage          | I <sub>SD</sub> =1 A, V <sub>GS</sub> =0 V                                                | -   | 0.7  | 1.2  | V    |
| t <sub>rr</sub>                          | Reverse Recovery Time          | I <sub>SD</sub> =20 A, V <sub>GS</sub> =0 V, dI <sub>SD</sub> /dt=100 A/μs                | -   | 60   | -    | ns   |
| Q <sub>rr</sub>                          | Reverse Recovery Charge        |                                                                                           | -   | 104  | -    | nC   |
| Dynamic Characteristics <sup>b</sup>     |                                |                                                                                           |     |      |      |      |
| C <sub>iss</sub>                         | Input Capacitance              | V <sub>DS</sub> =60 V, V <sub>GS</sub> =0 V, Frequency=1 MHz                              | -   | 1890 | -    | pF   |
| C <sub>oss</sub>                         | Output Capacitance             |                                                                                           | -   | 930  | -    |      |
| C <sub>rss</sub>                         | Reverse Transfer Capacitance   |                                                                                           | -   | 15   | -    |      |
| t <sub>d(on)</sub>                       | Turn-on Delay Time             | V <sub>DS</sub> =60 V, V <sub>GEN</sub> =10 V, R <sub>G</sub> =3 Ω, I <sub>DS</sub> =20 A | -   | 5.5  | -    | ns   |
| t <sub>r</sub>                           | Turn-on Rise Time              |                                                                                           | -   | 5    | -    |      |
| t <sub>d(off)</sub>                      | Turn-off Delay Time            |                                                                                           | -   | 25   | -    |      |
| t <sub>f</sub>                           | Turn-off Fall Time             |                                                                                           | -   | 13   | -    |      |
| Gate Charge Characteristics <sup>b</sup> |                                |                                                                                           |     |      |      |      |
| Q <sub>g</sub>                           | Total Gate Charge              | V <sub>DS</sub> =60 V, V <sub>GS</sub> =10 V, I <sub>DS</sub> =20 A                       | -   | 30   | -    | nC   |
| Q <sub>gs</sub>                          | Gate-Source Charge             |                                                                                           | -   | 5.5  | -    |      |
| Q <sub>gd</sub>                          | Gate-Drain Charge              |                                                                                           | -   | 7    | -    |      |

Notes:

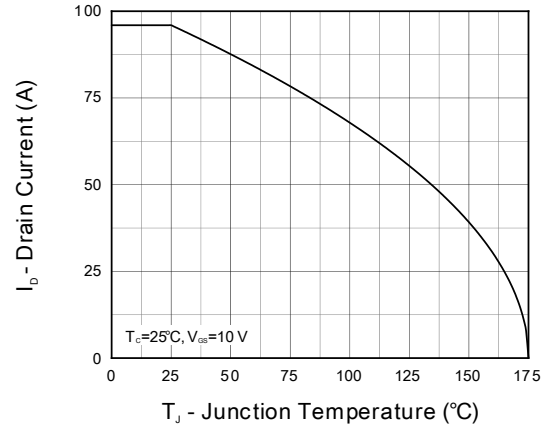
a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.

b. Guaranteed by design, not subject to production testing.

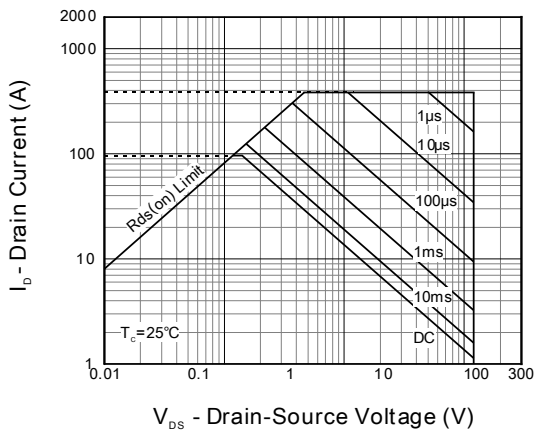
## 7. Typical Characteristics



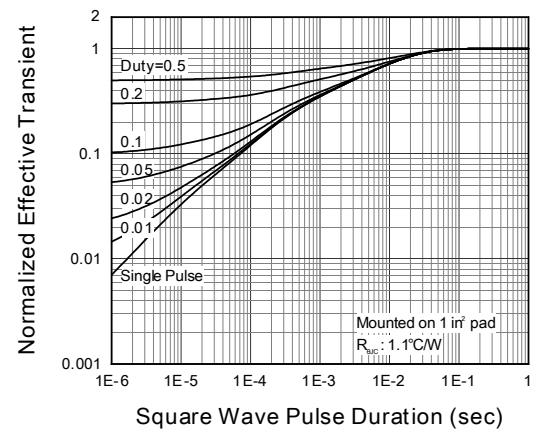
**Figure 1. Output Characteristics**



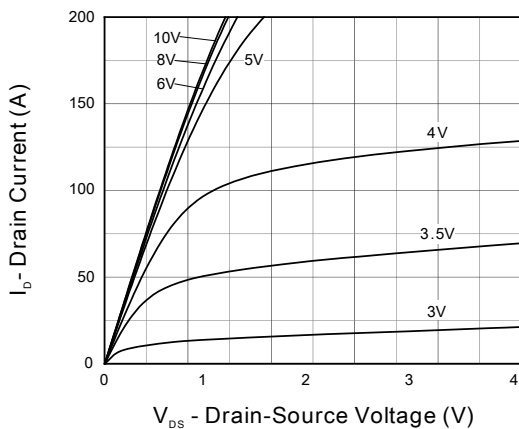
**Figure 2. Current Capability**



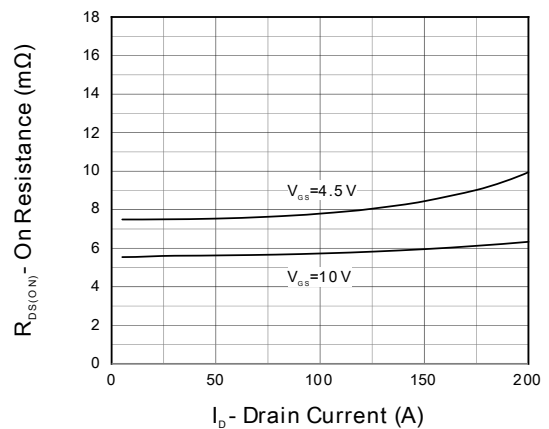
**Figure 3. Safe Operation Area**



**Figure 4. Transient Thermal Impedance**



**Figure 5. Output Characteristics**



**Figure 6. On Resistance**

## 7. Typical Characteristics (cont.)

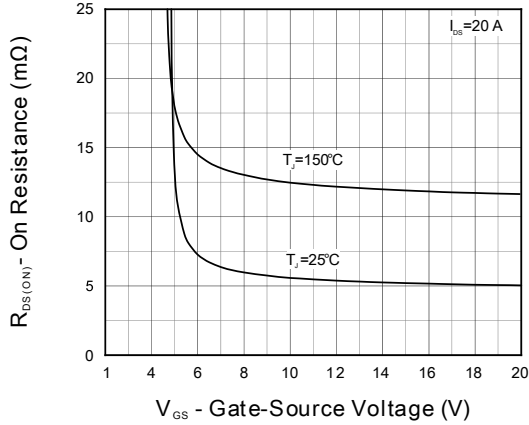


Figure 7. Transfer Characteristics

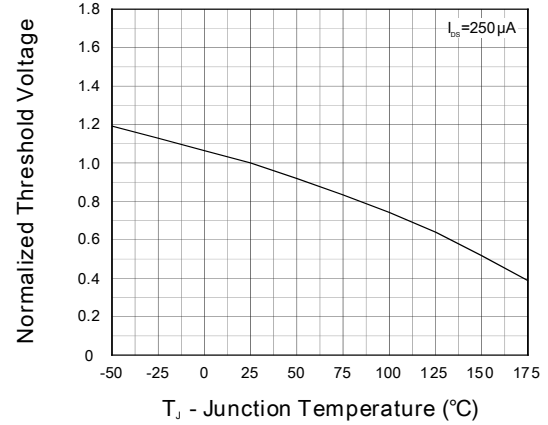


Figure 8. Normalized Threshold Voltage

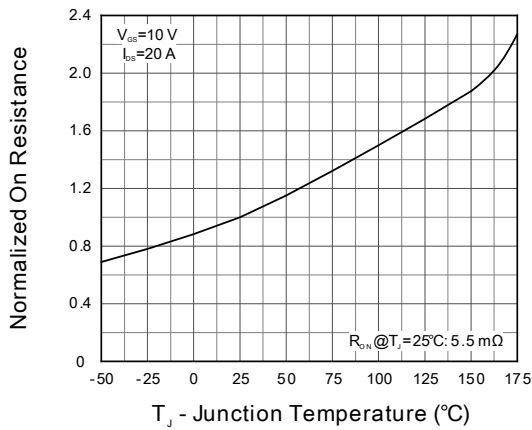


Figure 9. Normalized On Resistance

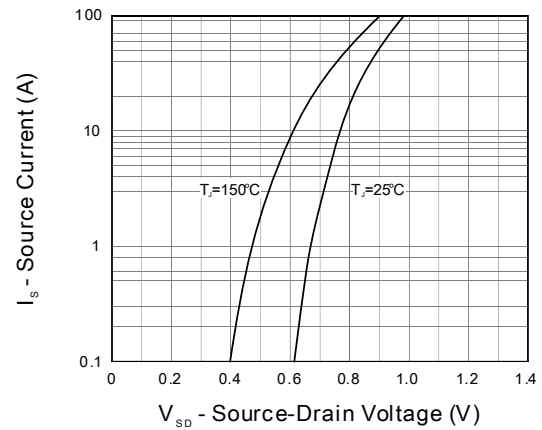


Figure 10. Diode Forward Current

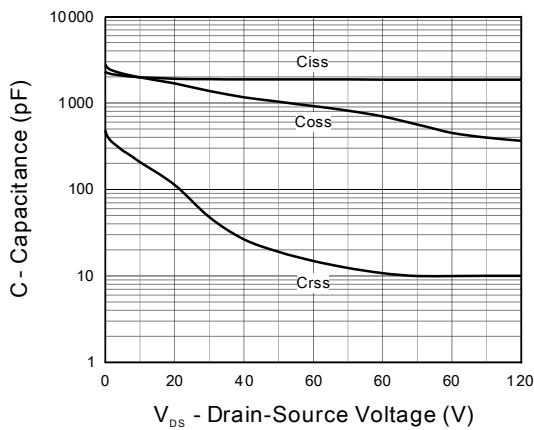


Figure 11. Capacitance

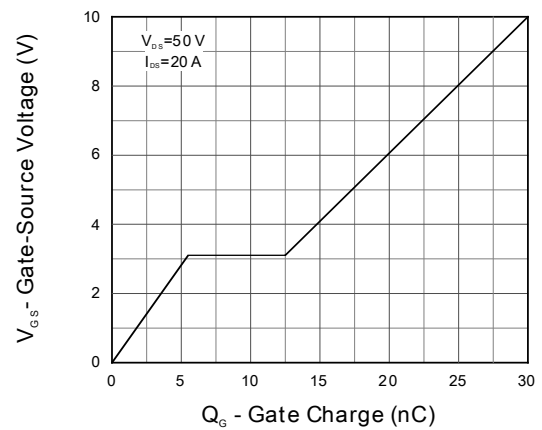
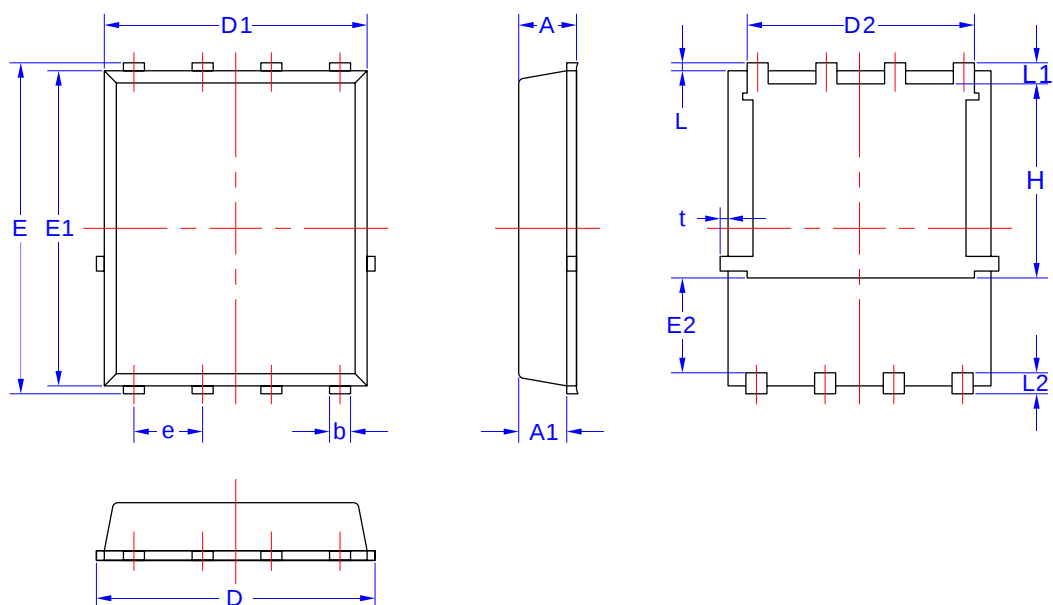


Figure 12. Gate Charge

## 8. Package Dimensions

### PDFN 5×6-8L Package



| Symbol | Dimensions in Millimeters |      |
|--------|---------------------------|------|
|        | MIN.                      | MAX  |
| A      | 1.03                      | 1.17 |
| A1     | 0.824                     | 0.97 |
| b      | 0.34                      | 0.48 |
| D      | 4.80                      | 5.40 |
| D1     | 4.80                      | 5.00 |
| D2     | 4.11                      | 4.31 |
| E      | 5.95                      | 6.15 |
| E1     | 5.65                      | 5.85 |
| E2     | 1.40                      | -    |
| e      | 1.27 BSC                  |      |
| L      | 0.05                      | 0.25 |
| L1     | 0.38                      | 0.50 |
| L2     | 0.38                      | 0.71 |
| H      | 3.30                      | 3.50 |
| t      | -                         | 0.18 |