

N-Channel Enhancement Mode MOSFET

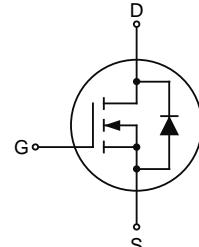
1. Product Information

1.1 Features

Shielded Gate Trench Technology

Excellent low $R_{DS(ON)}$

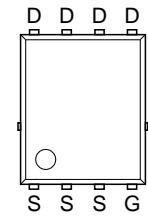
Low gate charge



1.2 Applications

DC/DC converter

Power management switches



Top View
PDFN 5x6-8L

1.3 Quick reference

$V_{DS} = 150 \text{ V}$

$I_D = 140 \text{ A}$

$R_{DS(ON)} \leq 10 \text{ m}\Omega @ V_{GS} = 10\text{V}$ (Type: 8.6 m Ω)

2. Package Marking and Ordering Information

| Product Name | Package | Marking | Reel size | Tape width | Quantity (pcs) |
|--------------|-------------|-------------------|-----------|------------|----------------|
| KJ0815G | PDFN 5x6-8L | KJ0815G XXXXXX | 13" | 12 mm | 5000 |

3. Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|-----------------|---|------------|--------------------|
| V_{DS} | Drain-Source Voltage | 150 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| I_D | Continuous Drain Current, $T_C=25^\circ\text{C}$ ^{1, 2} | 140 | A |
| | Continuous Drain Current, $T_C=100^\circ\text{C}$ ^{1, 2} | 60 | A |
| I_{DM} | Pulsed Drain Current ^{1, 2} | 550 | A |
| I_{AS} | Avalanche Current | 65 | A |
| E_{AS} | Single Pulse Avalanche Energy ³ | 506 | mJ |
| P_D | Power Dissipation ⁴ | 147 | W |
| T_J, T_{stg} | Operating Junction and Storage Temperature Range | -55 to 150 | $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient ¹ | 25 | $^\circ\text{C/W}$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case ¹ | 0.85 | $^\circ\text{C/W}$ |

4. Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|------------------------------------|---------------------------------------|---|-----|------|-----------|---------------|
| Static Characteristics | | | | | | |
| BV_{DSS} | Drain-Source Breakdown Voltage | $\text{V}_{\text{GS}}=0 \text{ V}, \text{I}_{\text{DS}}=250 \mu\text{A}$ | 150 | - | - | V |
| $\text{V}_{\text{GS(th)}}$ | Gate Threshold Voltage | $\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{DS}}=250 \mu\text{A}$ | 2.0 | - | 4.5 | V |
| I_{DSS} | Drain Leakage Current | $\text{V}_{\text{DS}}=150 \text{ V}, \text{V}_{\text{GS}}=0 \text{ V}$ | - | - | 1 | μA |
| I_{GSS} | Gate Leakage Current | $\text{V}_{\text{GS}}=0 \text{ V}, \text{V}_{\text{GS}}=\pm 20 \text{ V}$ | - | - | ± 100 | nA |
| $\text{R}_{\text{DS(ON)}}$ | On-State Resistance ⁴ | $\text{V}_{\text{GS}}=10 \text{ V}, \text{I}_{\text{DS}}=20 \text{ A}$ | - | 8.6 | 10 | mΩ |
| g_{FS} | Forward Transconductance ⁴ | $\text{V}_{\text{GS}}=5 \text{ V}, \text{I}_{\text{DS}}=20 \text{ A}$ | - | 60 | - | S |
| R_g | Gate Resistance | Frequency=1 MHz | - | 2.5 | - | Ω |
| Diode Characteristics | | | | | | |
| V_{SD} | Diode Forward Voltage | $\text{I}_{\text{SD}}=20 \text{ A}, \text{V}_{\text{GS}}=0 \text{ V}$ | - | - | 1.2 | V |
| t_{rr} | Reverse Recovery Time | $\text{I}_{\text{DS}}=20 \text{ A}, \text{V}_{\text{GS}}=0 \text{ V}, \frac{d\text{I}_{\text{SD}}}{dt}=100 \text{ A}/\mu\text{s}$ | - | 98 | - | ns |
| Q_{rr} | Reverse Recovery Charge | | - | 320 | - | nC |
| Dynamic Characteristics | | | | | | |
| C_{iss} | Input Capacitance | $\text{V}_{\text{GS}}=10 \text{ V}, \text{V}_{\text{DS}}=75 \text{ V}, \text{I}_{\text{DS}}=20 \text{ A}, \text{Frequency}=1 \text{ MHz}$ | - | 2184 | - | pF |
| C_{oss} | Output Capacitance | | - | 360 | - | |
| C_{rss} | Reverse Transfer Capacitance | | - | 8 | - | |
| $\text{t}_{\text{d(on)}}$ | Turn-on Delay Time | $\text{V}_{\text{DS}}=75 \text{ V}, \text{V}_{\text{GEN}}=10 \text{ V}, \text{R}_g=3 \Omega, \text{I}_{\text{DS}}=20 \text{ A}$ | - | 13 | - | ns |
| t_r | Turn-on Rise Time | | - | 24 | - | |
| $\text{t}_{\text{d(off)}}$ | Turn-off Delay Time | | - | 30 | - | |
| t_f | Turn-off Fall Time | | - | 25 | - | |
| Gate Charge Characteristics | | | | | | |
| Q_g | Total Gate Charge | $\text{V}_{\text{DS}}=75 \text{ V}, \text{V}_{\text{GS}}=10 \text{ V}, \text{I}_{\text{DS}}=20 \text{ A}$ | - | 30 | - | nC |
| Q_{gs} | Gate-Source Charge | | - | 7.5 | - | |
| Q_{gd} | Gate-Drain Charge | | - | 6.6 | - | |

Notes:

- Tested by surface mounted on a 1 inch² FR-4 board with 2 OZ copper.
- Tested by pulsed, pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- The E_{AS} data shows Max. rating. The test condition is $\text{V}_{\text{DD}}=50 \text{ V}, \text{V}_{\text{GS}}=10 \text{ V}, \text{L}=0.5 \text{ mH}, \text{I}_{\text{AS}}=45 \text{ A}$.
- The power dissipation is limited by 150°C junction temperature.
- The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

5. Typical Characteristics

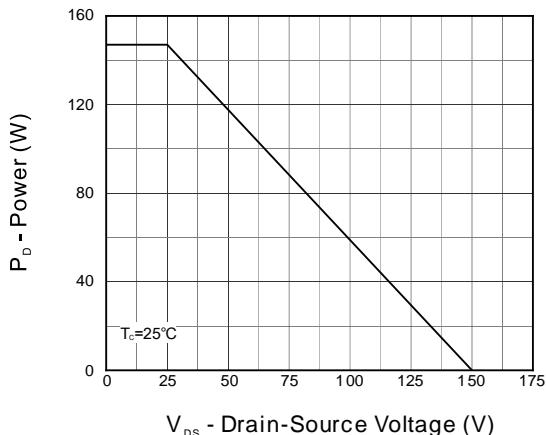


Figure 1. Output Characteristics

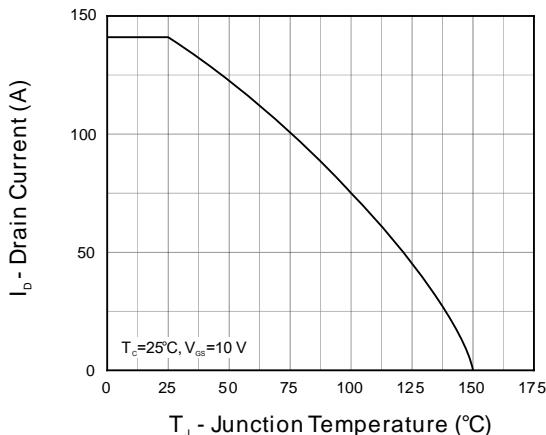


Figure 2. Current Capability

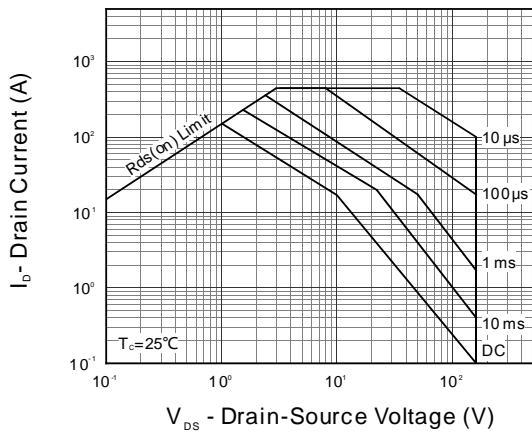


Figure 3. Safe Operation Area

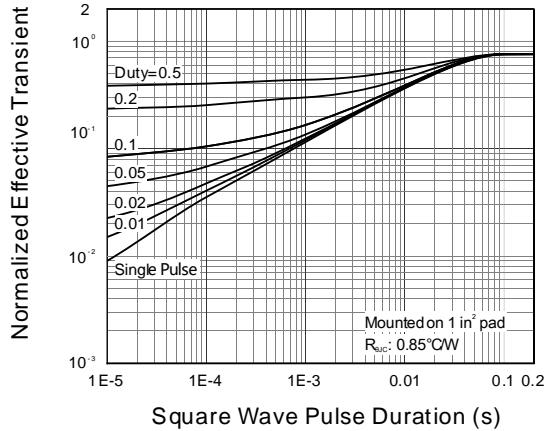


Figure 4. Transient Thermal Impedance

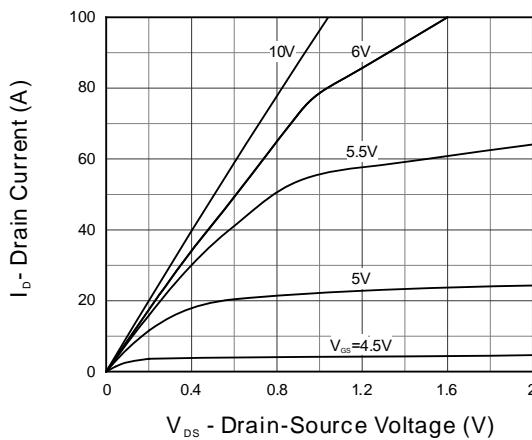


Figure 5. Output Characteristics

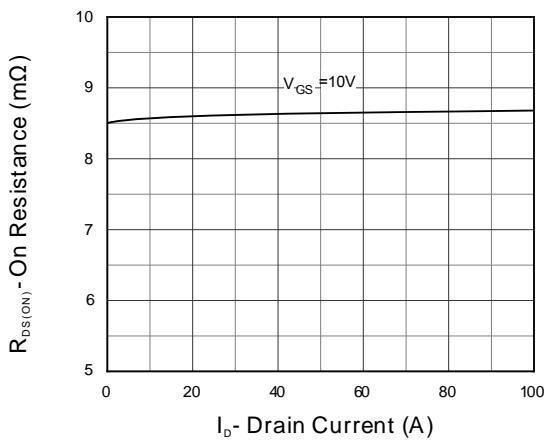
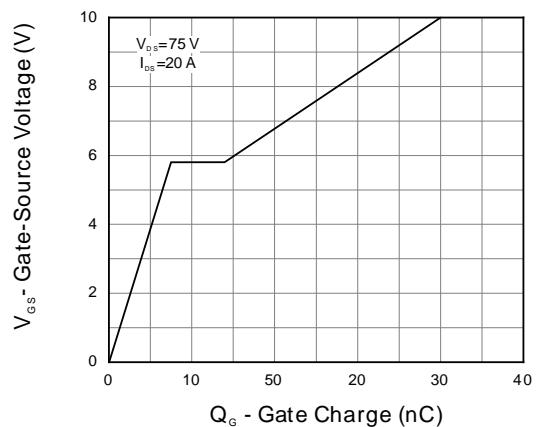
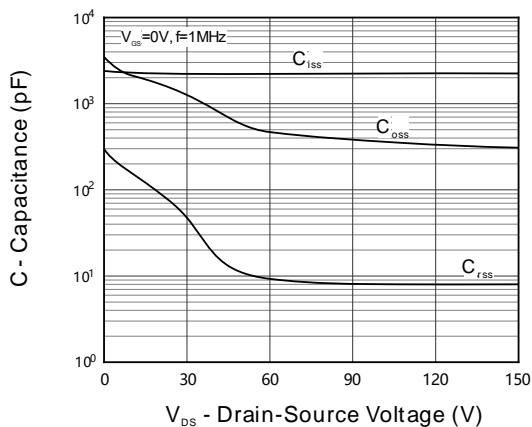
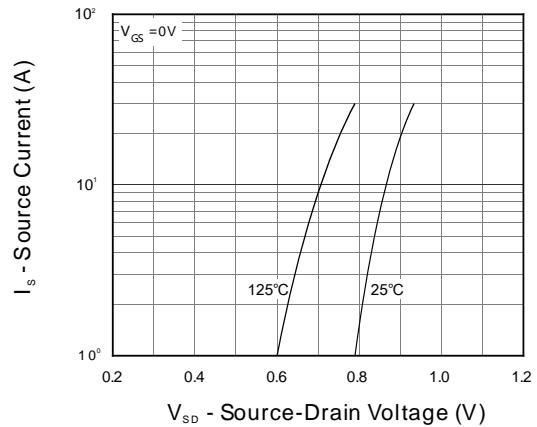
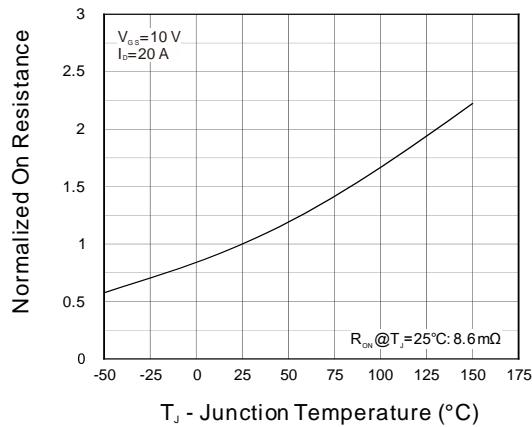
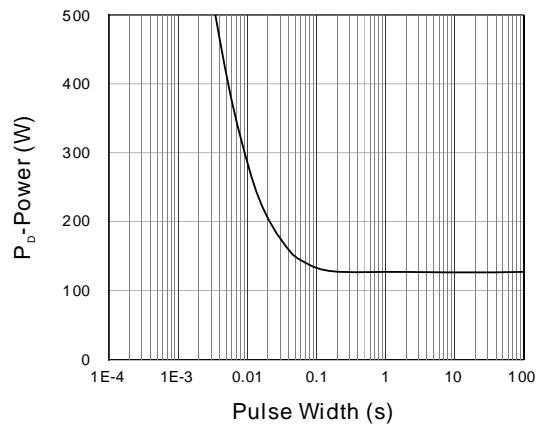
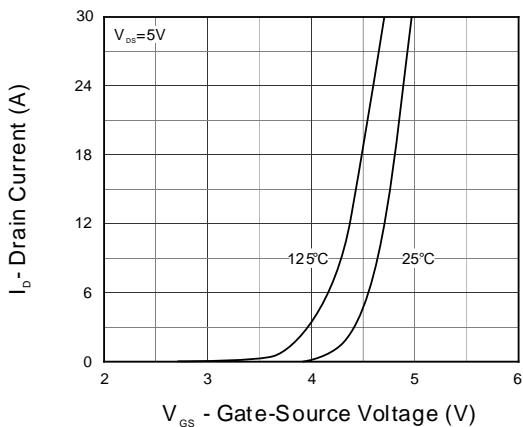


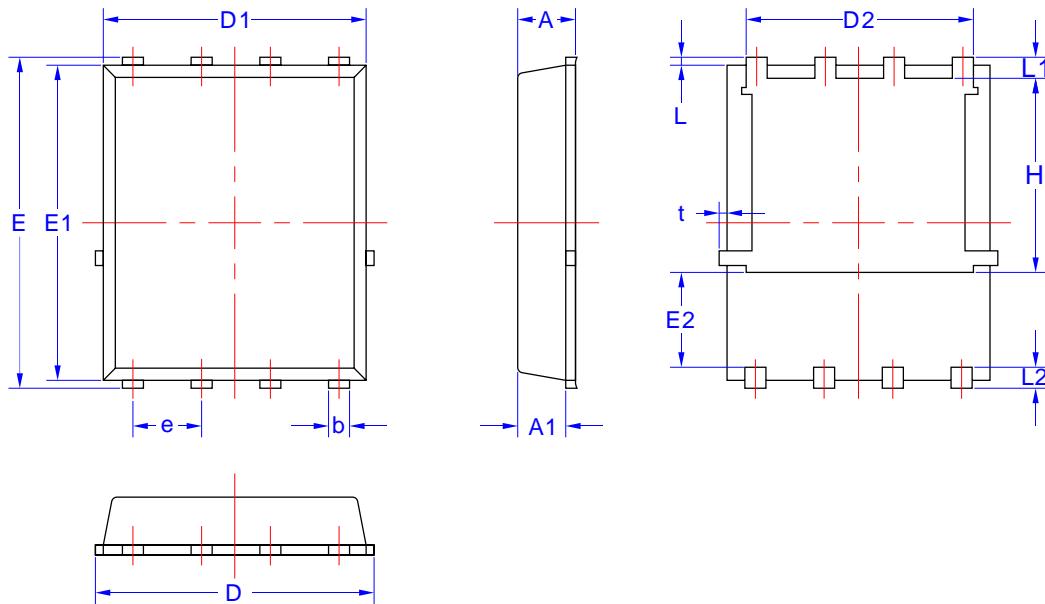
Figure 6. On Resistance

5. Typical Characteristics (cont.)



6. Package Dimensions

PDFN 5x6-8L Package



| Symbol | Dimensions in Millimeters | |
|--------|---------------------------|------|
| | MIN. | MAX |
| A | 1.03 | 1.17 |
| A1 | 0.824 | 0.97 |
| b | 0.34 | 0.48 |
| D | 4.80 | 5.40 |
| D1 | 4.80 | 5.00 |
| D2 | 4.11 | 4.31 |
| E | 5.95 | 6.15 |
| E1 | 5.65 | 5.85 |
| E2 | 1.40 | - |
| e | 1.27 BSC | |
| L | 0.05 | 0.25 |
| L1 | 0.38 | 0.50 |
| L2 | 0.38 | 0.71 |
| H | 3.30 | 3.50 |
| t | - | 0.18 |