

## P-Channel Enhancement Mode MOSFET

### 1. Product Information

#### 1.1 Features

- Surface-mounted package
- Low on-resistance
- Low gate charge

#### 1.2 Applications

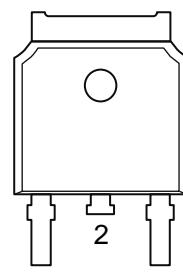
- PWM applications
- Load switch

#### 1.3 Quick reference

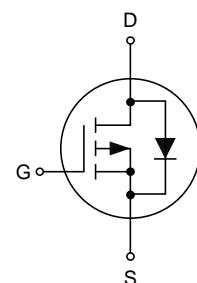
- $BV \geq -30 V$
- $R_{DS(ON)} \leq 42 m\Omega @ V_{GS} = -10 V$
- $P_{tot} \leq 25 W$
- $R_{DS(ON)} \leq 70 m\Omega @ V_{GS} = -4.5 V$
- $I_D \leq -20 A$

### 2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate (G)		
2	Drain (D)		
3	Source (S)		



Top View  
TO-252



**KJ20P03K**

### 3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	Drain-Source Voltage	T <sub>A</sub> =25°C	-30	-	V
V <sub>GS</sub>	Gate-Source Voltage	T <sub>A</sub> =25°C	-	±20	V
I <sub>D</sub> *	Drain Current	T <sub>A</sub> =25°C, V <sub>GS</sub> =-10 V	-	-20	A
		T <sub>A</sub> =70°C, V <sub>GS</sub> =-10 V	-	-12.7	A
I <sub>DM</sub> *	Pulsed Source Current	T <sub>A</sub> =25°C, V <sub>GS</sub> =-10 V	-	-60	A
P <sub>tot</sub>	Total Power Dissipation	T <sub>A</sub> =25°C	-	25	W
T <sub>J</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature Range		-55	150	°C
R <sub>θJA</sub> *	Thermal Resistance-Junction to Ambient		-	62.5	°C/W
R <sub>θJC</sub> *	Thermal Resistance-Junction to Case		-	5	

Notes:

\* Surface mounted on 1 in<sup>2</sup> pad area, t ≤ 10 sec.

\*\* Pulse width ≤ 300 μs, duty cycle ≤ 2%.

\*\*\* Limited by bonding wire.

### 4. Marking Information

Product Name	Marking
KJ20P03K	KJ20P03K XXXXXX

### 5. Ordering Code

Product Name	Package	Reel size	Tape width	Quantity (pcs)
KJ20P03K	TO-252	13"	16 mm	2500

Note: KUAIJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

**KJ20P03K**

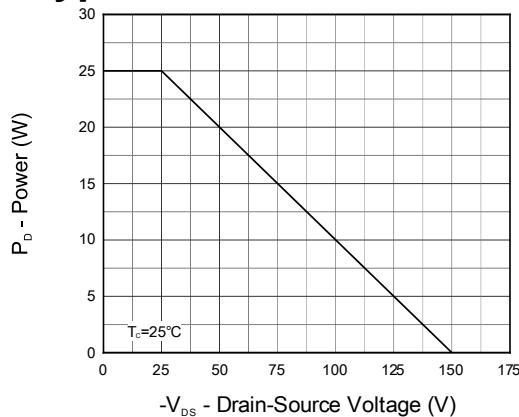
## 6. Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0 \text{ V}, I_{\text{DS}}=-250 \mu\text{A}$	-30	-	-	V
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{DS}}=-250 \mu\text{A}$	-1	-1.5	-2.5	V
$I_{\text{DSS}}$	Drain Leakage Current	$V_{\text{DS}}=-24 \text{ V}, V_{\text{GS}}=0 \text{ V}$	-	-	-1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm 20 \text{ V}, V_{\text{DS}}=0 \text{ V}$	-	-	$\pm 100$	nA
$R_{\text{DS(ON)}}^{\text{a}}$	On-State Resistance	$V_{\text{GS}}=-10 \text{ V}, I_{\text{D}}=-10 \text{ A}$	-	36	42	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5 \text{ V}, I_{\text{D}}=-4.5 \text{ A}$	-	50	70	
$g_{\text{fs}}^{\text{a}}$	Forward transconductance	$V_{\text{GS}}=-5 \text{ V}, I_{\text{D}}=-10 \text{ A}$	-	12	-	S
<b>Dynamic Characteristics <sup>b</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-15 \text{ V}, V_{\text{GS}}=0 \text{ V}, f=1 \text{ MHz}$	-	590	-	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		-	100	-	
$C_{\text{rss}}$	Reverse Transfer Capacitance		-	85	-	
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DS}}=-15 \text{ V}, V_{\text{GEN}}=-10 \text{ V}, R_{\text{GEN}}=3.3 \Omega, I_{\text{D}}=-10 \text{ A}$	-	3	-	$\text{ns}$
$t_{\text{r}}$	Turn-on Rise Time		-	9	-	
$t_{\text{d(off)}}$	Turn-off Delay Time		-	34	-	
$t_{\text{f}}$	Turn-off Fall Time		-	6.5	-	
<b>Gate Charge Characteristics <sup>b</sup></b>						
$Q_{\text{g}}$	Total Gate Charge	$V_{\text{DS}}=-15 \text{ V}, V_{\text{GS}}=-10 \text{ V}, I_{\text{D}}=-10 \text{ A}$	-	9.5	-	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		-	3.3	-	
$Q_{\text{gd}}$	Gate-Drain Charge		-	2	-	
<b>Diode Characteristics</b>						
$V_{\text{SD}}^{\text{a}}$	Diode Forward Voltage	$I_{\text{SD}}=-1 \text{ A}, V_{\text{GS}}=0 \text{ V}$	-	-	-1.2	V

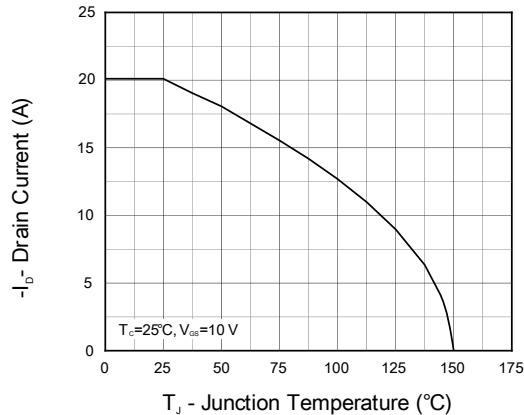
Notes:

- Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.

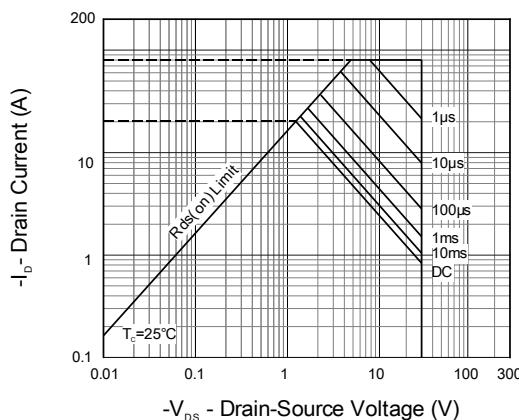
## 7. Typical Characteristics



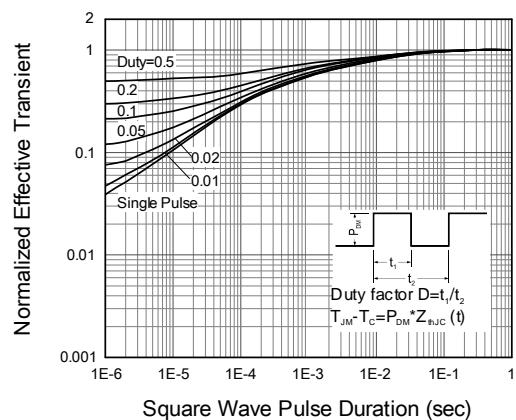
**Figure 1. Output Characteristics**



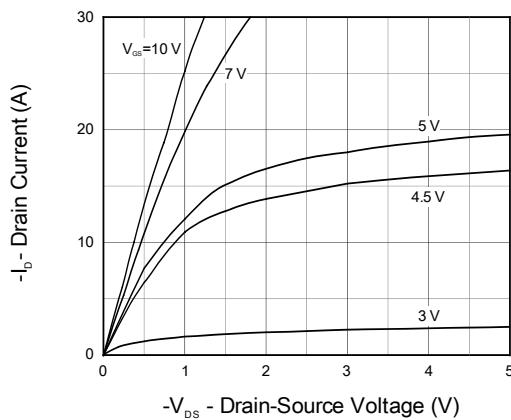
**Figure 2. Current Capability**



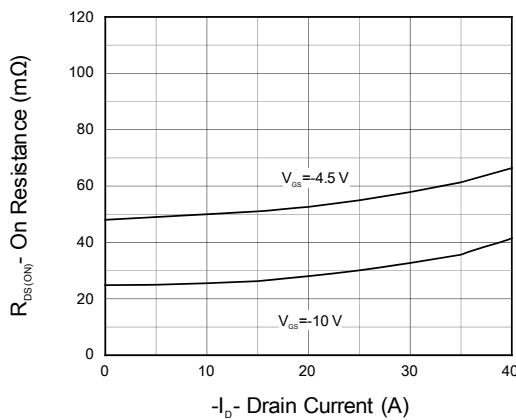
**Figure 3. Safe Operation Area**



**Figure 4. Transient Thermal Impedance**

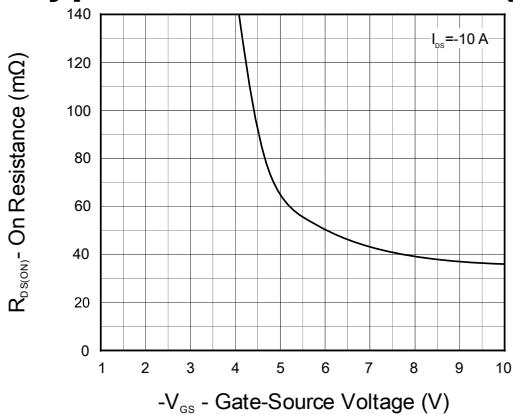


**Figure 5. Output Characteristics**

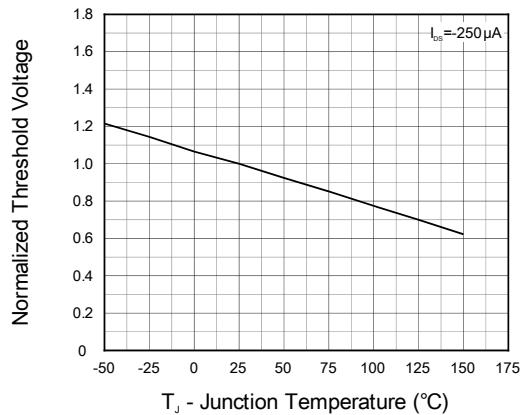


**Figure 6. On Resistance**

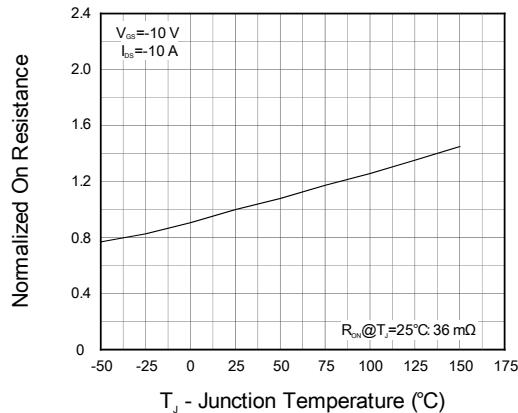
## 7. Typical Characteristics (cont.)



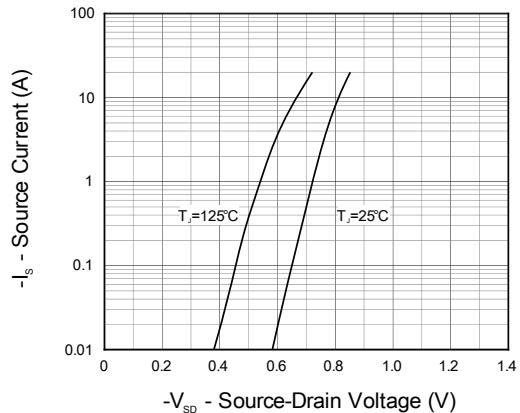
**Figure 7. Transfer Characteristics**



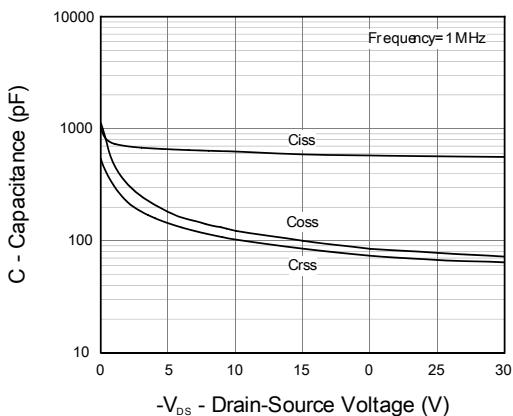
**Figure 8. Normalized Threshold Voltage**



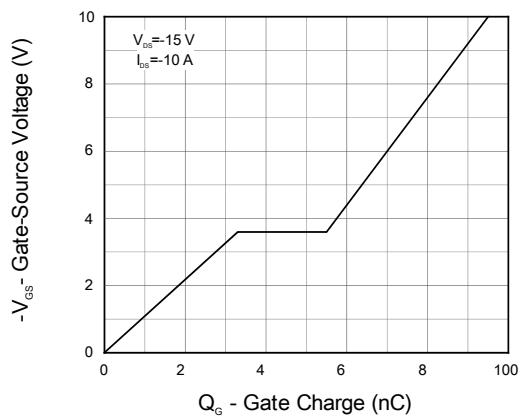
**Figure 9. Normalized On Resistance**



**Figure 10. Diode Forward Current**



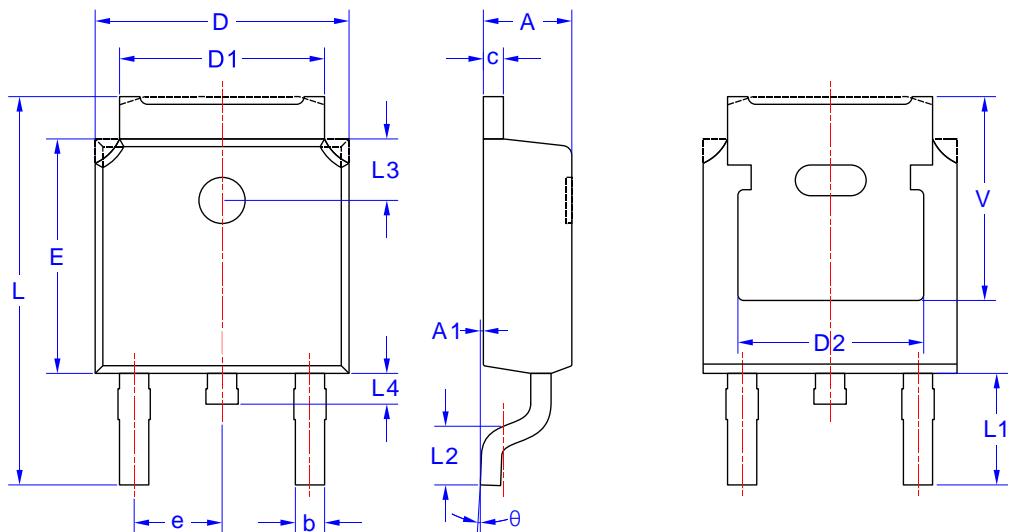
**Figure 11. Capacitance**



**Figure 12. Gate Charge**

## 8. Package Dimensions

TO-252 Package



Note: There are two possible shapes for the dashed area.

Symbol	Dimensions in Millimeters	
	MIN	MAX
A	2.200	2.400
A1	0	0.127
b	0.660	0.860
c	0.460	0.580
D	6.500	6.700
D1	5.100	5.460
D2	4.830 REF.	
E	6.000	6.200
e	2.186	1.386
L	9.800	10.400
L1	2.900 REF.	
L2	1.400	1.700
L3	1.600 REF.	
L4	0.600	1.000
V	5.350 REF.	
θ	0°	8°