

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Low thermal impedance
- ☒ Low $R_{DS(ON)}$
- ☒ 100% avalanche tested

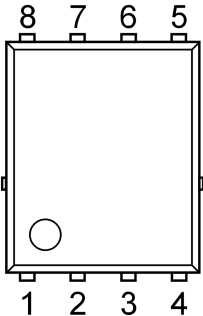
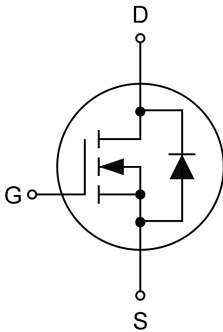
1.2 Applications

- ☒ BMS appliances
- ☒ DC/DC conversion
- ☒ Power switch
- ☒ Light electric vehicles

1.3 Quick reference

- ☒ $BV \geq 40\text{ V}$
- ☒ $P_D \leq 167\text{ W}$
- ☒ $I_D \leq 264\text{ A}$
- ☒ $R_{DS(ON)} \leq 1.0\text{ m}\Omega @V_{GS} = 10\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source	 Top View PDFN5x6-8L	
4	Gate		
5,6,7,8	Drain		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C=25^{\circ}\text{C}$	40	-	V
V_{GS}	Gate-Source Voltage	$T_C=25^{\circ}\text{C}$	-	± 20	V
I_D	Drain Current (DC)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	264	A
		$T_C=100^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	186	A
I_{DM}^*	Drain Current (Pulsed)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	1056	A
P_D	Drain Power Dissipation	$T_C=25^{\circ}\text{C}$	-	167	W
I_S	Continuous-Source Current	$T_C=25^{\circ}\text{C}$	-	264	A
E_{AS}	Single Pulsed Avalanche Energy	$V_{DD}=35\text{ V}, L=0.3\text{ mH}$	-	833	mJ
T_J, T_{stg}	Operating Junction and Storage Temperature		-55	175	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient		-	46	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case		-	0.9	$^{\circ}\text{C}/\text{W}$

Notes:

- * Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- ** Surface mounted on minimum footprint pad area.
- *** Limited by bonding wire.

4. Marking Information

Product Name	Marking
KJ009N04GH	KJ009N04GH XXXXXX-X

5. Ordering Code

Product Name	Package	Reel size	Tape width	Quantity (pcs)
KJ009N04GH	PDFN 5x6-8L	13"	12 mm	5000

Note: KJ009N04GH defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

6. Electrical Characteristics (T_C=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0 V, I _{DS} =250 μA	40	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250 μA	2.0	-	3.5	V
I _{DSS}	Drain Leakage Current	V _{DS} =40 V, V _{GS} =0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{DS} =0 V, V _{GS} =±20 V	-	-	±100	nA
R _{DS(ON)}	On-State Resistance	V _{GS} =10 V, I _{DS} =75 A	-	0.9	1.0	mΩ
R _g	Gate resistance	f=1 MHz, open drain	-	2.8	-	Ω
Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0 V, I _{SD} =1 A	-	-	1.1	V
t _{rr}	Reverse Recovery Time	V _{DS} =20 V, I _{SD} =75 A, dI _{SD} /dt=100 A/μs	-	87	-	ns
Q _{rr}	Reverse Recovery Charge		-	67	-	nC
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} =0 V, V _{DS} =25 V, f=1 MHz	-	7250	-	pF
C _{oss}	Output Capacitance		-	3722	-	
C _{rss}	Reverse Transfer Capacitance		-	636	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =20 V, V _{GEN} =10 V, R _G =6 Ω, I _{DS} =75 A	-	33	-	ns
t _r	Turn-on Rise Time		-	47	-	
t _{d(off)}	Turn-off Delay Time		-	81	-	
t _f	Turn-off Fall Time		-	35	-	
Gate Charge Characteristics						
Q _g	Total Gate Charge	V _{DS} =20 V, V _{GS} =10 V, I _{DS} =75 A	-	125	-	nC
Q _{gs}	Gate-Source Charge		-	33	-	
Q _{gd}	Gate-Drain Charge		-	35	-	

7. Typical Characteristics

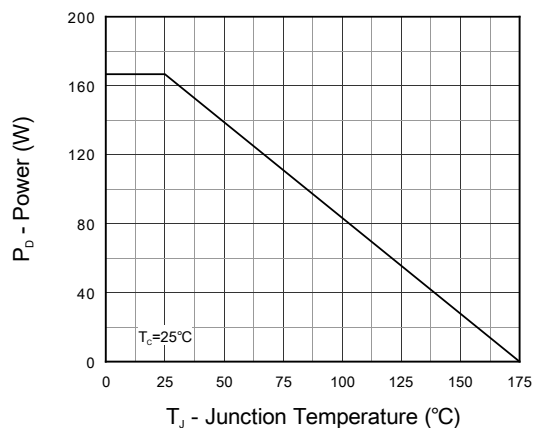


Figure 1. Power Capability

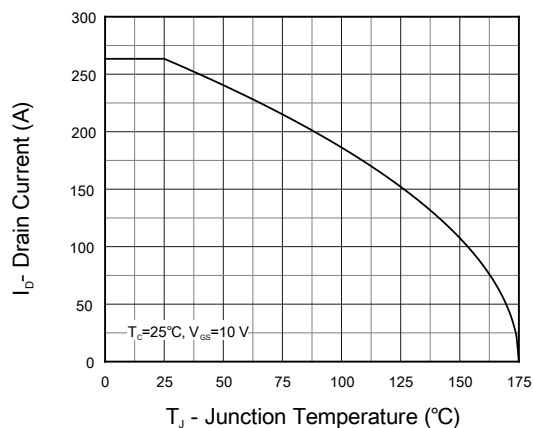


Figure 2. Current Capability

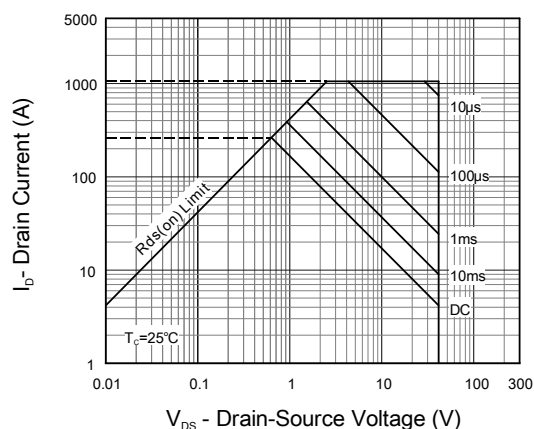


Figure 3. Safe Operation Area

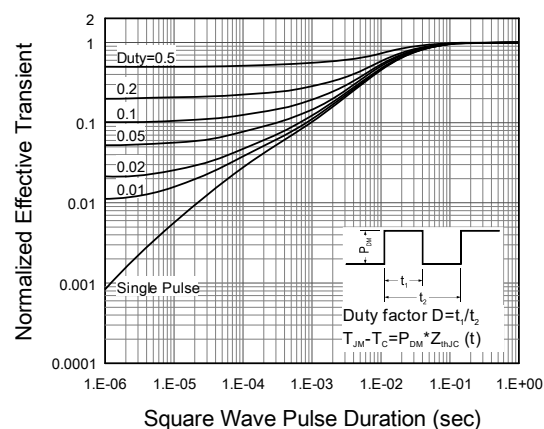


Figure 4. Transient Thermal Impedance

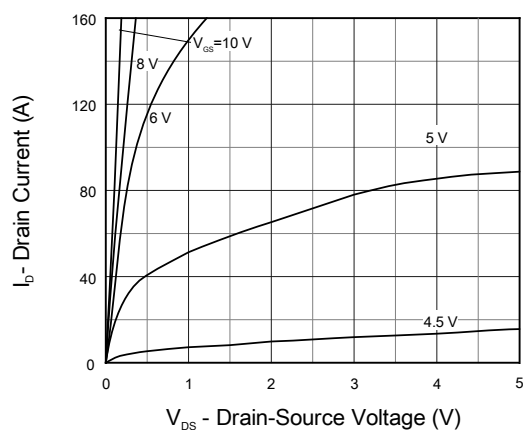


Figure 5. Output Characteristics

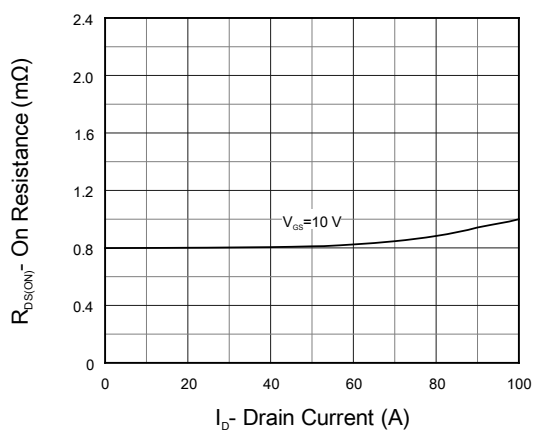


Figure 6. On Resistance

7. Typical Characteristics (cont.)

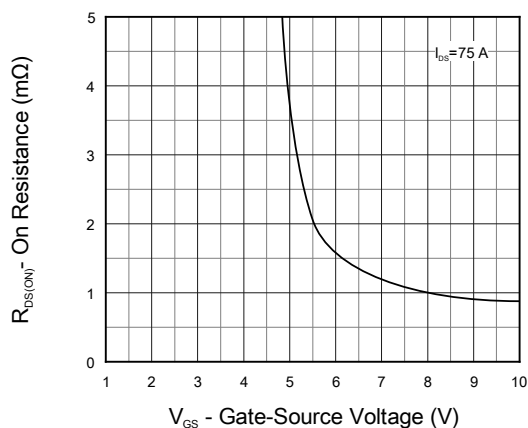


Figure 7. Transfer Characteristics

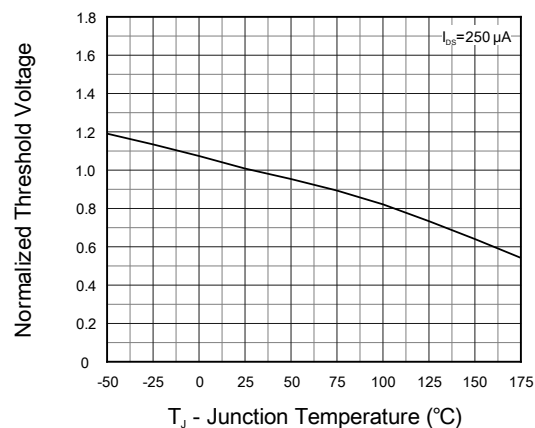


Figure 8. Normalized Threshold Voltage

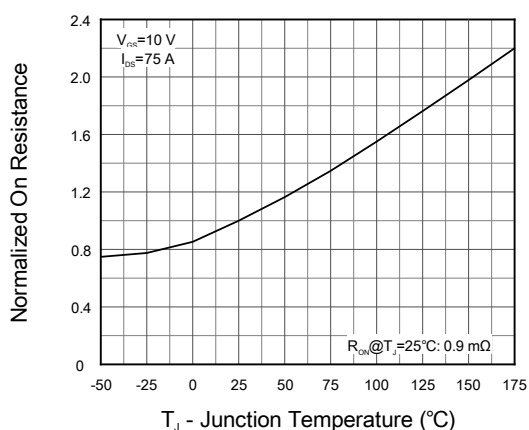


Figure 9. Normalized On Resistance

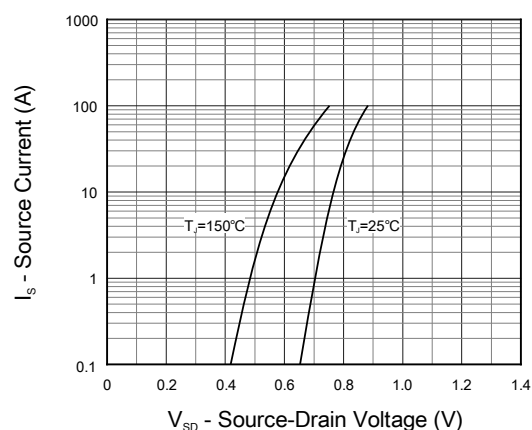


Figure 10. Diode Forward Current

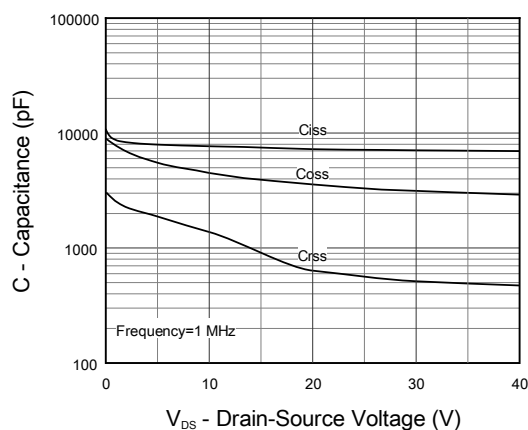


Figure 11. Capacitance

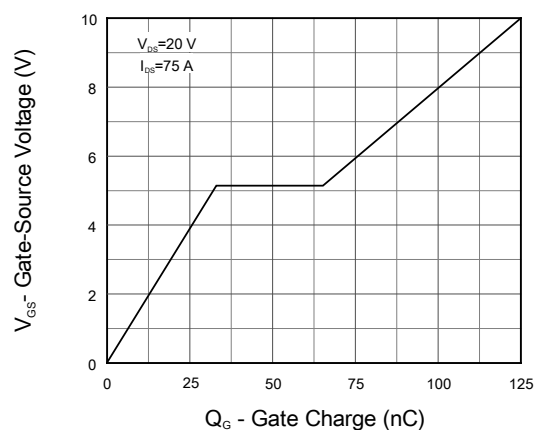
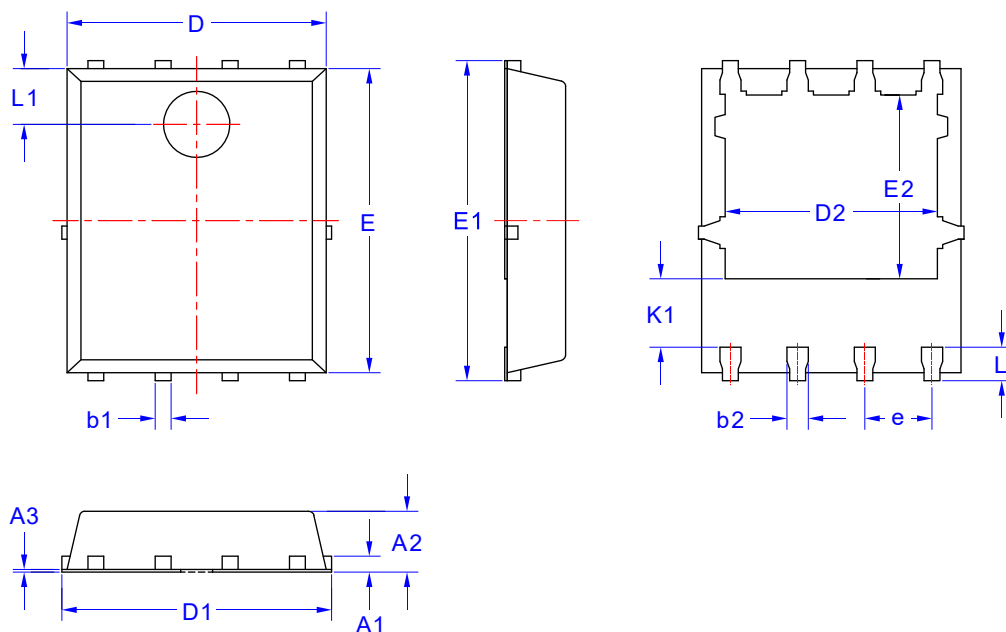


Figure 12. Gate Charge

8. Package Dimensions

PDFN5x6-8L Package



Symbol	Dimensions in Millimeters		
	MIN	NOM	MAX
A1		0.254 BSC	
A2	1.000	1.100	1.200
A3	0.005		0.020
b1	0.250	0.300	0.350
b2	0.350	0.400	0.450
D	4.800	4.900	5.000
D1	5.000	5.100	5.200
D2	3.910	4.010	4.110
E	5.650	5.750	5.850
E1	5.950	6.050	6.150
E2	3.375	3.475	3.575
e		1.270 TYPE	
K		0.600 REF	
K1		1.235 REF	
L	0.530	0.630	0.730
L1		1.000 REF	