快捷芯
KUAIJIEXIN

KJ0903QD

Dual N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☑ Surface-mounted package
- ☑ Low thermal resistance

1.2 Applications

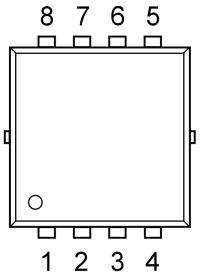
- ☑ Motor drivers
- ☑ DC/DC converter

1.3 Quick reference

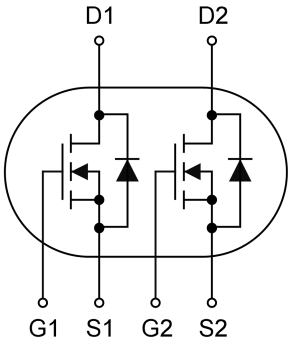
- ☑ BV ≥ 30 V
- ☑ P_{tot} ≤ 20.8 W
- ☑ I_D ≤ 34 A
- ☑ $R_{DS(ON)}$ ≤ 12 mΩ @V_{GS} = 10 V
- ☑ $R_{DS(ON)}$ ≤ 17 mΩ @V_{GS} = 4.5 V

2. Pin Description

Pin	Description
1	Source(S2)
2	Gate(G2)
3	Source(S1)
4	Gate(G1)
5,6	Drain(D1)
7,8	Drain(D2)



Top View
PDFN3.3x3.3-8L



3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	T_C=25℃	30	-	V
V_{GS}	Gate-Source Voltage	T_C=25℃	-	±20	V
I_D	Drain Current	T_C=25℃, V_{GS}=10 V	-	34	A
I_{DM} *	Pulsed Source Current	T_C=25℃, V_{GS}=10 V	-	80	A
P_{tot}	Total Power Dissipation	T_C=25℃	-	20.8	W
I_S	Diode Forward Current	T_C=25℃	-	34	A
T_J, T_{stg}	Operating Junction and Storage Temperature Range		-55	150	℃
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient		-	62.5	℃/W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		-	6	

Notes:

* Pulse width ≤ 10 μs, duty cycle ≤ 1%.

** Surface mounted on 1 in^2 pad area, t ≤ 10 sec.

*** Limited by bonding wire.

4. Marking Information

Product Name	Marking
KJ0903QD	0903D XXXXXX

5. Ordering Code

Product Name	Package	Reel size	Tape width	Quantity (pcs)
KJ0903QD	PDFN 3.3x3.3-8L	13"	12 mm	5000

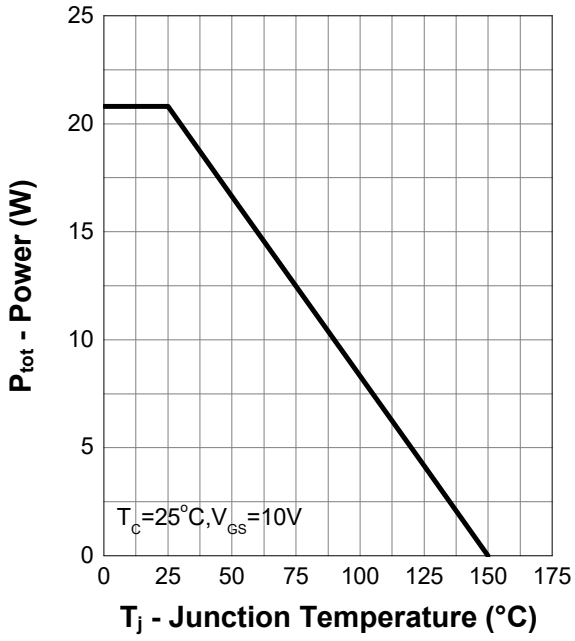
Note: KUAIJIEXIN defines “Green” as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

6. Electrical Characteristics (T_A=25°C unless otherwise noted)

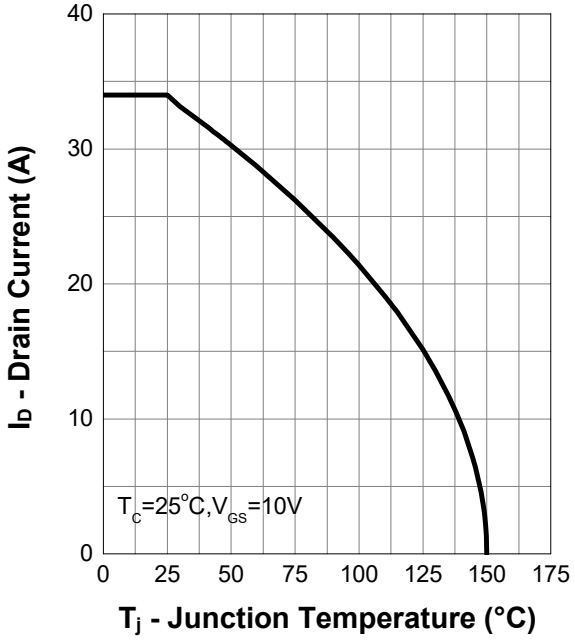
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0 V, I_D=250 μA	30	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250 μA	0.8	-	1.6	V
I_{DSS}	Zero Gate Voltage Source Current	V_{DS}=24V, V_{GS}=0 V	-	-	1	μA
		T_J=85℃	-	-	30	μA
I_{GSS}	Gate Leakage Current	V_{GS}=±20 V, V_{DS}=0 V	-	-	±100	nA
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=10 V, I_D=20 A	-	10	12	mΩ
		V_{GS}=4.5 V, I_D=10 A	-	15	17	
Diode Characteristics						
V_{SD}	Diode Forward Voltage	I_{SD}=20 A, V_{GS}=0 V	-	-	1.3	V
t_{rr}	Reverse Recovery Time	I_{SD}=20 A, dI_{SD}/dt=100 A/μs	-	16	-	ns
Q_{rr}	Reverse Recovery Charge		-	7	-	nC
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS}=0 V, V_{DS}=15 V, Frequency=1 MHz	-	918	-	pF
C_{oss}	Output Capacitance		-	114	-	
C_{rss}	Reverse Transfer Capacitance		-	101	-	
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=15 V, V_{GEN}=10 V, R_G=4.5 Ω, R_L=0.75Ω, I_D=20 A	-	7	-	ns
t_r	Turn-on Rise Time		-	55	-	
$t_{d(off)}$	Turn-off Delay Time		-	19	-	
t_f	Turn-off Fall Time		-	23	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge	V_{GS}=10 V, V_{DS}=15 V, I_{DS}=20 A	-	20	-	nC
Q_{gs}	Gate-Source Charge		-	3.6	-	
Q_{gd}	Gate-Drain Charge		-	3.9	-	

7. Typical Characteristics

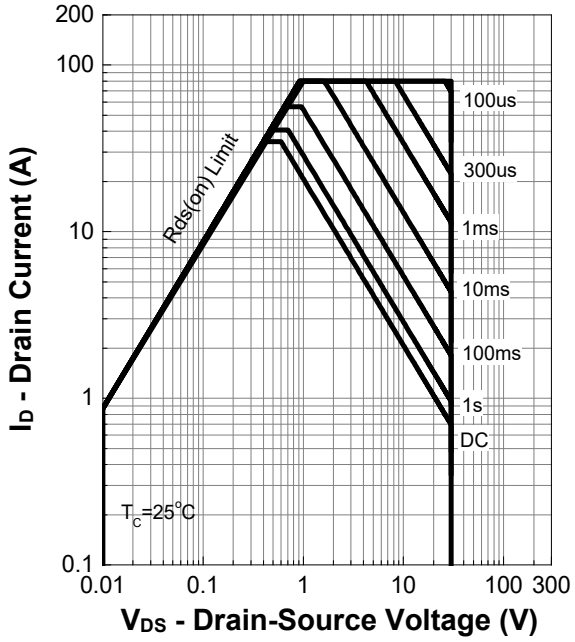
Power Capability



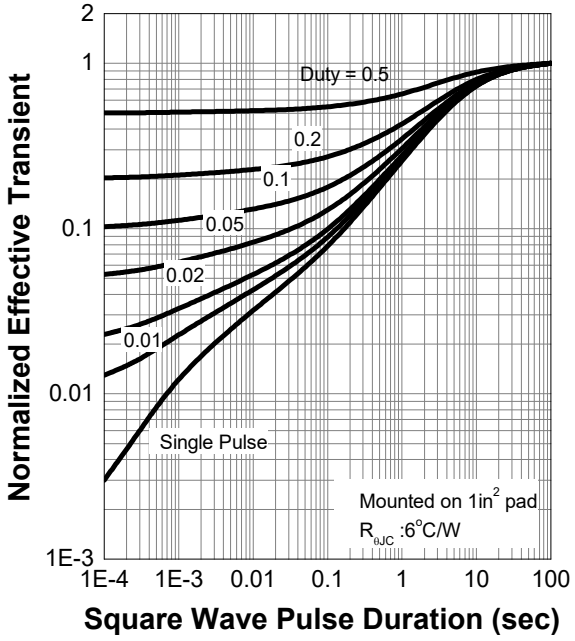
Current Capability



Safe Operation Area

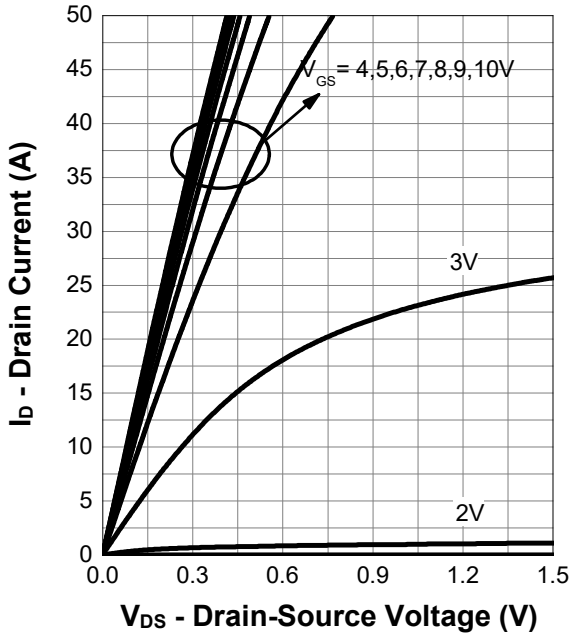


Transient Thermal Impedance

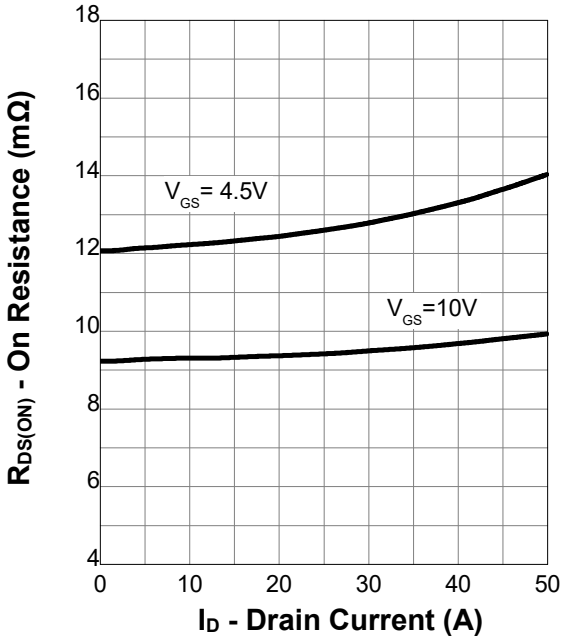


7. Typical Characteristics (cont.)

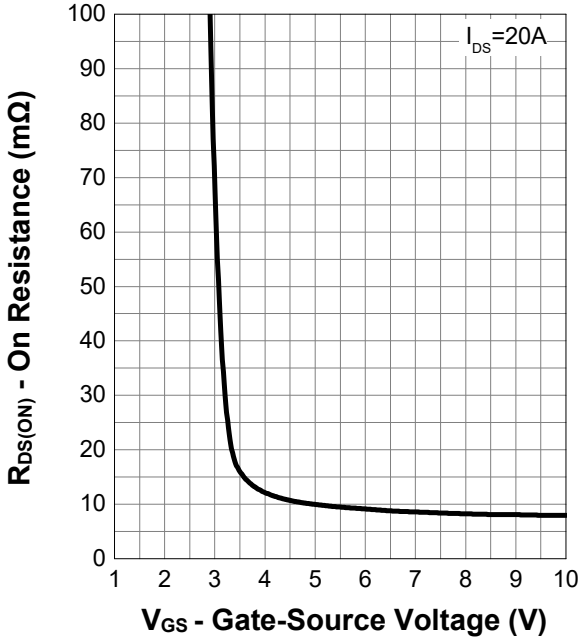
Output Characteristics



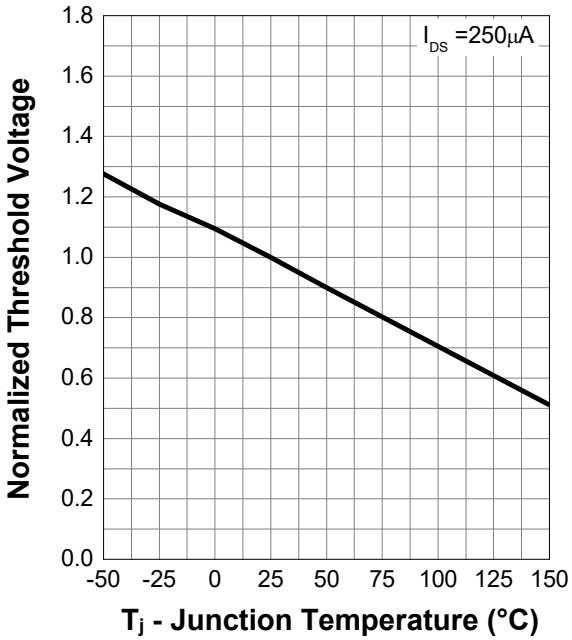
On Resistance



Transfer Characteristics

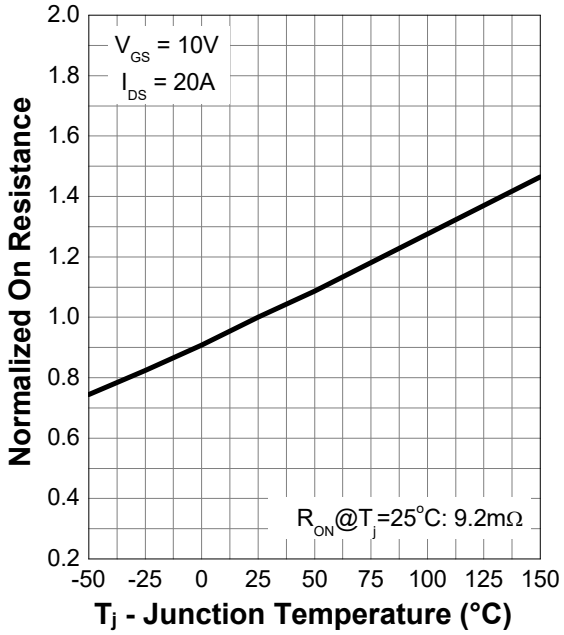


Normalized Threshold Voltage

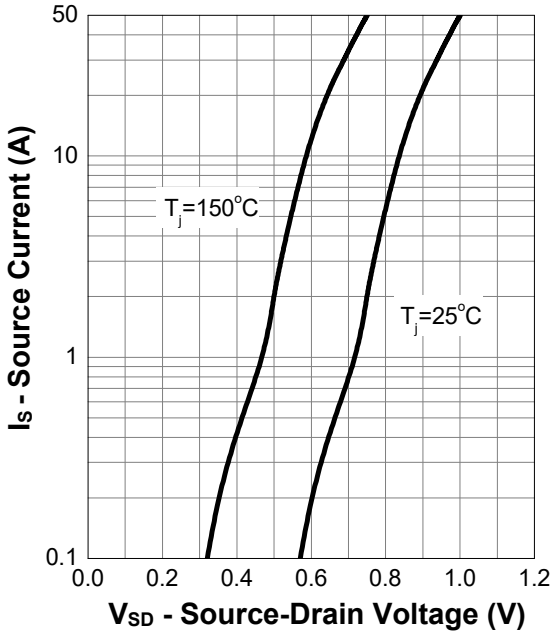


7. Typical Characteristics (cont.)

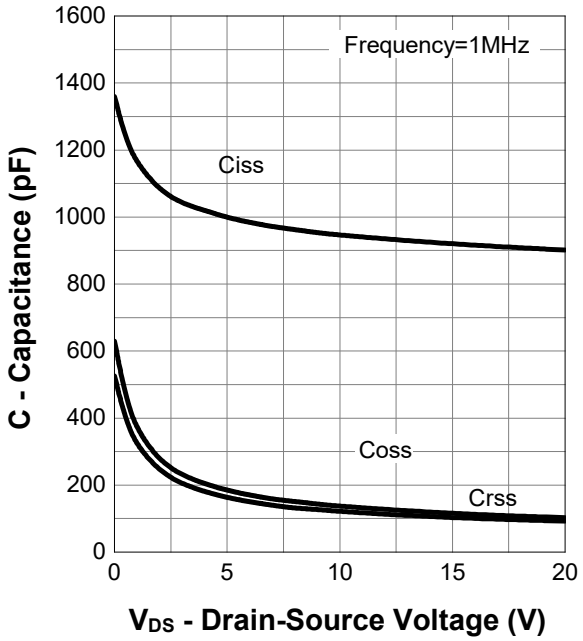
Normalized On Resistance



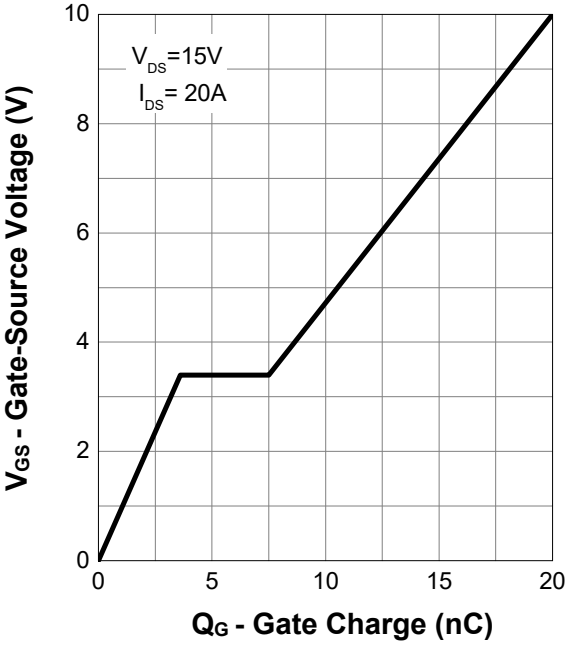
Diode Forward Current



Capacitance

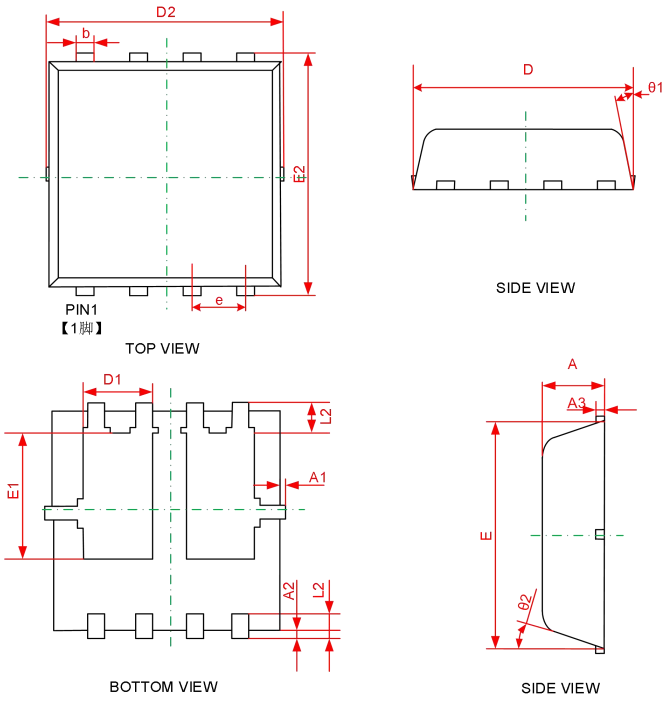


Gate Charge



8. Package Dimensions

PDFN 3.3×3.3-8L Package



Symbol	Dimensions in Millimeters	
	Min	Max
A	0.750	0.850
A1	0.000	0.050
A2	0.100	0.200
A3	0.152 REF.	
b	0.200	0.400
D	3.050	3.250
D1	0.935	1.135
D2	3.200	3.400
E	2.900	3.100
E1	1.635	1.835
E2	3.150	3.350
e	0.625	0.675
L1	0.350	0.450
L2	0.365	0.465
θ1	10°	12°
θ2	10°	12°