

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Advanced trench cell design
- ☒ Low thermal resistance

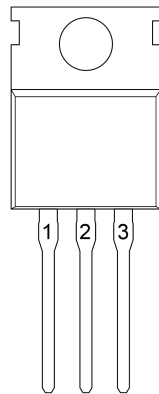
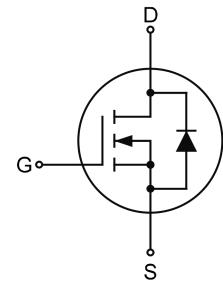
1.2 Applications

- ☒ Motor drivers
- ☒ Battery management

1.3 Quick reference

- ☒ $BV \geq 200\text{ V}$
- ☒ $P_D \leq 278\text{ W}$
- ☒ $I_D \leq 107\text{ A}$
- ☒ $R_{DS(ON)} \leq 10.5\text{ m}\Omega @V_{GS} = 10\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate (G)	 Top View TO-220	
2	Drain (D)		
3	Source (S)		

2. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C=25^{\circ}\text{C}$	200	-	V
V_{GS}	Gate-Source Voltage	$T_C=25^{\circ}\text{C}$	-	± 20	V
I_D	Drain Current (DC)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	107	A
		$T_C=100^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	83	A
I_{DM}^*	Drain Current (Pulsed)	$T_C=25^{\circ}\text{C}, V_{GS}=10\text{ V}$	-	428	A
P_D^*	Drain Power Dissipation	$T_C=25^{\circ}\text{C}$	-	278	W
I_S	Diode Forward Current	$T_C=25^{\circ}\text{C}$	-	107	A
E_{AS}	Single Pulsed Avalanche Energy	$V_{DD}=100\text{ V}, L=0.3\text{ mH}$	-	650	mJ
T_J, T_{stg}	Operating Junction and Storage Temperature Range		-55	150	$^{\circ}\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient		-	62.5	$^{\circ}\text{C/W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case		-	0.45	$^{\circ}\text{C/W}$

Notes:

- * Surface mounted on 1 in² pad area, $t \leq 10\text{ sec}$.
- ** Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- *** Limited by bonding wire.

4. Marking Information

Product Name	Marking
KJ10N20C	KJ10N20C XXXXXX-X

5. Ordering Code

Product Name	Package	Reel size	Tape width	Quantity (pcs)
KJ10N20C	TO-220	N/A	N/A	1000

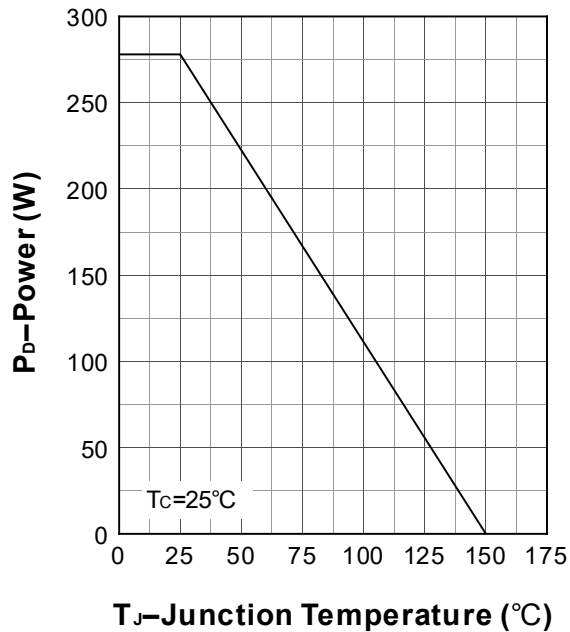
Note: KUAIJIEXIN defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC/JEDEC J-STD-020C).

6. Electrical Characteristics (T_A=25°C unless otherwise noted)

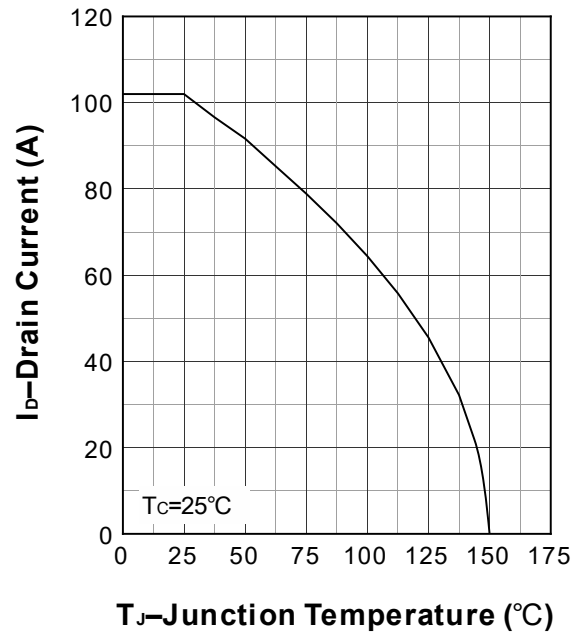
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0 V, I _{DS} =250 μA	200	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250 μA	2	-	4	V
I _{DSS}	Drain Leakage Current	V _{DS} =200 V, V _{GS} =0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{DS} =0 V, V _{GS} =±20 V	-	-	±100	nA
R _{DS(ON)}	On-State Resistance	V _{GS} =10 V, I _{DS} =50 A	-	8.7	10.5	mΩ
R _G	Gate Resistance	V _{GS} =V _{DS} =0 V, f=1 MHz	-	3.5	-	Ω
g _{FS}	Forward Transconductance	V _{DS} =5 V, I _D =50 A	-	90	-	S
Diode Characteristics						
V _{SD}	Diode Forward Voltage	I _{SD} =1 A, V _{GS} =0 V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} =50 A, V _{GS} =0 V dI _{SD} /dt=100 A/μs	-	128	-	ns
Q _{rr}	Reverse Recovery Charge		-	755	-	nC
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} =0 V, V _{DS} =100 V, f=1 MHz	-	5600	-	pF
C _{oss}	Output Capacitance		-	420	-	
C _{rss}	Reverse Transfer Capacitance		-	120	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =100 V, V _{GEN} =10 V, R _G =2.7 Ω, I _{DS} =50 A	-	16	-	ns
t _r	Turn-on Rise Time		-	88	-	
t _{d(off)}	Turn-off Delay Time		-	53	-	
t _f	Turn-off Fall Time		-	82	-	
Gate Charge Characteristics						
Q _g	Total Gate Charge	V _{DS} =100 V, V _{GS} =10 V, I _{DS} =50 A	-	70	-	nC
Q _{gs}	Gate-Source Charge		-	25	-	
Q _{gd}	Gate-Drain Charge		-	17	-	

7. Typical Characteristics

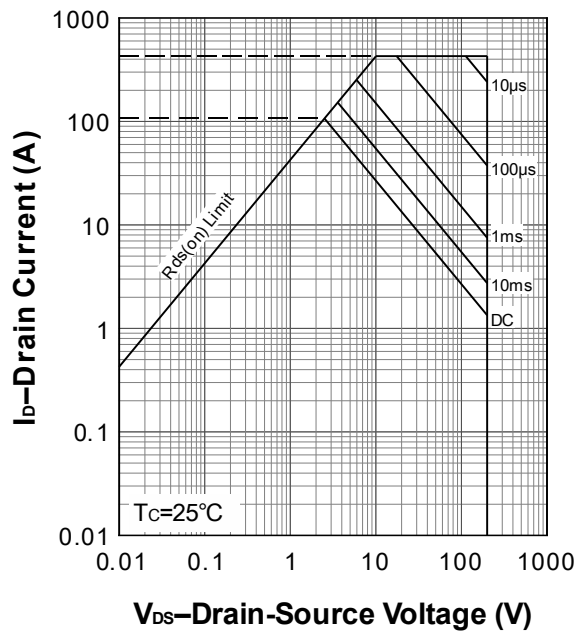
Power Capability



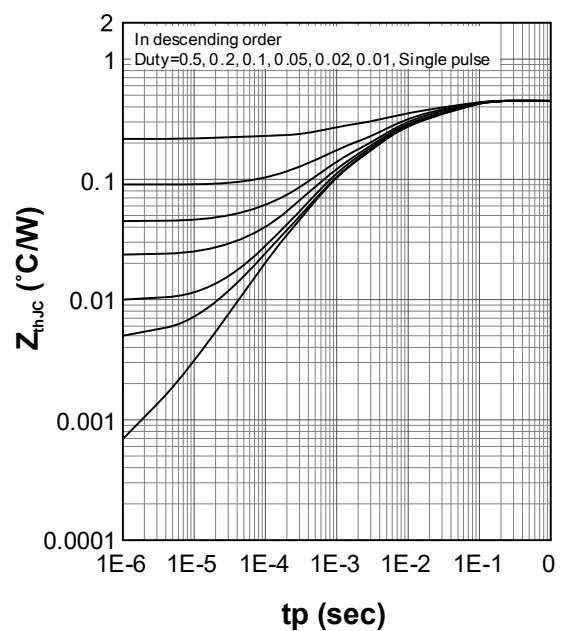
Current Capability



Safe Operating Area



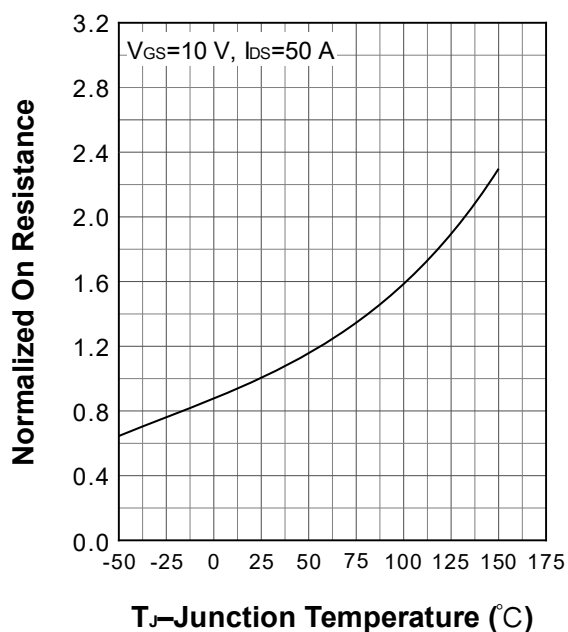
Transient Thermal Impedance



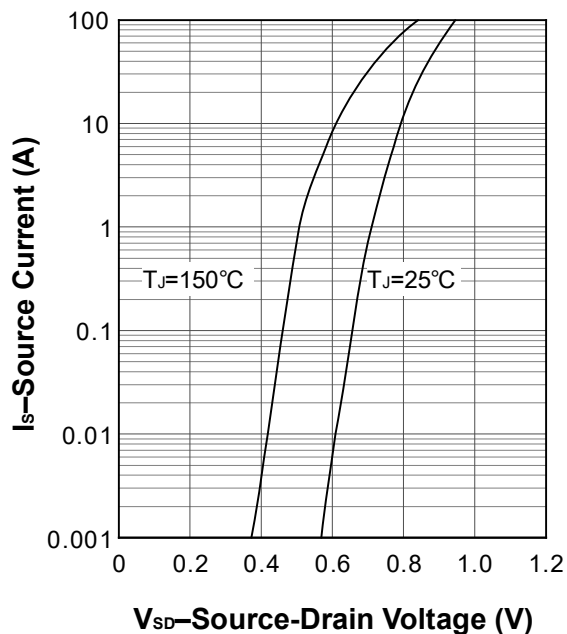
7. Typical Characteristics (cont.)

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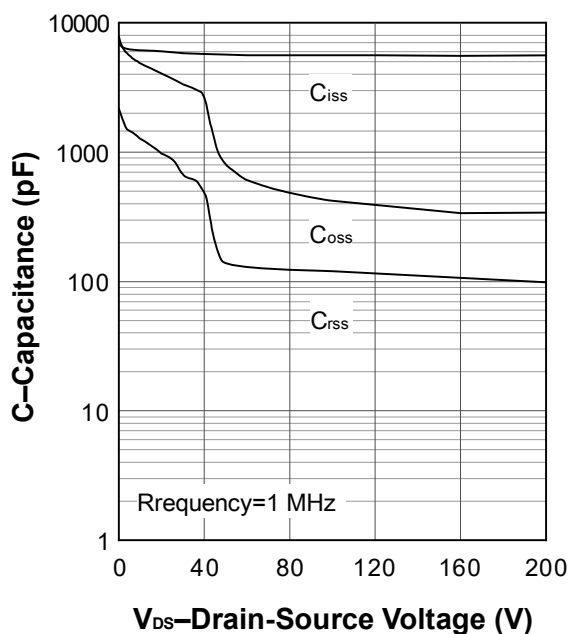
Normalized On Resistance



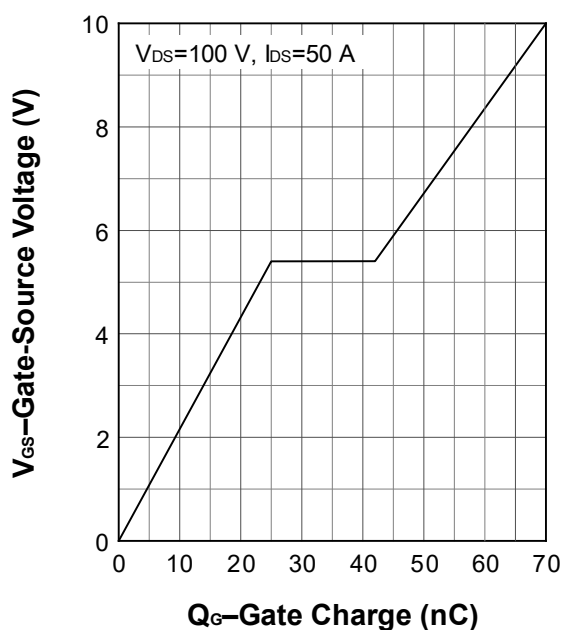
Diode Forward Current



Capacitance

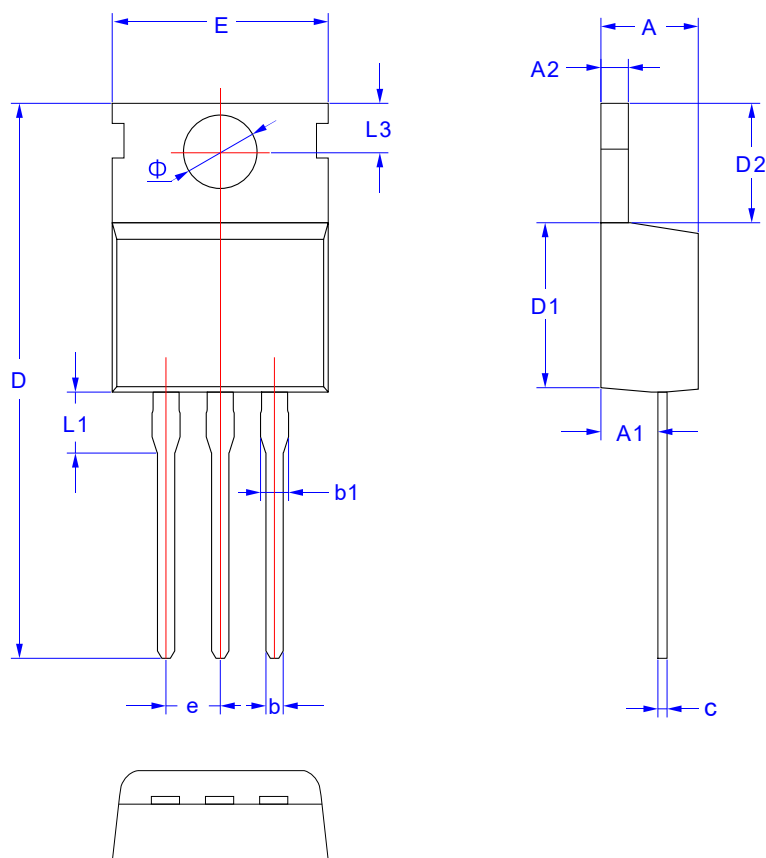


Gate Charge



8. Package Dimensions

TO-220 Package



Symbol	Dimensions in Millimeters	
	MIN	MAX
A	4.24	4.70
A1	2.20	3.00
b	0.70	0.95
b1	1.14	1.70
c	0.40	0.60
c1	1.15	1.40
D	28.0	29.8
D1	8.80	9.90
E	9.70	10.50
L1	3.80	
L2	6.25	6.90
L3	2.40	3.00
e	2.54 BSC	