

1. Product Information

Features

- Low FOM $R_{DS(ON)} \times Q_G$
- Fast switching capability
- Lead free product is acquired

Applications

- Power supplies
- Charger
- Solar/UPS-Micro inverter system

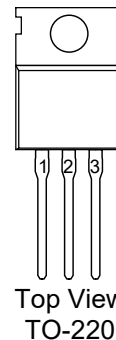
Quick reference

- $V_{DS} \geq 800\text{ V}$
- $I_D \leq 24\text{ A}$
- $R_{DS(ON)} = 180\text{ m}\Omega @ V_{GS} = 18\text{ V (Type)}$

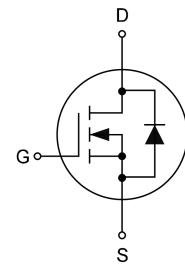
Pin Description

Pin	Description
1	Gate(G)
2	Drain(D)
3	Source(S)

Simplified Outline



Symbol



Package Marking and Ordering Information

Part number	Marking	Package	Packaging	Quantity	Reel size	Tape width
KJSC65R190C	KJSC65R190C	TO-220	☐ Tape & Reel	N/A	N/A	N/A
			☒ Tube	1000 PCS	N/A	N/A

2. Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Values	Unit
V_{DS}	Drain-Source Voltage	800	V
V_{GS}	Gate-Source Voltage	-10/+25	V
I_D	Continuous Drain Current (T _C =25°C)	24	A
	Continuous Drain Current (T _C =100°C)	17	A
I_{DM}	Pulsed Drain Current	48	A
P_D	Power Dissipation	176	W
T_J, T_{stg}	Operating Junction and Storage Temperature Range	-55 to 175	°C
$R_{\theta JA}$	Thermal Resistance, Junction-Ambient	42	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-Case	0.85	°C/W

3. Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Type	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0 V, I _D =100 μA	800	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =800 V, V _{GS} =0 V	-	1	50	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =18 V, V _{DS} =0 V	-	-	250	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250 μA	-	3.1	-	V
		T _J =175°C	-	2	-	V
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =15 V, I _D =5 A	-	240	300	mΩ
		V _{GS} =18 V, I _D =5 A	-	180	190	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =400 V, V _{GS} =0 V, f=1 MHz	-	380	-	pF
C _{oss}	Output Capacitance		-	42	-	pF
C _{rss}	Reverse Transfer Capacitance		-	9	-	pF
R _G	Internal Gate Resistance	f=1 MHz	-	3.5	-	Ω
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =400 V, I _D =5 A, R _G =2 Ω, V _{GS} =18 V	-	8	-	ns
t _r	Turn-on Rise Time		-	9	-	ns
t _{d(off)}	Turn-off Delay Time		-	15	-	ns
t _f	Turn-off Fall Time		-	14	-	ns
R _g	Gate resistance	f=1 MHz	-	6.5	-	Ω
Q _g	Total Gate Charge	V _{DS} =400 V, I _D =5 A, V _{GS} =18 V	-	30	-	nC
Q _{gs}	Gate-Source Charge		-	9	-	nC
Q _{gd}	Gate-Drain Charge		-	8	-	nC
Source-Drain Diode Characteristics						
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} =-4 V, I _F =5 A	-	3	-	V
Q _{rr}	Reverse Recovery Time	V _R =400 V, I _F =5 A, di/dt=100 A/μs	-	55	-	μC
T _{rr}	Reverse Recovery Charge		-	22	-	ns

4. Electrical Characteristics

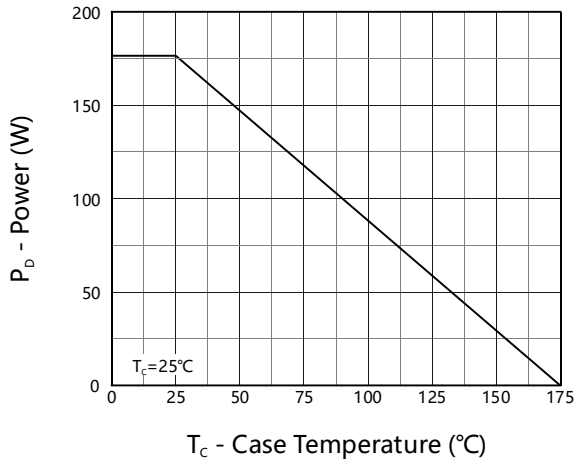


Fig 1. Power Capability

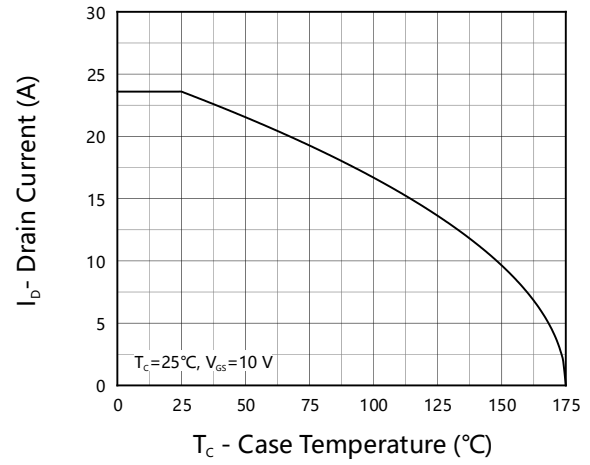


Fig 2. Current Capability

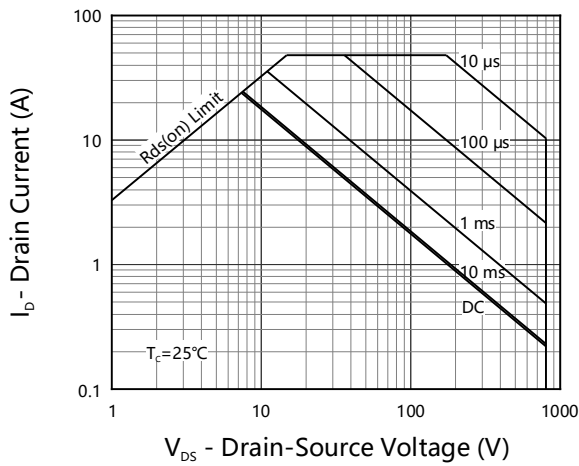


Fig 3. Safe Operation Area

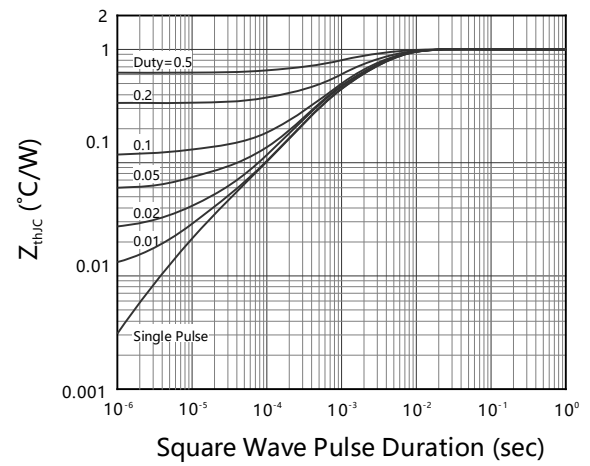


Fig 4. Transient Thermal Impedance

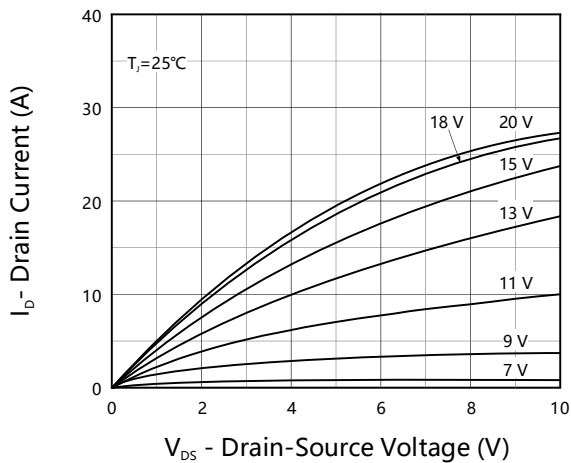


Fig 5. Output Characteristics, $T_J = 25^{\circ}\text{C}$

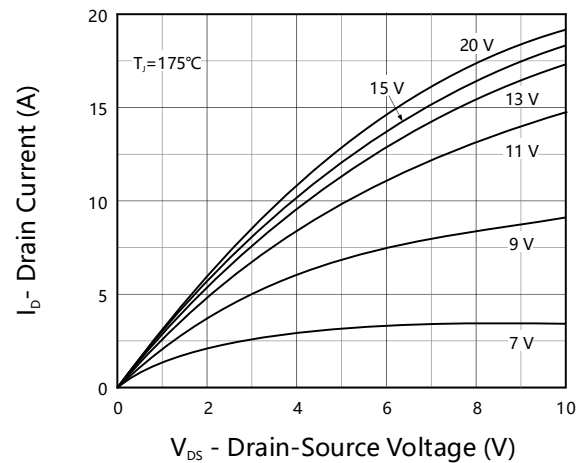


Fig 6. Output Characteristics, $T_J = 175^{\circ}\text{C}$

4. Electrical Characteristics (cont.)

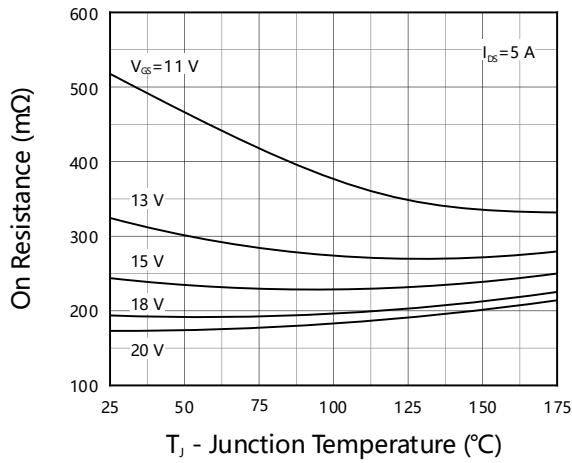


Fig 7. On Resistance vs. T_j

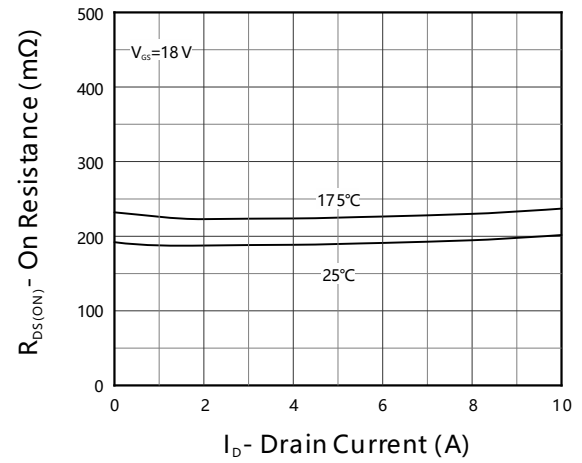


Fig 8. On Resistance vs. I_D

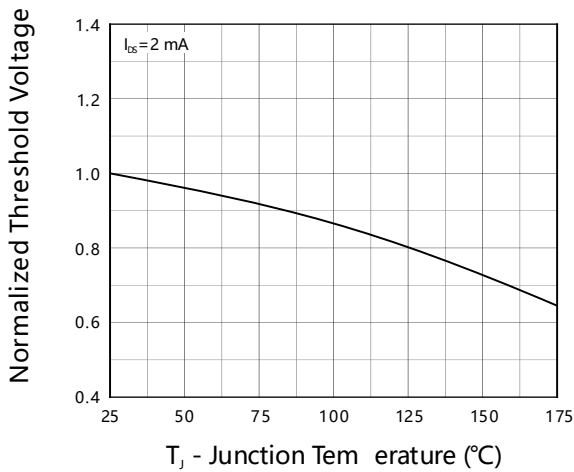


Fig 9. Normalized Threshold Voltage

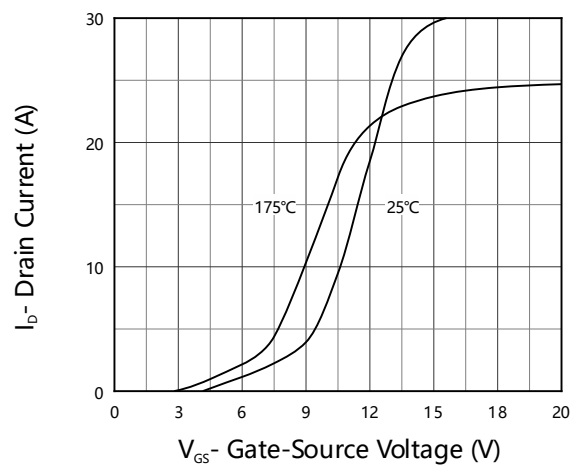


Fig 10. Diode Forward Current

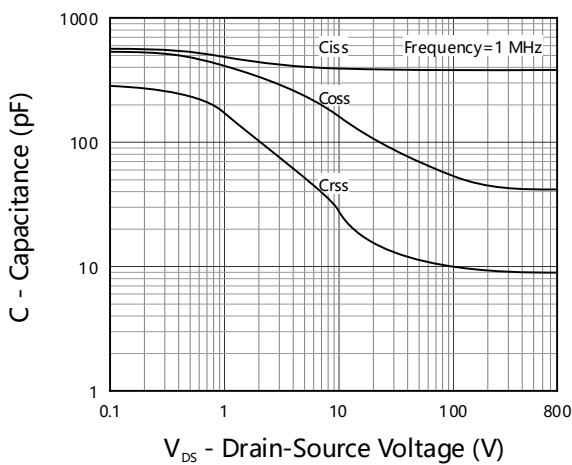


Fig 11. Capacitance

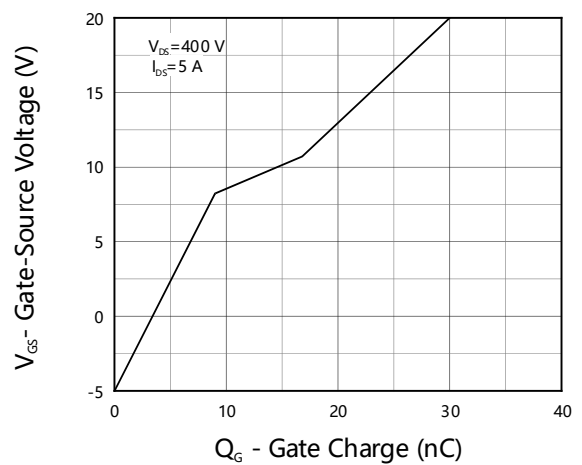
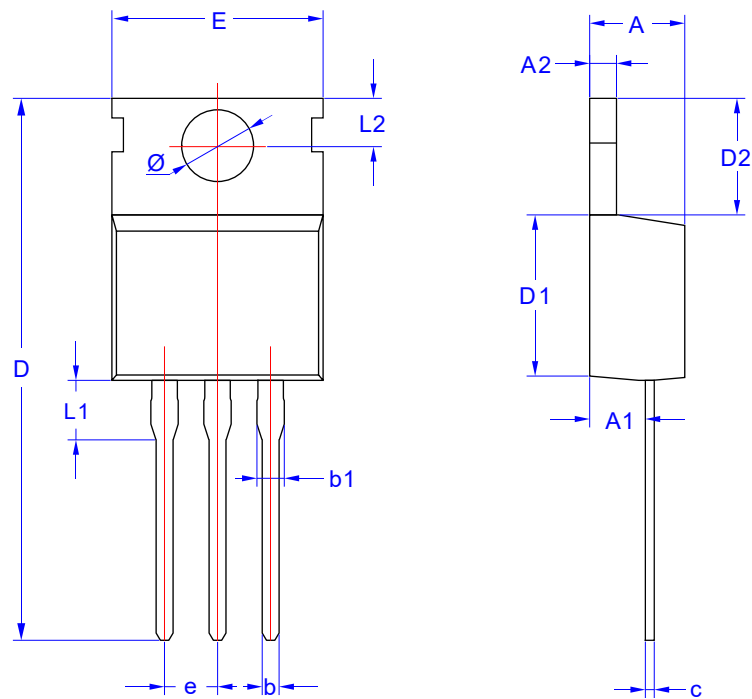


Fig 12. Gate Charge

5. Package Mechanical Data

TO-220 Package



Symbol	Dimensions in Millimeters	
	MIN	MAX
A	4.24	4.70
A1	2.20	3.00
A2	1.15	1.40
b	0.70	0.95
b1	1.14	1.70
c	0.40	0.60
D	28.0	29.8
D1	8.80	9.90
D2	6.25	6.90
E	9.70	10.50
L1	3.80	
L2	2.40	3.00
e	2.54 BSC	
Φ	3.60	